

NPN Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

Ordering Information

Type No.	Marking	Package Code
SRC1204EF	<u>R4</u> ① ②	SOT-523F

①Device Code ②Year&Week Code

Absolute Maximum Ratings

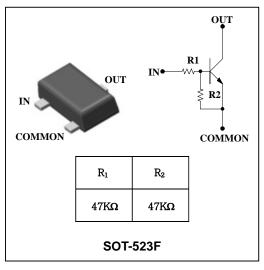
(Ta=25°C) Characteristic Unit Symbol Rating V Output voltage Vo 50 V Input voltage Vı 40,-10 Output current I_{O} 100 mΑ Power dissipation 150 P_D mW Junction temperature ТJ 150 °C $\mathsf{T}_{\mathsf{stg}}$ -55 ~ 150 °C Storage temperature range

Flactrical Characteristics

Electrical Characteristics					(Ta:	=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10mA$	80	200	-	-
Output voltage	V _{O(ON)}	$I_0 = 10mA$, $I_1 = 0.5mA$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	2.8	5.0	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = 5V, I_0 = 0.1mA$	1.0	1.2	-	V
Transition frequency	f_{T}^{*}	$V_0=10V$, $I_0=5mA$, $f=1MHz$	-	200	-	MHz
Input current	l ₁	$V_1 = 5V, I_0 = 0$	-	-	0.18	mA
Input resistor (Input to base)	R ₁	-	33	47	61	KΩ
Input resistor (Base to common)	R ₂	-	33	47	61	KΩ

* : Characteristic of transistor only

PIN Connection



Electrical Characteristic Curves

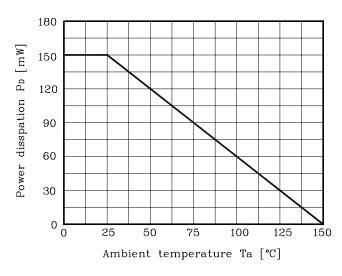


Fig. 1 P_D - Ta

Fig. 2 $I_O - V_{I(ON)}$

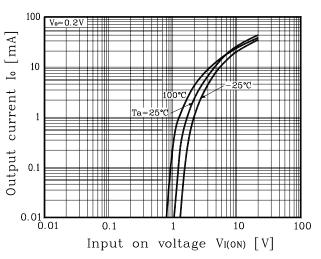
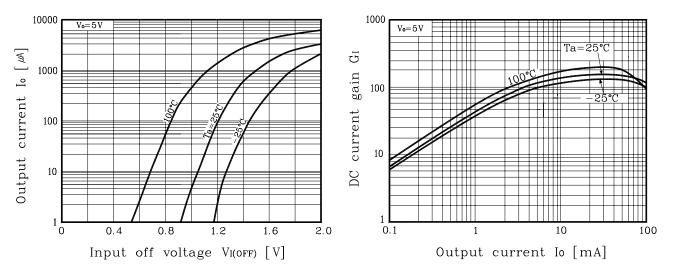
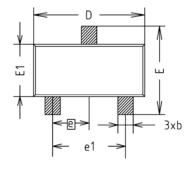


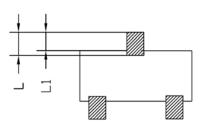
Fig. 3 I_O - $V_{I(OFF)}$

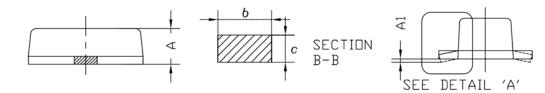
Fig. 4 G_I - I_O



Outline Dimension







SYMBOL	١	MILLIMETERS			
STMBUL	MINIMUM	NOMINAL	MAXIMUM	NDTE	
A	0.63	0.68	0.73		
A1	0.00	-	0.10		
A2	-	-	-		
b	0.25	0.30	0.35		
С	0.04	0.11	0.20		
D	1.50	1.60	1.70		
E	1.50	1.60	1.70		
E1	0.78	0.88	0.98		
e	0.50BSC				
e1	0.90	-	1.10		
L	0.34	0.44	0.54		
L1	0.28	0.34	0.43		

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