

# **SRC1203UF**

**NPN Silicon Transistor** 

#### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

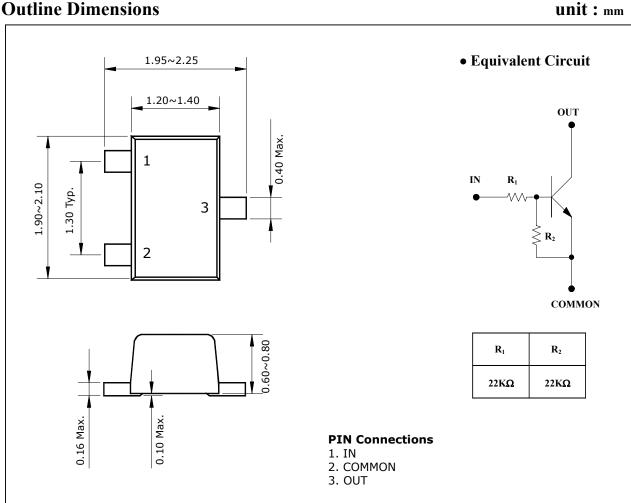
#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## **Ordering Information**

Type NO.	Marking	Package Code
SRC1203UF	R3	SOT-323F

# **Outline Dimensions**



**Absolute Maximum Ratings** 

(Ta=25°C)

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Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V <sub>I</sub>	40,-10	V
Output current	I <sub>O</sub>	100	mA
Power dissipation	P <sub>D</sub>	200	mW
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C

## **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	$V_0 = 50V, V_I = 0$	-	-	500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =5V, I <sub>O</sub> =10mA	70	120	-	-
Output voltage	V <sub>O(ON)</sub>	$I_O$ =10mA, $I_I$ =0.5mA	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>O</sub> =0.2V, I <sub>O</sub> =5mA	-	2.1	3.0	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>O</sub> =5V, I <sub>O</sub> =0.1mA	1.0	1.2	-	V
Transition frequency	f <sub>T</sub> *	$V_0=10V$ , $I_0=5mA$ , $f=1MHz$	-	200	-	MHz
Input current	$I_{\rm I}$	$V_I=5V$ , $I_O=0$	-	-	0.36	mA
Input resistor (Input to base)	R <sub>1</sub>	-	15.4	22	28.6	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	15.4	22	28.6	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

#### **Electrical Characteristic Curves**

Fig. 1  $I_O$  -  $V_{I(ON)}$ 

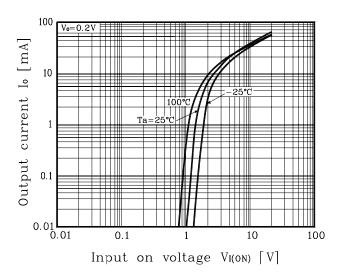


Fig. 2 I<sub>O</sub> - V<sub>I(OFF)</sub>

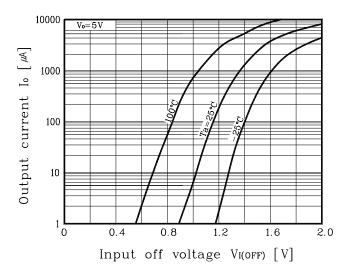
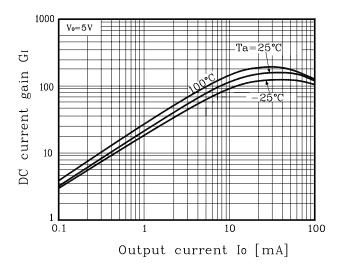


Fig. 3 G<sub>I</sub> - I<sub>O</sub>



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