

# **SRA2202S**

**PNP Silicon Transistor** 

#### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

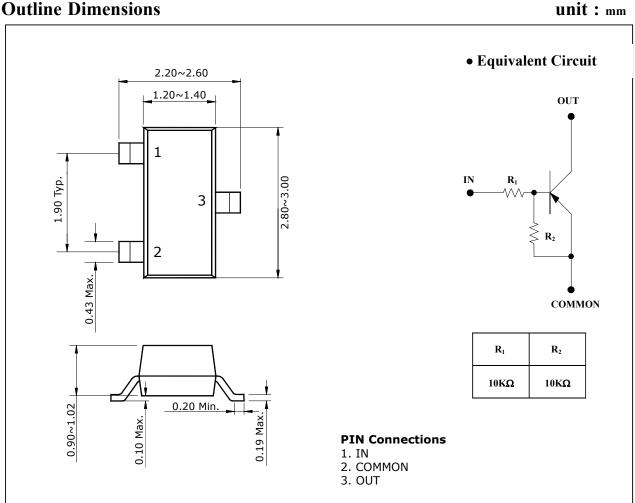
#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## **Ordering Information**

Type NO.	Marking	Package Code	
SRA2202S	RA2	SOT-23	

# **Outline Dimensions**



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**Absolute Maximum Ratings** 

(Ta=25°C)

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Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-30, 10	V
Output current	$I_{O}$	-100	mA
Power dissipation	$P_{D}$	200	mW
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C

## **Electrical Characteristics**

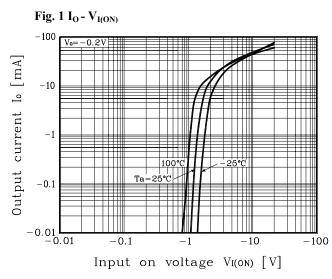
(Ta=25°C)

Characteristic	Symbol	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = -50V, V_I = 0$	1	1	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-10mA	50	80	-	-
Output voltage	V <sub>O(ON)</sub>	$I_O$ =-10mA, $I_I$ =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_{O}$ =-0.2V, $I_{O}$ =-5mA	-	-1.8	-2.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_{O}$ =-5V, $I_{O}$ =-0.1mA	-1.0	-1.2	-	V
Transition frequency	f <sub>T</sub> *	$V_O$ =-10V, $I_O$ =-5mA, f=1MHz	-	200	-	MHz
Input current	$I_{\mathrm{I}}$	$V_{\rm I}$ =-5V, $I_{\rm O}$ =0	-	-	-0.88	mA
Input resistor (Input to base)	R <sub>1</sub>	-	7	10	13	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	7	10	13	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

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#### **Electrical Characteristic Curves**



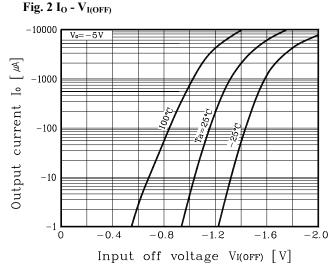
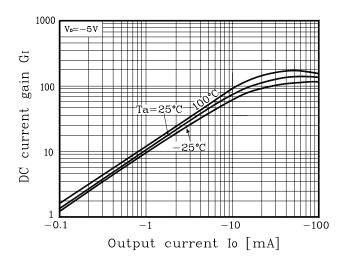


Fig. 3 G<sub>I</sub> - I<sub>O</sub>



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