

# RD5CYDT08

## IGBT Driver / CMOS Logic Level Shifter

REJ03D0181-0700  
 Rev.7.00  
 Apr 22, 2008

### Description

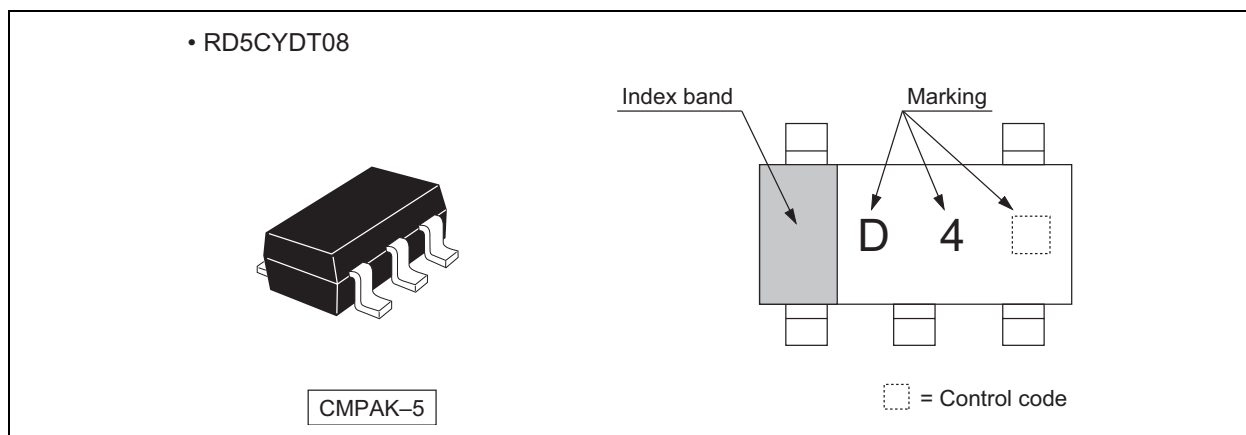
The RD5CYDT08 has two-input AND gate in a 5 pin package. This product is suited as IGBT Driver IC for the strobe.

### Features

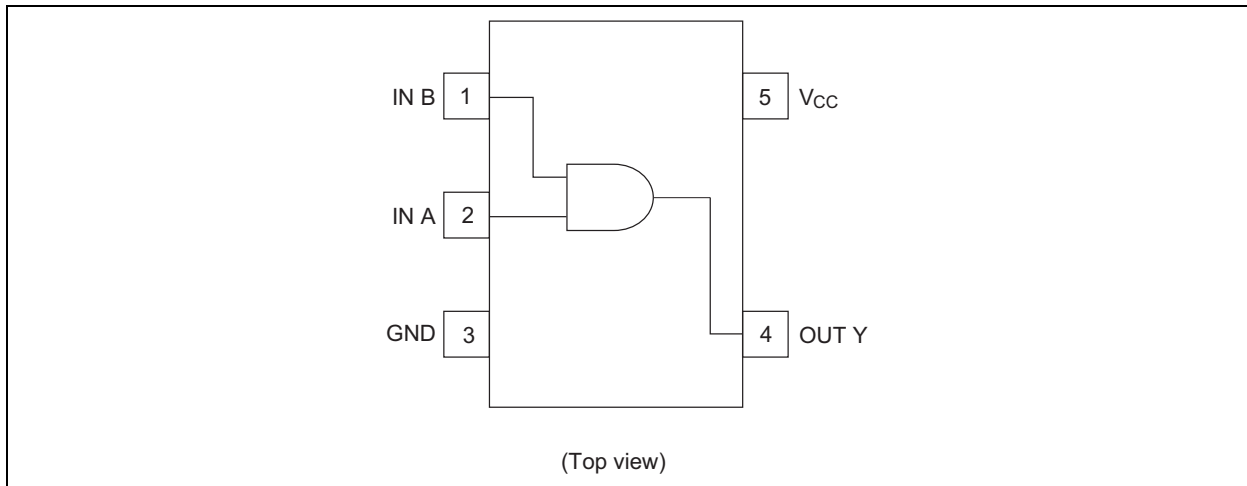
- Supplied on emboss taping for high-speed automatic mounting.
- TTL compatible input level
- Supply voltage range : 4.0 to 6.0 V
- Operating temperature range: -40 to +85°C
- Logic-level translate function  
 3.0 V CMOS logic → 5.0 V CMOS logic
- High drive current  
 $I_{OH \text{ short}} = -130 \text{ mA (min)}$  (@ $V_{CC} = 5.0 \text{ V}$ )
- Low sink current  
 $I_{OL \text{ short}} = 40 \text{ mA (max)}$  (@ $V_{CC} = 5.0 \text{ V}$ )
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD5CYDT08CME	CMPAK-5 pin	PTSP0005ZC-A (CMPAK-5V)	CM	E (3,000 pcs/reel)

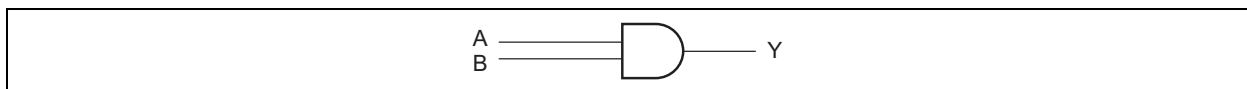
### Outline and Article Indication



## Pin Arrangement



## Logic Diagram



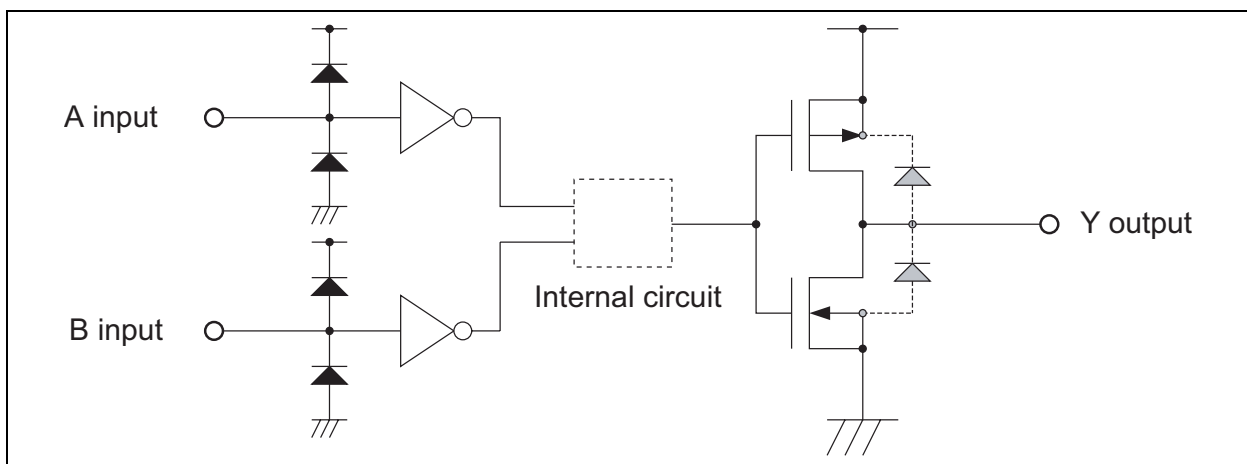
## Function Table

Inputs		Output Y
A	B	
L	L	L
H	L	L
L	H	L
H	H	H

H: High level

L: Low level

## Block Diagram



## Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	$V_{CC}$	-0.5 to 7.0	V	
Input voltage range <sup>*1</sup>	$V_I$	-0.5 to $V_{CC} + 0.5$	V	
Output voltage range <sup>*1,2</sup>	$V_O$	-0.5 to $V_{CC} + 0.5$	V	
Input clamp current	$I_{IK}$	$\pm 20$	mA	$V_I < 0$ or $V_I > V_{CC}$
Output clamp current	$I_{OK}$	$\pm 50$	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	$I_O$	-200	mA	$V_O = 0$
		100		$V_O = V_{CC}$
Continuous current through $V_{CC}$ or GND	$I_{CC}$ or $I_{GND}$	$\pm 200$	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) <sup>*3</sup>	$P_T$	200	mW	
Storage temperature	$T_{stg}$	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed. When Over shoot / Under shoot pulse width is under 10 ns, input and output voltage permit to -15 V or  $V_{CC}+1.5$  V.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150 $^\circ\text{C}$ .

## Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	$V_{CC}$	4.0	6.0	V	
Input voltage range	$V_I$	0	$V_{CC}$	V	
Output voltage range	$V_O$	0	$V_{CC}$	V	
Operating free-air temperature	$T_a$	-40	85	$^\circ\text{C}$	

Note: Unused or floating inputs must be held high or low.

## Electrical Characteristic

$T_a = -40$  to  $85^\circ\text{C}$

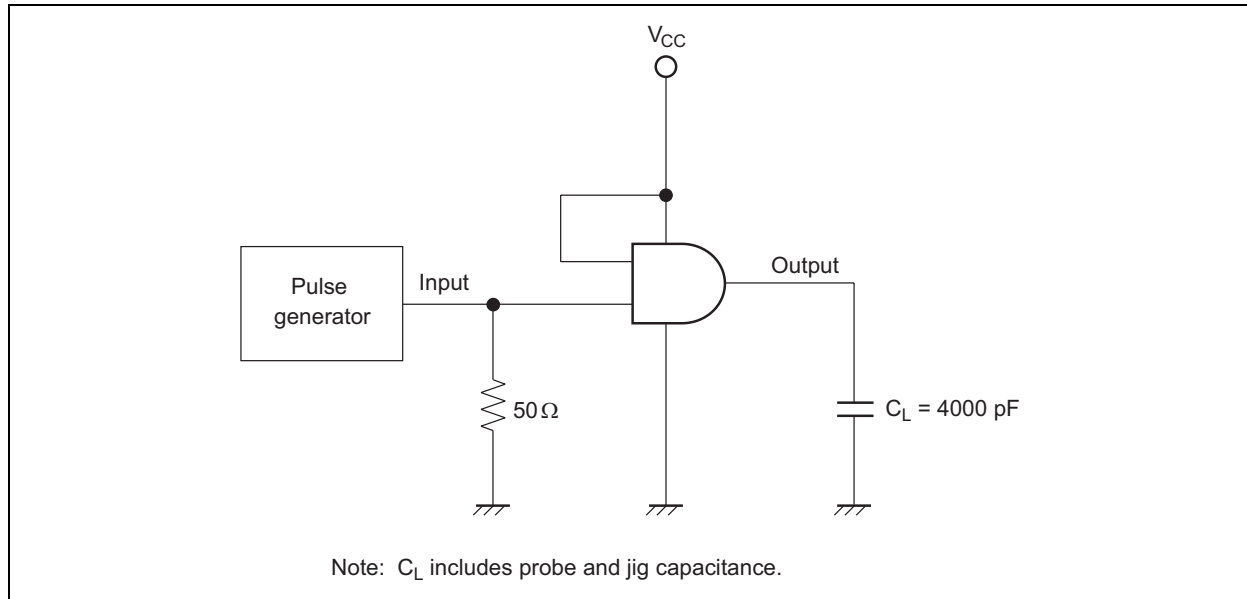
Item	Symbol	$V_{CC}$ (V)	Min	Typ	Max	Unit	Test condition
Input voltage	$V_{IH}$	4.5 to 5.5	2.0	—	—	V	
	$V_{IL}$	4.5 to 5.5	—	—	0.8		
Output current	$I_{OH}$ short	5.0	-100	-130	-160	mA	$V_O = 0$ V
	$I_{OL}$ short	5.0	30	40	50		$V_O = V_{CC}$
Input current	$I_{IN}$	5.5	—	—	$\pm 5$	$\mu\text{A}$	$V_{IN} = 5.5$ V or GND
Quiescent supply current	$I_{CC}$	5.5	—	—	10	$\mu\text{A}$	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Input capacitance	$C_{IN}$	5.0	—	2.5	—	pF	$V_{IN} = V_{CC}$ or GND

## Switching Characteristics

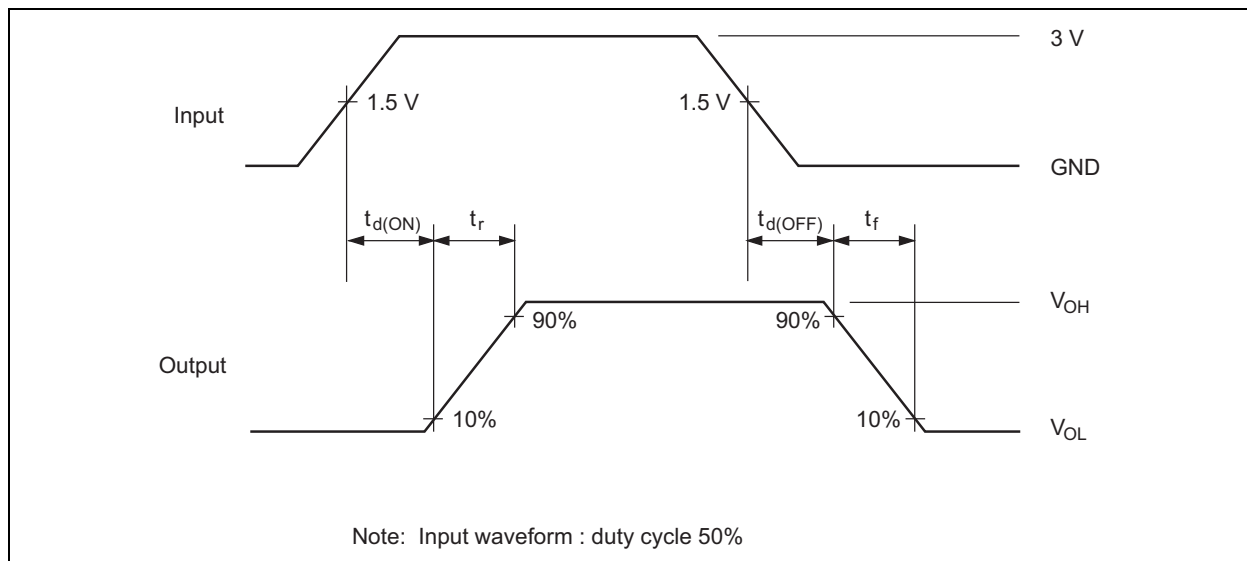
$V_{CC} = 5.0 \pm 0.5 \text{ V}$

Item	Symbol	$T_a = -40 \text{ to } 85^\circ\text{C}$			Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max				
Propagation delay time	$t_{d(ON)}$	—	—	70	ns	$C_L = 4000 \text{ pF}$	A or B	Y
	$t_{d(OFF)}$	—	—	140				
Output rise time	$t_r$	—	—	800				
Output fall time	$t_f$	—	—	1500				

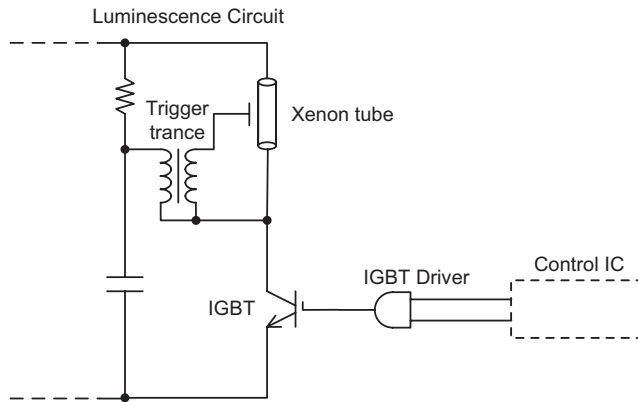
## Test Circuit



## Waveforms



## Application Example (Strobe circuit)



## Combination example

SYSTEM	IGBT	IGBT Driver	Control IC
3.3 V	RJP4002ANS RJP4002ASA	← RD3CYD08 ← RD3CYDT08	← 3.3 V signal
5.0 V	RJP4003ANS RJP4003ASA	← RD5CYD08 ← RD5CYDT08	← 5.0 V signal ← 3.3 V signal

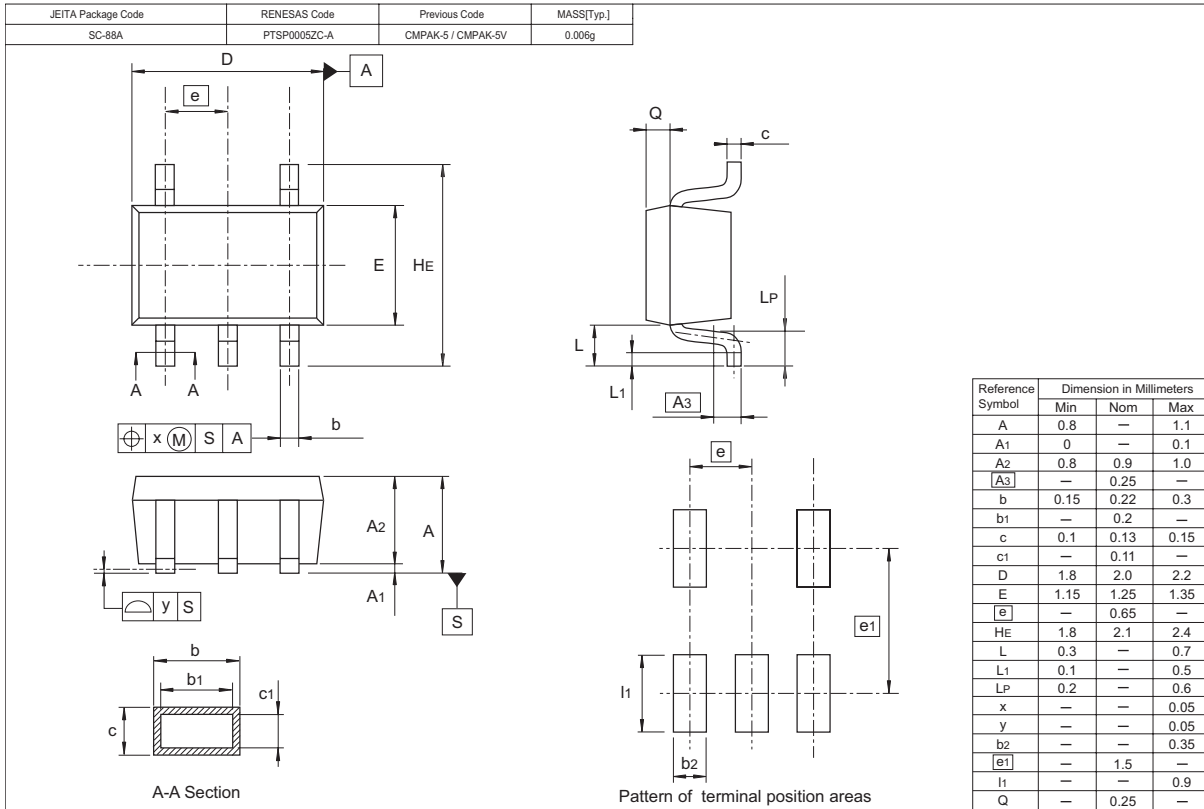
## IGBT Driver Lineup

TYPE No.	Specification	Package
RD3CYD08	$V_{CC} = 2.0$ to $3.6V$ CMOS lever input $I_{OH}(short) = -130mA(typ)$ @ $V_{CC} = 3.3V$ $I_{OL}(short) = 45mA(typ)$ @ $V_{CC} = 3.3V$	CMPAK-5 VSON-5
RD3CYDT08	$V_{CC} = 2.0$ to $3.6V$ CMOS lever input $I_{OH}(short) = -130mA(typ)$ @ $V_{CC} = 3.3V$ $I_{OL}(short) = 45mA(typ)$ @ $V_{CC} = 3.3V$	CMPAK-5
RD5CYD08	$V_{CC} = 4.0$ to $6.0V$ CMOS lever input $I_{OH}(short) = -130mA(typ)$ @ $V_{CC} = 5.0V$ $I_{OL}(short) = 40mA(typ)$ @ $V_{CC} = 5.0V$	CMPAK-5
RD5CYDT08	$V_{CC} = 4.0$ to $6.0V$ TTL lever input $I_{OH}(short) = -130mA(typ)$ @ $V_{CC} = 5.0V$ $I_{OL}(short) = 40mA(typ)$ @ $V_{CC} = 5.0V$	

## IGBT Lineup

TYPE No.	Specification	Package
RJP4002ANS	$V_{CES} = 400V(max)$ , $I_{CP} = 150A(max)$ , 2.5V drive	VSON-8
RJP4002ASA	$V_{CES} = 400V(max)$ , $I_{CP} = 150A(max)$ , 2.5V drive	TSSOP-8
RJP4003ANS	$V_{CES} = 400V(max)$ , $I_{CP} = 150A(max)$ , 4V drive	VSON-8
RJP4003ASA	$V_{CES} = 400V(max)$ , $I_{CP} = 150A(max)$ , 4V drive	TSSOP-8

### Package Dimensions



Notes:

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