# AN1431T, AN1431M

# Variable output shunt regulator

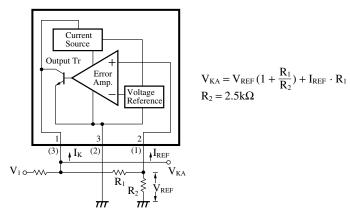
# Overview

The AN1431T and AN1431M are highly accurate stabilized power supplies in which the output voltage can be adjusted in the range from approximately 2.5 to 36V under the operating temperature by using the external resistor. Because of its fast rising characteristic, it can be used as a Zener diode and has the wide application.

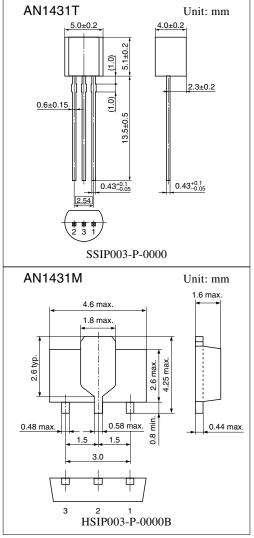
## Features

- High precision reference voltage: 2.5V (allowance: ±2%)
- High temperature stability: 17ppm/°C typ.
- Output voltage externally adjustable: 2.5 to 36V
- Fast rising output
- Low input impedance:  $0.2\Omega$  typ.
- Low output noise voltage

# ■ Block Diagram (AN1431T)



Note) The number in ( ) shows the pin number for the AN1431M.



Note) The packages (SSIP003-P-0000 and HSIP003-P-0000B) of this product will be changed to lead-free type (SSIP003-P-0000S and HSIP003-P-0000Q). See the new package dimensions section later of this datasheet.

# Pin Descriptions

#### • AN1431T

Pin No.	Description		
1	Cathode		
2	Reference pin		
3	Anode		

#### • AN1431M

Pin No.	Description				
1	Reference pin				
2	Anode				
3	Cathode				

# Absolute Maximum Ratings at $T_a = 25^{\circ}C$

Parameter		Symbol	Rating	Unit	
Supply voltage		V <sub>CC</sub>	37	v	
Supply current		I <sub>CC</sub>	-100 to +150	mA	
Power dissipation		P <sub>D</sub>	650 *	mW	
Reference input current		I <sub>REF</sub>	- 0.05 to +10	mA	
Operating ambient temperature	AN1431T	T <sub>opr</sub>	-20 to +85	00	
	AN1431M		-25 to +80	°C	
Storage temperature	AN1431T	T <sub>stg</sub>	-55 to +150		
	AN1431M		-55 to +125	°C	

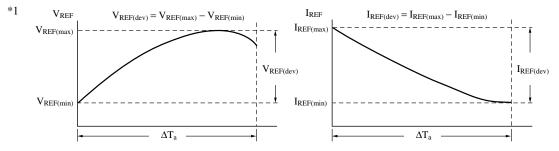
\* AN1431M is mounted on a standard board (glass epoxy: 20mm × 20mm × t1.7mm with Cu foil of 1cm<sup>2</sup> or more).

# ■ Recommended Operating Range at $T_a = 25^{\circ}C$

Parameter	Symbol	Range		
Supply voltage	V <sub>KA</sub>	V <sub>REF</sub> (2.5 to 36V)		

## Electrical Characteristics at $T_a = 25^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reference voltage	V <sub>REF</sub>	$V_{KA} = V_{REF}, I_K = 10mA$	2.45	2.50	2.55	V
Reference voltage change to temperature	V <sub>REF(Jev)</sub> *1, 2	$\label{eq:KA} \begin{split} V_{KA} = V_{REF}, \ I_K = 10 mA, \\ T_a = 0 \ to + 70^\circ C \end{split}$		3	17	mV
Reference voltage power supply characteristic	$\Delta V_{REF}$	$I_{K} = 10 \text{mA}, \Delta V_{KA} = 10 \text{V to } V_{REF}$		-1.2	-2.7	mV/V
	$\Delta V_{KA}$	$I_{K} = 10 \text{mA}, \Delta V_{KA} = 36 \text{V}$ to $10 \text{V}$		-1	-2	mV/V
Reference input current	I <sub>REF</sub>	$I_{K} = 10 \text{mA}, R_{1} = 10 \text{k}\Omega, R_{2} = \infty$		2	4	μA
Reference input current change to temperature	I <sub>REF(dev)</sub> *2	$I_{\rm K} = 10 {\rm mA}, R_1 = 10 {\rm k}\Omega, R_2 = \infty, T_{\rm a} = 0 \text{ to } +70^{\circ}{\rm C}$		0.4	1.2	μΑ
Minimum cathode current	I <sub>min</sub>	$V_{KA} = V_{REF}$		0.4	1.0	mA
Off-state cathode current	I <sub>OFF</sub>	$V_{KA} = 36V, V_{REF} = 0V$		0.1	1.0	μΑ
Dynamic impedance	Z <sub>KA</sub>   *3	$V_{KA} = V_{REF}, I_K = 1 \text{ to } 100\text{mA}, f \le 1\text{kHz}$		0.2	0.5	Ω



The temperature coefficient  $aV_{\text{REF}}$  for the reference input voltage is equivalently given by the following expression.

$$\left| aV_{\text{REF}} \right| = \frac{\frac{V_{\text{REF}}(\text{dev})}{V_{\text{REF}}^{\dagger}}}{\Delta T_{a}} \times 10^{6} \text{ (ppm/°C)} \quad \dagger V_{\text{REF}} \text{ at } T_{a} = 25^{\circ}\text{C}$$

For example, assuming  $V_{\text{REF(max)}} = 2500 \text{mV}$  ( $T_a = 30^{\circ}\text{C}$ ),  $V_{\text{REF(min)}} = 2497 \text{mV}$  ( $T_a = 0^{\circ}\text{C}$ ), ( $V_{\text{REF(dev)}} = 3 \text{mV}$ ) and  $V_{\text{REF}} = 2499 \text{mV}$  ( $T_a = 25^{\circ}\text{C}$ )  $\Delta T_a = 70^{\circ}\text{C}$ 

then, 
$$|aV_{REF}| = \frac{\frac{3mV}{2499mV}}{70^{\circ}C} \times 10^{6} = 17.1 \text{ (ppm/°C)}$$

\*2 These values are design reference values, not guaranteed ones.

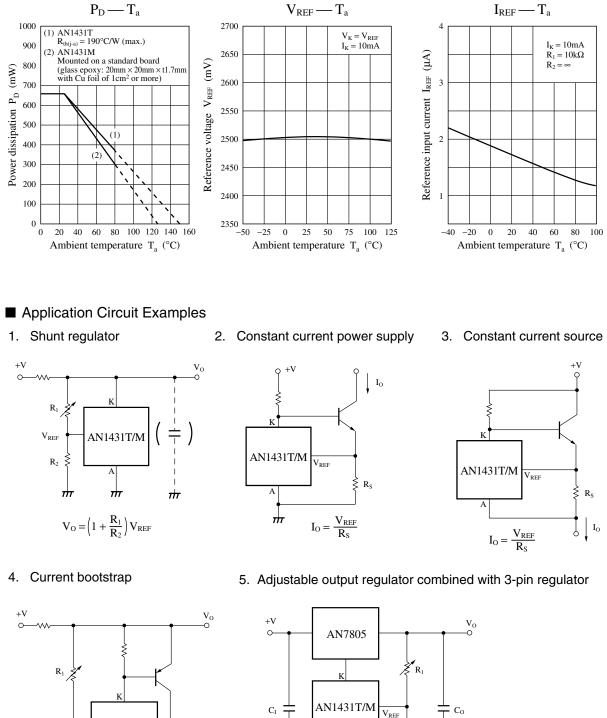
\*3 The dynamic impedance is defined by the following expression.  $|Z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_K}$ 

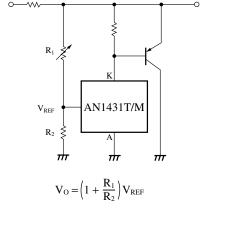
The total dynamic impedance at  $\Delta V_{REF}$  ,  $\Delta V_{KA}$  ,  $I_{REF}$  and  $I_{REF(dev)}$  is as follows.

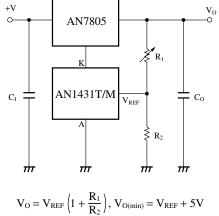
$$\left| \mathbf{Z} \right| = \frac{\Delta \mathbf{V}}{\Delta \mathbf{I}} = \left| \mathbf{Z}_{\mathrm{KA}} \right| \left( 1 + \frac{\mathbf{R}_1}{\mathbf{R}_2} \right)$$

2

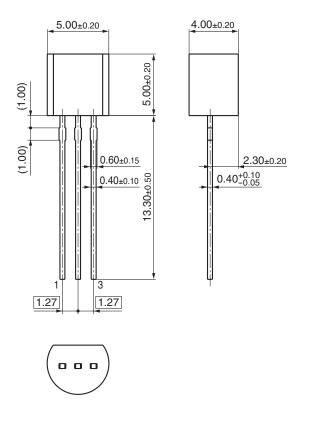
# Main Characteristics



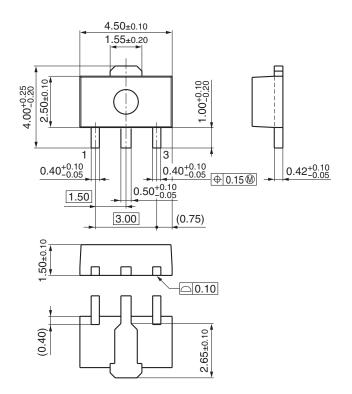




- New Package Dimensions (Unit: mm)
- SSIP003-P-0000S (Lead-free package)



• HSIP003-P-0000Q (Lead-free package)



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