BCP53 Series

PNP Silicon Epitaxial Transistors

This PNP Silicon Epitaxial transistor is designed for use in audio amplifier applications. The device is housed in the SOT-223 package which is designed for medium power surface mount applications.

- High Current: 1.5 Amps
- NPN Complement is BCP56
- The SOT-223 Package can be soldered using wave or reflow. The formed leads absorb thermal stress during soldering, eliminating the possibility of damage to the die
- Device Marking:

BCP53T1 = AH

BCP53-10T1 = AH-10

BCP53-16T1 = AH-16

• Pb-Free Packages are Available

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-80	Vdc
Collector-Base Voltage	V _{CBO}	-100	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current	I _C	1.5	Adc
Total Power Dissipation @ T _A = 25°C (Note 1.) Derate above 25°C	P _D	1.5 12	Watts mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient (surface mounted)	$R_{ hetaJA}$	83.3	°C/W
Lead Temperature for Soldering, 0.0625" from case Time in Solder Bath	T _L	260 10	°C Sec

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Device mounted on a glass epoxy printed circuit board 1.575 in. x 1.575 in. x 0.059 in.; mounting pad for the collector lead min. 0.93 sq. in.



ON Semiconductor®

http://onsemi.com

MEDIUM POWER HIGH CURRENT SURFACE MOUNT PNP TRANSISTORS

COLLECTOR 2,4

EMITTER 3



SOT-223 CASE 318E STYLE 1

MARKING DIAGRAM



AHxxx = Device Code xxx = -10 or -16 A = Assembly Location

Y = Year W = Work Week • Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

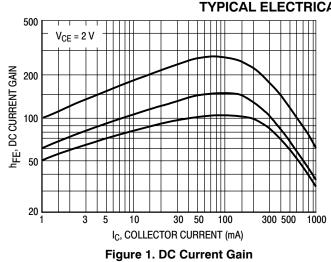
Device	Package	Shipping [†]
BCP53T1	SOT-223	1000/Tape & Reel
BCP53T1G	SOT-223 (Pb-Free)	1000/Tape & Reel
BCP53-10T1	SOT-223	1000/Tape & Reel
BCP53-10T1G	SOT-223 (Pb-Free)	1000/Tape & Reel
BCP53-16T1	SOT-223	1000/Tape & Reel
BCP53-16T1G	SOT-223 (Pb-Free)	1000/Tape & Reel
BCP53-16T3	SOT-223	4000/Tape & Reel
BCP53-16T3G	SOT-223 (Pb-Free)	4000/Tape & Reel

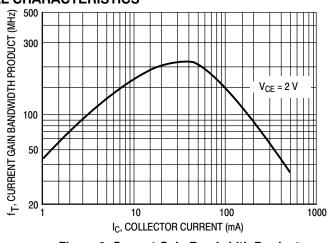
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

1

BCP53 Series

Characteristics	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					•
Collector-Base Breakdown Voltage (I _C = -100 μAdc, I _E = 0)	V _{(BR)CBO}	-100	-	-	Vdc
Collector-Emitter Breakdown Voltage (I _C = -1.0 mAdc, I _B = 0)	V _{(BR)CEO}	-80	-	-	Vdc
Collector-Emitter Breakdown Voltage (I _C = -100 μAdc, R _{BE} = 1.0 kohm)	V _{(BR)CER}	-100	-	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = -10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	-5.0	-	-	Vdc
Collector-Base Cutoff Current (V _{CB} = -30 Vdc, I _E = 0)	I _{CBO}	-	-	-100	nAdc
Emitter-Base Cutoff Current (V _{EB} = -5.0 Vdc, I _C = 0)	I _{EBO}	-	-	-10	μAdc
ON CHARACTERISTICS					
DC Current Gain (I_C = -5.0 mAdc, V_{CE} = -2.0 Vdc) All Part Types (I_C = -150 mAdc, V_{CE} = -2.0 Vdc) BCP53 BCP53-10 BCP53-16 (I_C = -500 mAdc, V_{CE} = -2.0 Vdc) All Part Types	h _{FE}	25 40 63 100 25	- - - -	250 160 250	-
Collector-Emitter Saturation Voltage ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$)	V _{CE(sat)}	-	-	-0.5	Vdc
Base-Emitter On Voltage ($I_C = -500 \text{ mAdc}$, $V_{CE} = -2.0 \text{ Vdc}$)	V _{BE(on)}	-	-	-1.0	Vdc
DYNAMIC CHARACTERISTICS					
Current-Gain - Bandwidth Product (I _C = -10 mAdc, V _{CE} = -5.0 Vdc, f = 35 MHz)	f _T	-	50	-	MHz
TYPICAL ELECTRICAL CHA		TICS			
500 V _{CE} = 2 V JORONA HIGH				V _{CE}	





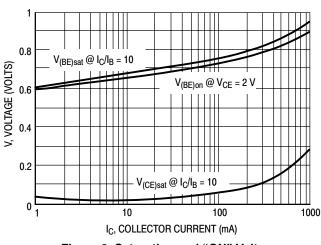


Figure 2. Current Gain Bandwidth Product

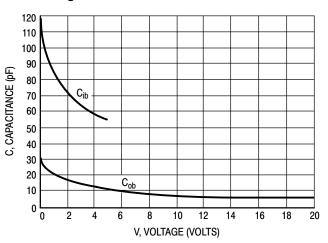


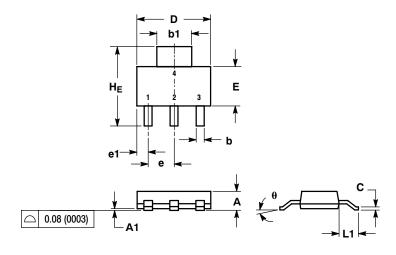
Figure 3. Saturation and "ON" Voltages

Figure 4. Capacitances

BCP53 Series

PACKAGE DIMENSIONS

SOT-223 CASE 318E-04 ISSUE K



NOTES:

DIMENSIONING AND TOLERANCING PER ANSI

Y14.5M, 1982.

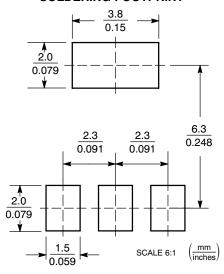
CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
С	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
е	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	_	10°	0°	-	10°

STYLE 1: PIN 1. BASE

2. COLLECTOR 3. EMITTER 4. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative