

## Features

- Low On-Resistance
- Very Low Gate Threshold Voltage, 0.9V Max.
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **ESD Protected Gate**

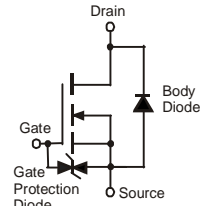


ESD protected

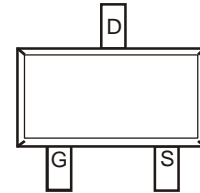


TOP VIEW

SOT-23



Equivalent Circuit



TOP VIEW

## Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.008 grams (approximate)

## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate-Source Voltage	V <sub>GSS</sub>	±10	V
Drain Current per element (Note 1)	I <sub>D</sub>	Continuous	300
		Pulsed (Note 3)	600

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P <sub>D</sub>	350	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	357	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 5)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 17V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±5	μA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 5)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.53	—	0.9	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 100μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	—	3.5	Ω	V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 200mA
		—	—	1.7		
Forward Transfer Admittance	Y <sub>fs</sub>	40	—	—	mS	V <sub>DS</sub> = 3V, I <sub>D</sub> = 10A

- Notes:
1. Device mounted on FR-4 PCB.
  2. No purposefully added lead.
  3. Pulse width ≤10μs, Duty Cycle ≤1%.
  4. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  5. Short duration pulse test used to minimize self-heating effect.

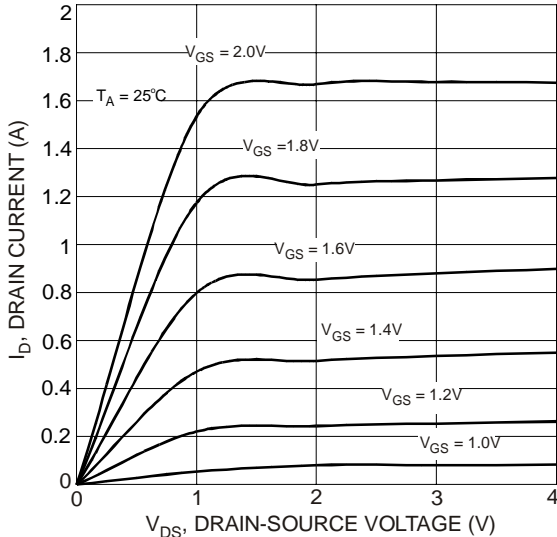


Fig. 1 Typical Output Characteristics

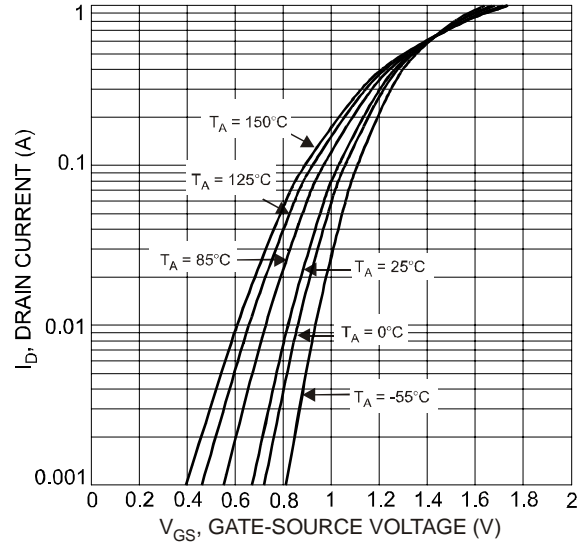


Fig. 2 Reverse Drain Current vs. Source-Drain Voltage

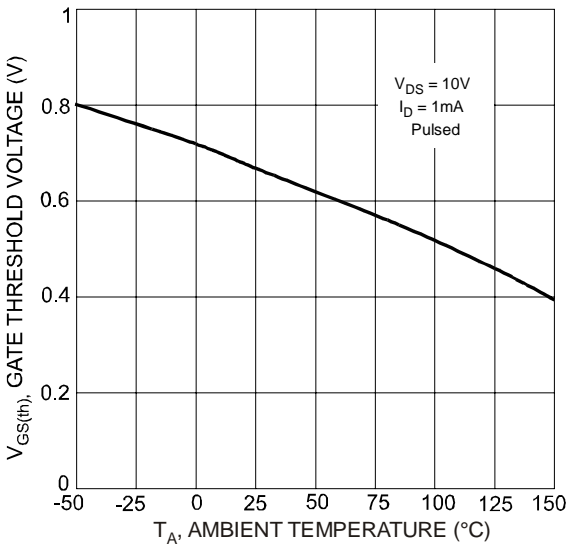


Fig. 3 Gate Threshold Voltage vs. Ambient Temperature

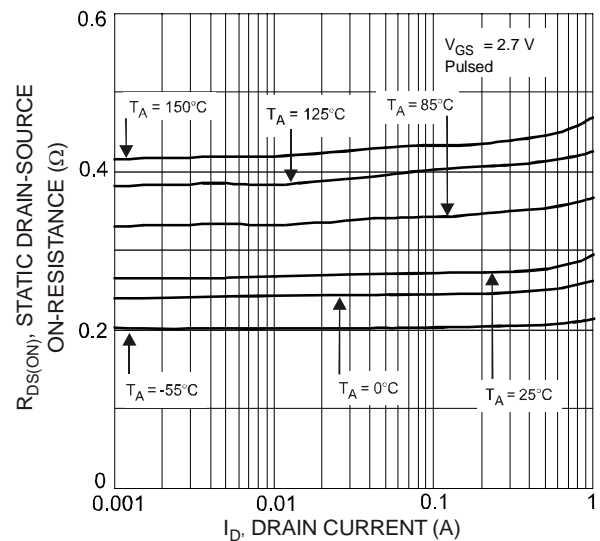


Fig. 4 Static Drain-Source On-State Resistance vs. Drain Current

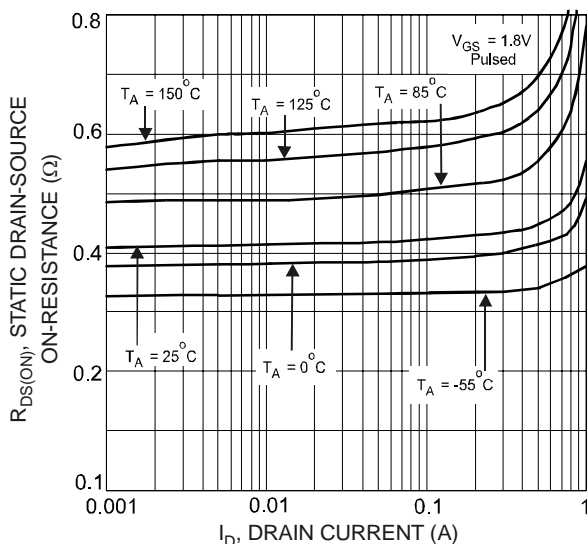


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

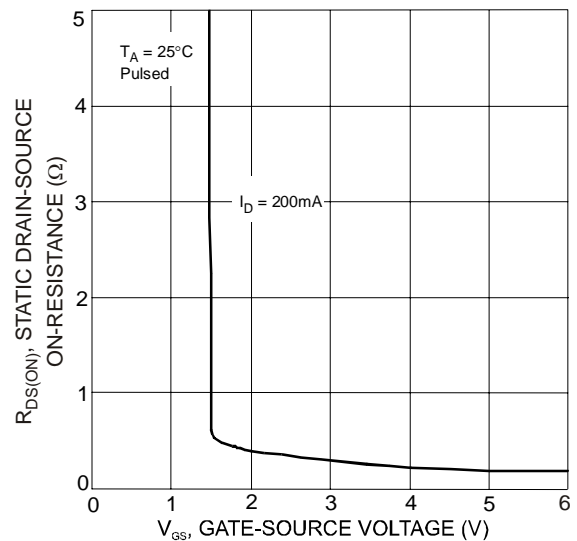


Fig. 6 Static Drain-Source, On-Resistance vs. Gate-Source Voltage

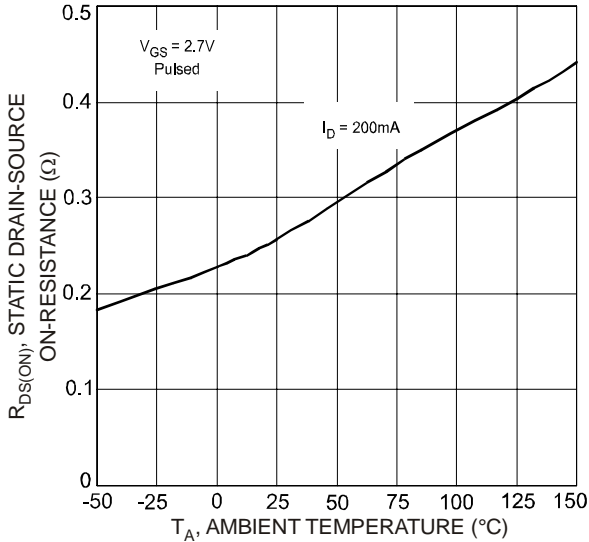


Fig. 7 Static Drain-Source, On-Resistance vs. Ambient Temperature

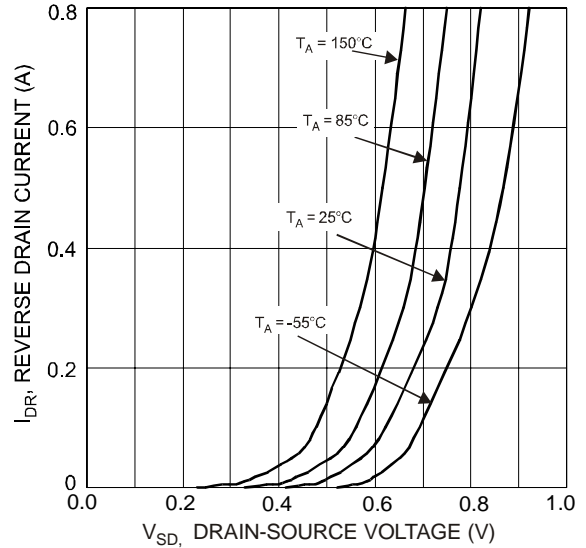


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

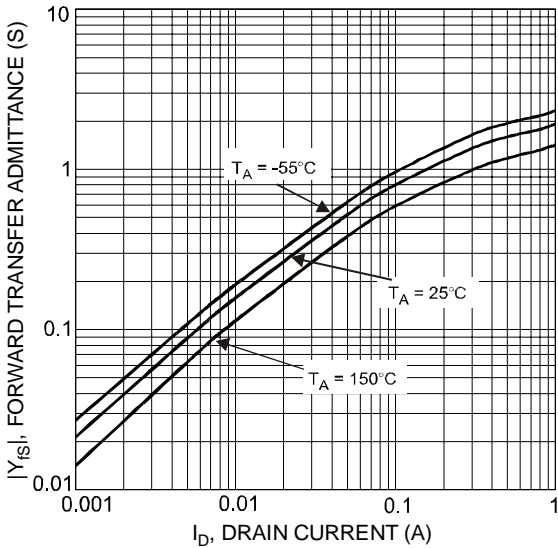


Fig. 9 Forward Transfer Admittance vs. Drain Current

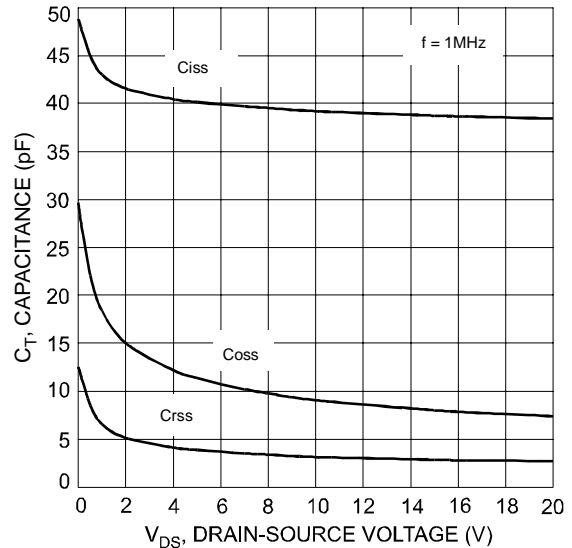


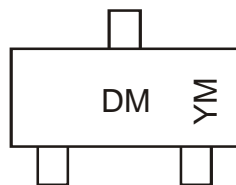
Fig. 10 Typical Capacitance

**Ordering Information** (Note 6)

Part Number	Case	Packaging
DMN2005K-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**



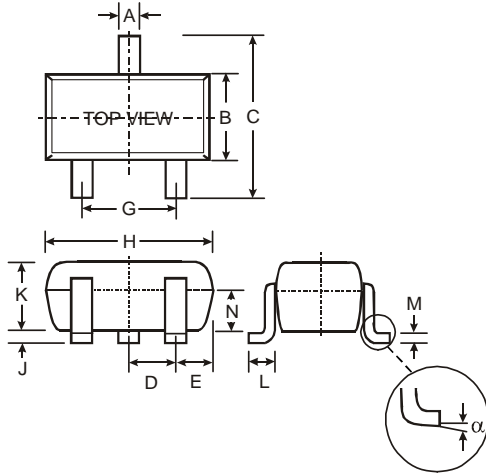
DM = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year ex: T = 2006  
 M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

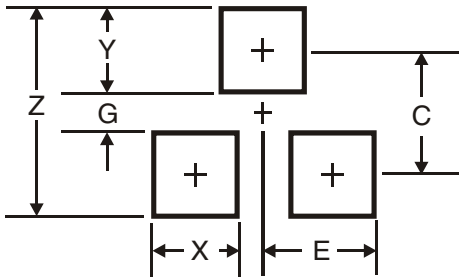
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Package Outline Dimensions**



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
N	—	—
$\alpha$	0°	8°
All Dimensions in mm		

**Suggested Pad Layout**



Dimensions	Value (in mm)
Z	3.4
G	0.7
X	0.9
Y	1.4
C	2.0
E	0.9

**IMPORTANT NOTICE**

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

**LIFE SUPPORT**

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.