# RENESAS

## **HAT2166H** Silicon N Channel Power MOS FET **Power Switching**

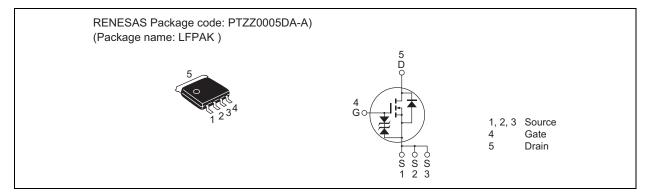
REJ03G0005-0600 Rev.6.00 Sep 20, 2005

### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} = 2.9 \text{ m}\Omega \text{ typ.}$  (at  $V_{GS} = 10 \text{ V}$ )

### Outline



### **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	Ι <sub>D</sub>	45	А
Drain peak current	I <sub>D(pulse)</sub> Note1	180	A
Body-drain diode reverse drain current	I <sub>DR</sub>	45	A
Avalanche current	I <sub>AP</sub> Note 2	25	A
Avalanche energy	E <sub>AR</sub> Note 2	62.5	mJ
Channel dissipation	Pch Note3	25	W
Channel to Case Thermal Resistance	θch-C	5.0	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

3. Tc = 25°C



## **Electrical Characteristics**

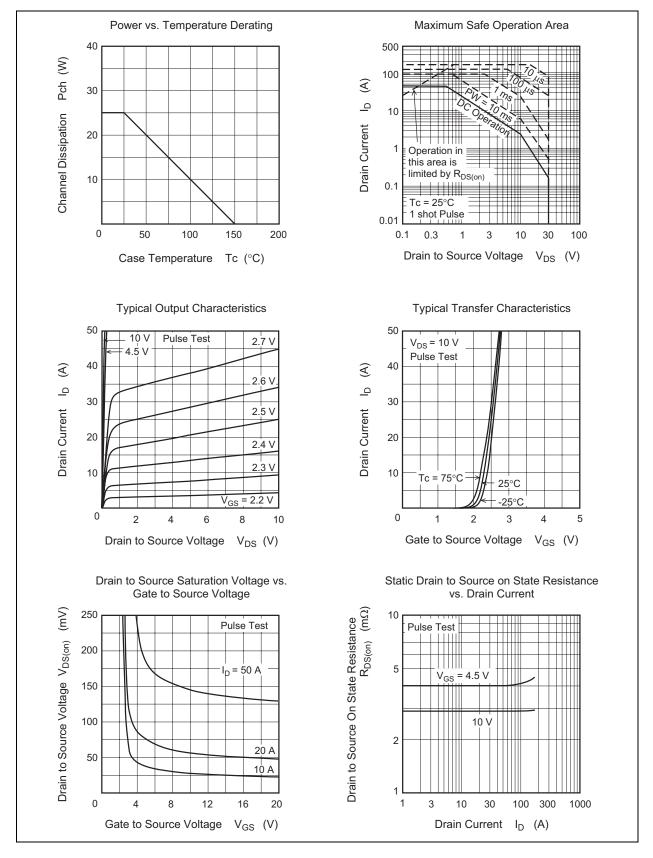
						(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	_	1	μΑ	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	2.9	3.8	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	4.0	6.1	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	52	87	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	4400	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1 MHz
Output capacitance	Coss	_	1000		pF	
Reverse transfer capacitance	Crss	_	330		pF	
Gate Resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	27	_	nC	$V_{DD} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_D = 45 \text{ A}$
Gate to source charge	Qgs	_	12	_	nC	
Gate to drain charge	Qgd	_	5.9	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	12	—	ns	$\label{eq:VGS} \begin{array}{l} V_{GS} = 10 \; V, \; I_{D} = 22.5 \; A, \\ V_{DD} \cong 10 \; V, \; R_{L} = 0.44 \; \Omega, \\ Rg = 4.7 \; \Omega \end{array}$
Rise time	tr	_	35	—	ns	
Turn-off delay time	t <sub>d(off)</sub>	_	55		ns	
Fall time	t <sub>f</sub>	_	7.5	_	ns	
Body-drain diode forward voltage	V <sub>DF</sub>		0.83	1.08	V	$IF = 45 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t <sub>rr</sub>	_	37	_	ns	$IF = 45 A, V_{GS} = 0$
time						di <sub>F</sub> / dt = 100 A/ μs

Notes: 4. Pulse test

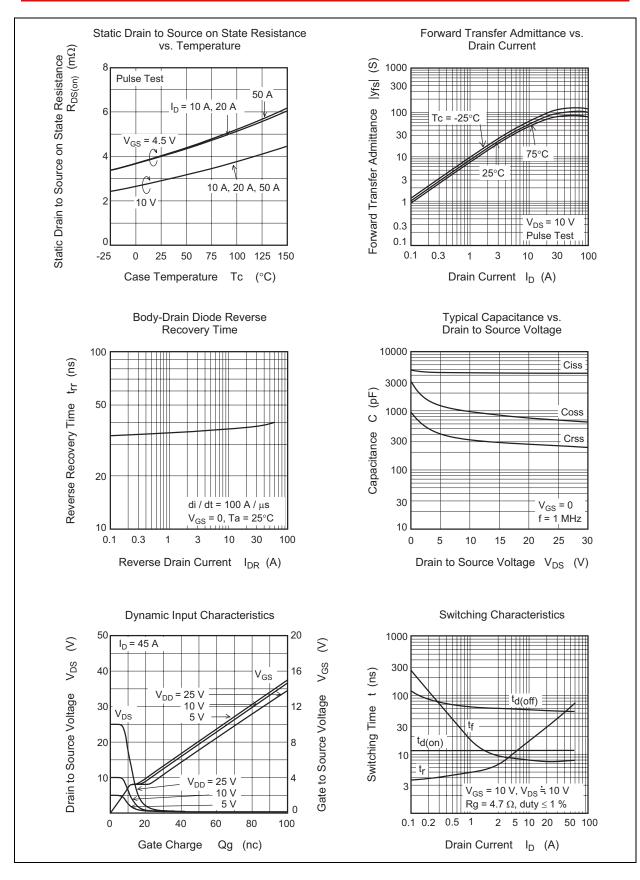
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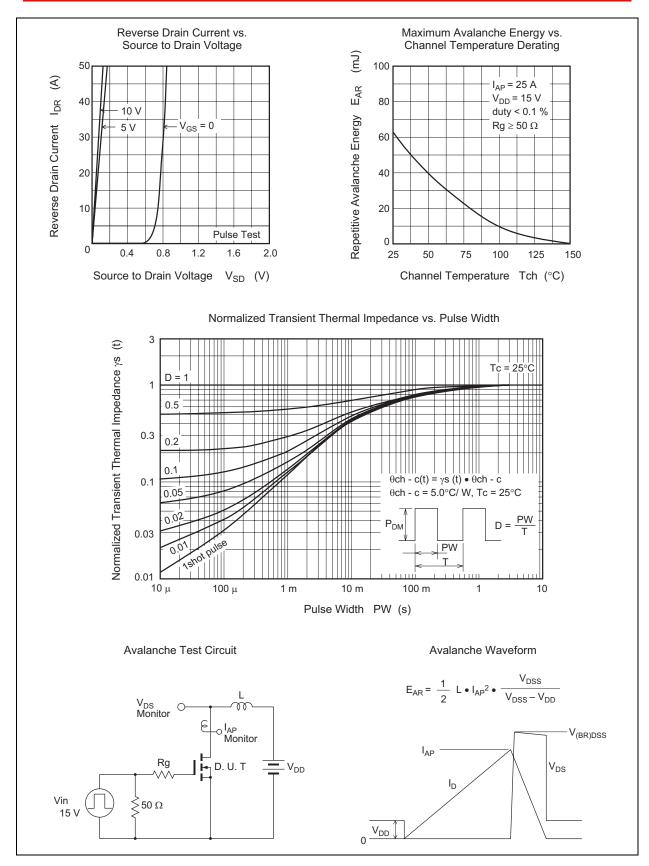
### **Main Characteristics**





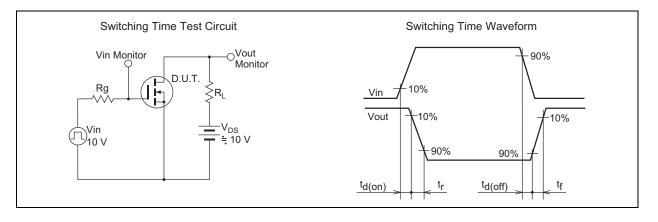






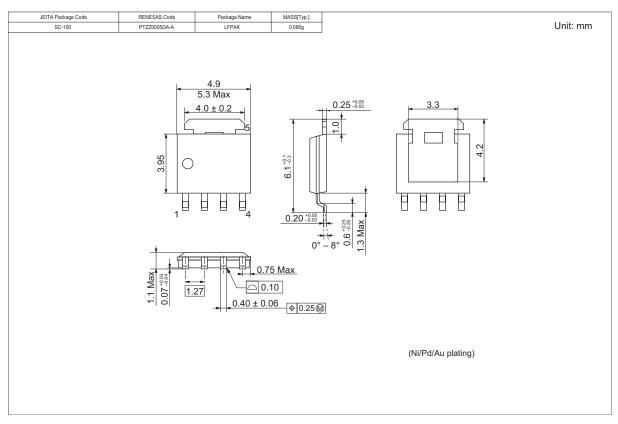
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## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2166H-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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