TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOS VI)

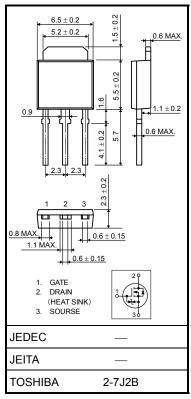
2SK4003

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: R_{DS (ON)} = 1.7 Ω (typ.)
- Low leakage current: I_{DSS} = 100 μ A (max) (V_{DS} = 600 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	600	V
Drain-gate voltage (R _{GS} = 20 kΩ)			V _{DGR}	600	V
Gate-source voltage			V _{GSS}	±30	V
Drain current	DC	(Note 1)	Ι _D	3	А
	Pulse	(Note 1)	I _{DP}	12	А
Drain power dissipation (Tc = 25°C)			PD	20	W
Single-pulse avalanche energy (Note 2)			EAS	168	mJ
Avalanche current			I _{AR}	3	А
Repetitive avalanche energy (Note 3)			E _{AR}	2	mJ
Channel temperature			T _{ch}	150	°C
Storage temperature range			T _{stg}	–55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Мах	Unit	
Thermal resistance, channel to ambient	R _{th (ch−a)}	125	°C / W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 8.2 mH, R_G = 25 Ω , I_{AR} = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm

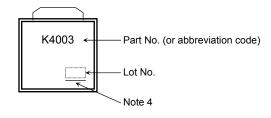
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	—	—	±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	rain cutoff current		V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	—	4.0	V
Drain-source O	N-resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 1.5 A	—	1.7	2.2	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1.5 A	0.5	2.0	—	S
Input capacitance Reverse transfer capacitance		C _{iss}		—	600	—	pF
		C _{rss}	C _{rss} V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	7	_	
Output capacitance		C _{oss}		_	60	—	
Switching time	Rise time	tr	V_{GS} $0 V$ $I_D = 1.5 A V_{OUT}$ V_{GS} $0 V$ F_{C} $F_{L} = 133\Omega$ $V_{DD} \approx 200 V$	_	16	_	ns
	Turn-on time	t _{on}		_	40	_	
	Fall time	t _f		_	18	_	
	Turn-off time	t _{off}	Duty \leq 1%, t _W = 10 μ s	_	80	_	
Total gate charge (gate-source plus gate-drain)		Qg		—	15	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		9		
Gate-drain ("Miller") charge		Q _{gd}]	—	6	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Мах	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	3	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	12	A
Forward voltage (diode)	VDSF	I _{DR} = 3 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 3 A, V _{GS} = 0 V	_	800	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs		5	—	μC

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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