**Monolithic Linear IC** 

LA4728



2-Channel BTL Power Amplifier (35 W+35 W) with Standby Switch for Car Stereos

# Preliminary

## Overview

The LA4728 is a BTL two-channel power IC for car audios developed in pursuit of excellent sound quality. Low-region frequency characteristics have been improved through the use of a new NF capacitorless circuit, and crosstalk which causes "muddy" sound has been reduced by improving both circuit and pattern layout. As a result the LA4728 provides powerful bass and clear treble.

### Features

- $\cdot$  High power: supports total output of 35 W+35 W. [EIAJ power] (V\_{CC}=14.4 V, THD=30 %, R\_L=4 \Omega)
- · Less pop noise.
- $\cdot$  Designed for excellent sound quality. (fL<10 Hz, fH=130 kHz)
- · Any rise time settable by an external capacitor.
- $\cdot$  Standby switch circuit on chip. (microcontroller supported)
- · Various protectors on chip.
- (output-to-ground short/ output-to- $V_{CC}$  short/ load short/ overvoltage/ thermal shutdown circuit)
- $\cdot$  The LA4728 is pin-compatible with the LA4725.

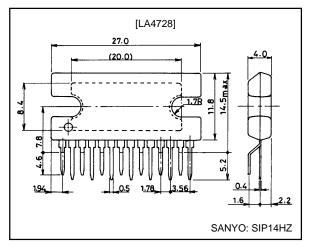
# Specifications

## Maximum Ratings at Ta = 25 °C

# **Package Dimensions**

unit: mm

#### 3113A-SIP14HZ



Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		18	V
Surge supply voltage	$V_{CC}$ surge	$f \le 0.2 \text{ s}$ , single giant pulse	50	V
Maximum output current	I <sub>O</sub> peak	Per channel	3.5	A
Allowable power disspation	Pd max	With arbitrarily large heat sink	32	W
Operating temperature	Topr		-35 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

#### Recommended Conditions at Ta = 25 C

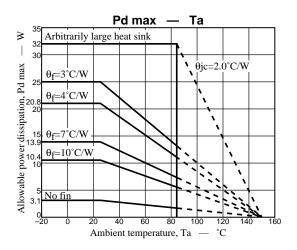
Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		13.2	V
Operating voltage range	V <sub>CC</sub> op	Range where Pd max is not exceeded	9 to 16	V
Recommended load resistance	R <sub>L</sub> op		4	Ω

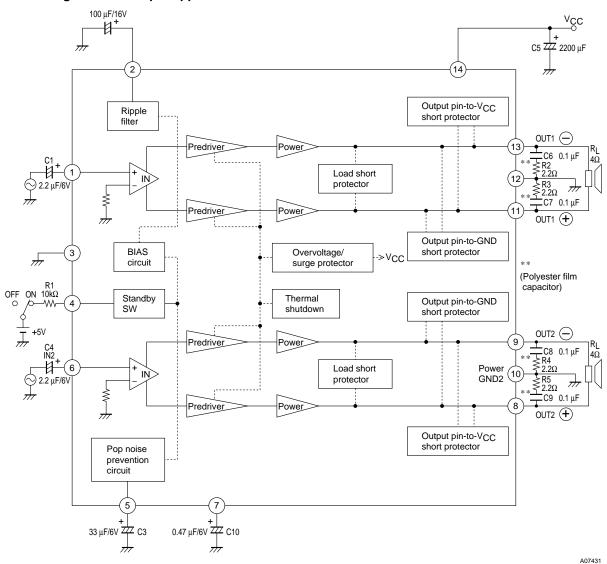
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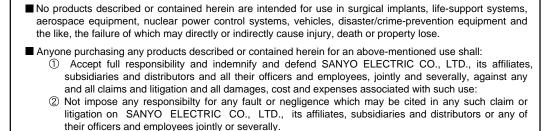
# Operating Characteristics Ta = 25 °C, V\_{CC} = 13.2 V, R<sub>L</sub> = 4 k $\Omega$ , f = 1 kHz, Rg = 600 $\Omega$

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Quiescent current	Icco	Rg=0	70	125	250	mA
Standby current	I <sub>ST</sub>			10	60	μΑ
Voltage gain	VG		38	40	42	dB
Total harmonic distortion	THD	P <sub>O</sub> =1 W		0.06	0.2	%
Output power	P <sub>O1</sub>	R <sub>L</sub> =4 Ω, THD=10 %, V <sub>CC</sub> =13.2 V	16	20		W
	P <sub>O2</sub>	R <sub>L</sub> =4 Ω, THD=10 %, V <sub>CC</sub> =14.4 V		25		W
	P <sub>O3</sub>	R <sub>L</sub> =4 Ω, THD=30 %, V <sub>CC</sub> =14.4 V		35		W
Output offset voltage	V <sub>N offset</sub>	Rg=0	-300		+300	mV
Output noise voltage	V <sub>NO</sub>	Rg=0, B.P.F.=20 Hz to 20 kHz		0.1	0.5	mVrms
Ripple rejection ratio	SVRR	Rg=0, f <sub>R</sub> =100 Hz, V <sub>R</sub> =0 dBm	40	50		dB
Channel separation	Chsep	Rg=10 kΩ, V <sub>O</sub> =0 dBm	50	60		dB
Input resistance	Ri		21	30	39	kΩ
Standby pin applied voltage	Vst	Amp on, applied through 10 k $\Omega$	2.5		V <sub>CC</sub>	V





**Block Diagram and Sample Application Circuit** 



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