MF1 IC S70 01

Standard 4Kbyte card IC sawn wafer on UV-tape addendum

Rev. 3.1 — 18 April 2007 101731 Product data sheet PUBLIC

1. General description

The MF1 IC S70 01 is a contactless smart card IC designed for card IC coils following the MIFARE card IC coil design guide and is qualified to work properly in NXP reader environment, which is built according to NXP specification.

This specification describes electrical, physical and dimensional properties of wafers.

2. Ordering information

Table 1. Ordering information

| Type number | Package | | |
|-----------------|---------|-------------------|----------------|
| | Name | Description | Ordering Code |
| MF1ICS7001W/V9D | | Die on sawn wafer | 9352 774 53005 |

3. Mechanical specification

3.1 Wafer

• Diameter: 8"

• Thickness: 150 μ m \pm 15 μ m

• PGDW: 15601

PCM location: reticle area

3.2 Wafer backside

Material: Si

Treatment: ground and stress relieve

• Roughness: R_a max. 0.5 μm

R_t max. 5 μm

3.3 Chip dimensions

Chip size: 1.42 x 1.34 mm
 Scribe lines: x-line: 86.4 μm

y-line: 86.4 μm



3.4 Passivation

Type: sandwich structure
 Material: PSG / Nitride(on top)
 Thickness: 500 nm / 600 nm

3.5 Bond pads

• Pad size:

 $\begin{array}{lll} \textbf{-} & \text{LA,LB} & & 118 \times 118 \ \mu\text{m}^1 \\ \textbf{-} & \text{TESTIO} & & 95 \times 110 \ \mu\text{m}^2 \\ \textbf{-} & \text{VSS} & & 108 \times 108 \ \mu\text{m}^2 \end{array}$

Material: Al-CuThickness: 0.85 μm

Remark: Substrate is connected to VSS.

^{1.}Passivation window: 90 x 90 µm

^{2.} Pads VSS and TESTIO are disconnected when wafer is sawn.

Limiting values

Limiting values[1][2][3] Table 2.

In accordance with the Absolute Maximum Rating System (IEC 134)

| Symbol | Parameter | Min | Max | Unit |
|------------------|---|-------|------|------|
| I _{IN} | Input Current | - | 30 | mA |
| P _{tot} | Total power dissipation per package | - | 200 | mW |
| T _{stg} | Storage temperature range | -55 | +125 | °C |
| T _{amb} | Operating temperature | -25 | 70 | °C |
| V _{ESD} | electrostatic discharge voltage LA-LB [4] | 2 | | kV |
| I _{LU} | Latch-up current | ± 100 | | mA |

- [1] Stresses above one or more of the limiting values may cause permanent damage to the device
- [2] These are stress ratings only. Operation of the device at these or any other conditions above those given in the Characteristics section of the specification is not implied
- [3] Exposure to limiting values for extended periods may affect device reliability
- MIL Standard 883-C method 3015; Human body model: C = 100 pF, R = 1.5 kW

Characteristics 5.

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Electrical characteristics [1][2][3] Table 3.

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---|--------------------------------|-----------------|-------|------|--------|
| f_{IN} | input frequency | | - | 13.56 | - | MHz |
| C _{IN} | Input capacitance (LCR meter HP4258) | 22 °C, Cp-D, 13.56 MHz, 2 V | 14.4 | 16.1 | 17.4 | pF |
| t_{W} | EEPROM write time | | - | 2.9 | - | ms |
| t _{RET} | EEPROM data retention | | 10 | | | years |
| N_{WE} | EEPROM write endurance | | 10 ⁵ | | | cycles |

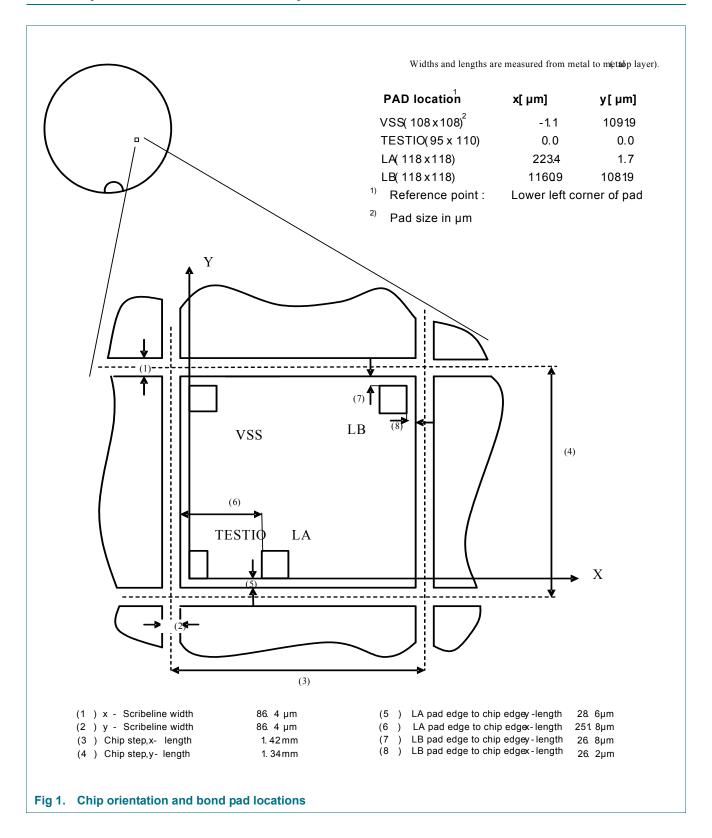
- [1] Stresses above one or more of the limiting values may cause permanent damage to the device
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- [3] Exposure to limiting values for extended periods may affect device reliability

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Chip orientation and bond pad locations



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7. Application information

References

- data sheet "general wafer specification for 8" wafers on uv-tape"
- data sheet "standard 4kbyte card ic mf1 ic s70 functional specification"
- product qualification package "standard card ic mf1 ic s70 01"
- application note "mifare, card ic coil design guide"

Revision history

Table 4. **Revision history**

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| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|---|--------------------|---------------|--------------|
| 101730 | August 2004 | Initial version | | Revision 3.0 |
| 101731 | 18 April 2007 | Product data sheet | | Revision 3.1 |
| Modifications: | The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. | | | |
| | Legal texts have been adapted to the new company name. | | | |

10. Legal information

10.1 Data sheet status

| Document status[1][2] | Product status[3] | Definition |
|--------------------------------|-------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com

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