

# Memory Module Specification

## KVR16X72V84-50EG

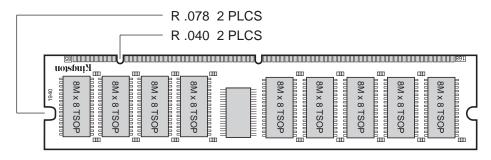
## **EDO Memory Module Kit**

### **DESCRIPTION:**

This document describes ValueRAM's 16M x 72-bit (128MB) 168-Pin DIMM (Dual In-line Memory Module). The components on this module include eighteen EDO (Extended Data Output) 8M x 8-bit DRAM (4K Refresh) in TSOP packages and two 16-bit drivers in SSOP packages. This 50ns DIMM is a +3.3 volt design with gold contact fingers. The electrical and mechanical specifications are as follows:

#### **FEATURES:**

☑ Timing Parameter	50ns
☑ RAS Access Time (trac)	50ns
☑ Column Address Access Time (table)	25ns
☑ Cycle Time (t <sub>RC</sub> )	84ns
☑ Hyper Page Cycle Time (thrc)	20ns
☑ Pulse Width (tras)	50ns (min.)
	10,000ns (max.)
☑ Single Power Supply of	+3.3V (+/- 0.3V)
☑ Low Power	4.485 W (operating)
	59.5 mW (max. standby)
☑ UL Rating	94 V - 0



Note: All measurements are in decimal inches.

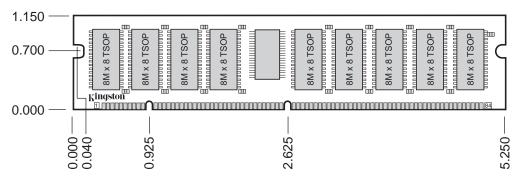


Figure 1: KVR16X72V84-50EG