

KVR133X64C2/128 128MB 16M x 64-Bit PC133 CL2 168-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 16M x 64-bit (128MB) PC133 CL2 SDRAM (Synchronous DRAM) memory module. The components on this module include four 16M x 16-bit (4M x 16-bit x 4 Bank) PC133 SDRAM in TSOP packages. This 168-pin DIMM uses gold contact fingers and requires +3.3V. The electrical and mechanical specifications are as follows:

FEATURES:

<input checked="" type="checkbox"/> CLK Cycle Time (tCC)	min. = 7.5ns (CAS Latency = 2) / max. = 1000ns
<input checked="" type="checkbox"/> Row Cycle Time (tRC)	60ns
<input checked="" type="checkbox"/> CLK to Output (tSAC)	max. = 5.4ns (CAS Latency = 2)
<input checked="" type="checkbox"/> Single Power Supply of	+3.3V (+/- .3V)
<input checked="" type="checkbox"/> Power	1.188 W (operating)
<input checked="" type="checkbox"/> UL Rating	94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.375" (34.92mm).

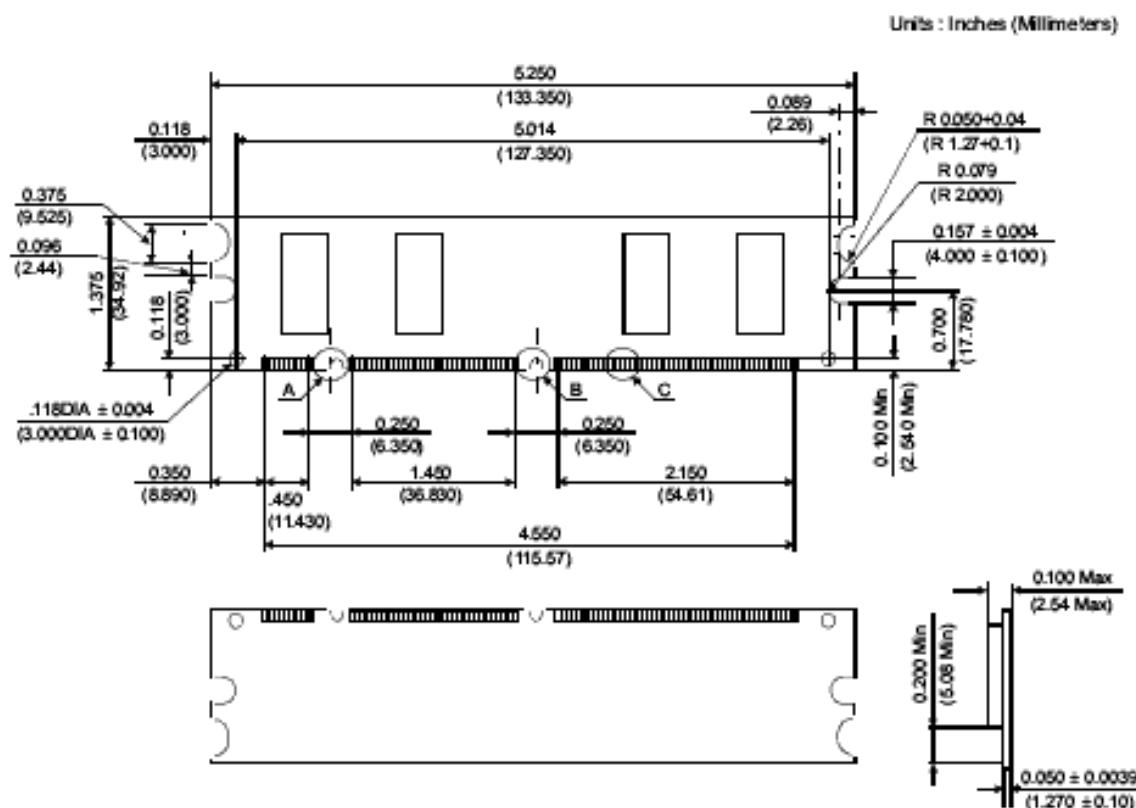


Figure 1: KVR133X64C2/128 168-Pin DIMM