



MAAPSS0095 V1

RF Power Amplifier for RFID 850 - 960 MHz

Features

- Single Supply Voltage
- Covers International RFID Band

902 - 928 MHz, US

865 - 868 MHz, EU

948 - 958 MHz, Japan

- High Gain 3 Stage HBT Design
- High Efficiency: 50 %
- Lead-Free 3 mm 16-Lead PQFN Package
- 100% Matte Tin Plating
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Re-flow Compatible

Description

M/A-COM's MAAPSS0095 is a high power three stage HBT power amplifier tuned to operate in the RFID bands in the US, the EU and Japan.

The MAAPSS0095 can also be used in other applications where high power and high efficiency are required, such as remote meter reading, electronic vehicle toll systems and ISM applications at 900 MHz. The high power and efficiency at 3.3 V make the MAAPSS0095 ideal for mobile applications.

The MAAPSS0095 is packaged in a lead-free 3 mm, 16-lead PQFN package and includes bias networks that can be controlled by analog signals to switch from active to standby modes.

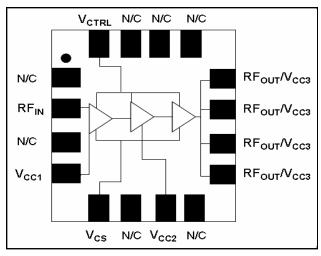
M/A-COM fabricates the MAAPSS0095 using a proprietary InGaP/GaAs HBT process for improved performance over temperature, higher reliability, and improved manufacturability.

Ordering Information¹

Part Number	Package
MAAPSS0095TR	1000 piece reel
MAAPSS0095TR-3000	3000 piece reel
MAAPSS0095SMB	Sample Test Board

1. Reference Application Note M513 for reel size information.

Functional Block Diagram



Pin Configuration

Pin	Pin Name	Description	
1	N/C	No Connection	
2	RF _{IN}	RF Input	
3	N/C	No Connection	
4	V _{CC1}	1st Stage Supply	
5	V _{cs}	Bias Supply Voltage	
6	N/C	No Connection	
7	V _{CC2}	2nd Stage Supply	
8	N/C	No Connection	
9	RF _{OUT} /V _{CC3}	RF Output, 3rd Stage Supply	
10	RF _{out} /V _{cc3}	RF Output, 3rd Stage Supply	
11	RF _{out} /V _{cc3}	RF Output, 3rd Stage Supply	
12	RF _{out} /V _{cc3}	RF Output, 3rd Stage Supply	
13	N/C	No Connection	
14	N/C	No Connection	
15	N/C	No Connection	
16	V _{CTRL}	Control Voltage	
17	Paddle ²	RF & DC Ground	

The exposed pad centered on the package bottom must be connected to RF and DC ground.

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^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.





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General Electrical Specifications 3 : $T_c = +25^{\circ}C$, $V_{cc} = +3.3 \text{ V}$, $P_{IN} = +5 \text{ dBm}$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Input impedance		Ohms	_	50	_
Input VSWR		Ratio	_	2.0:1	_
Control voltage range		V	0.2	_	2.7
Control voltage slope	0 ≤ P _{OUT} ≤ 32 dBm	dB/V	_	45	_
Duty cycle		%	_	100	
Off current	V _{CTRL1,2} = +0.2 V	μA	_	20	
Control current	V _{CTRL1,2} = +2.6 V, Pin = +5 dBm	mA	_	4	_

^{3.} Operating at or within these electrical bias conditions, at case temperatures up to 85°C, will ensure MTTF > 5 x 10⁶ hours.

Electrical Specifications: $T_c = +25$ °C, $Z_0 = 50 \Omega$, F = 960 MHz, $V_{CC} = 3.3 V$, $V_{CTRL} = 2.6 V$

Parameter	Test Conditions		Min.	Тур.	Max.
Output power	P _{IN} = +5 dBm	dBm	32.0	32.5	_
Power added efficiency	P _{IN} = +5 dBm	%	50	55	_
Input Return Loss	P _{IN} = -25 dBm	dB	_	12	_
Output Return Loss	P _{IN} = -25 dBm	dB	_	12	_
Harmonics	$P_{IN} = +5 \text{ dBm}$	dBc	_	-45	_
Small Signal Gain	P _{IN} = -25 dBm	dB	35	37	_
Current	$P_{IN} = -25 \text{ dBm}$	mA	100	180	250

Absolute Maximum Ratings

Parameter	Parameter Specifications		Specifications			
	Min	Тур	Max	Unit		
$V_{\text{CC1}}, V_{\text{CC2}}, V_{\text{CC3}}$ and V_{CS}	-0.5	_	6.0	V	No operation	
V _{CTRL1,2}	-0.5	_	3.8	V		
Input power	_	_	+8	dBm		
Operating duty cycle	_	_	100	%		
Operating temperature	-40	_	85	°C	Management of successful and	
Storage temperature	-20	_	125	°C	Measured at ground lead	

²

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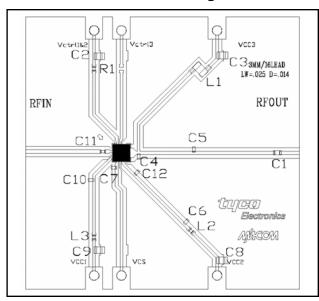




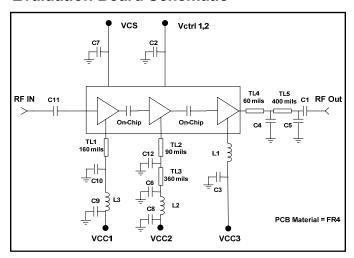
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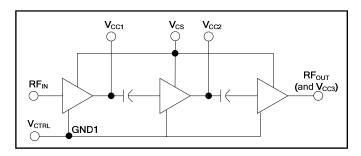
Recommended PCB Configuration



Evaluation Board Schematic



Functional Schematic



MAAPSS0095 External Parts List

Component	Value	Foot Print	Manufacturer		
C1, C7, C11	100 pF	0402	Murata		
C2, C3, C8, C9	3.3 µF	1208	Kemet		
C4	20 pF	603	ATC 600 S		
C5	8.2 pF	603	ATC 600 S		
C6, C10	1000 pF	0402	Murata		
C12	39 pF	603	ATC 600 S		
L1	5.6 nH	ı	Coilcraft 0906-5J		
L2, L3	22 nH	0402	Toko		
R1	0 Ohms	0402	-		
TL1	50 Ω, 8° @ 900 MHz				
TL2	50 Ω, 4.4° @ 900 MHz				
TL3	50 Ω, 17.5° @ 900 MHz				
TL4	50 Ω, 2.9° @ 900 MHz				
TL5	50 Ω, 19.5° @ 900 MHz				

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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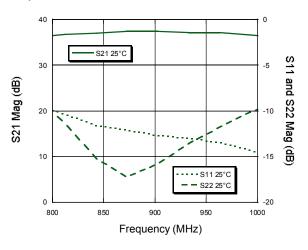


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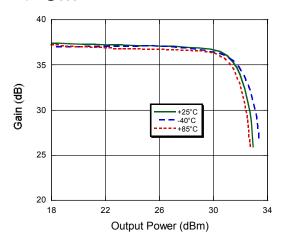
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Typical Performance Curves:

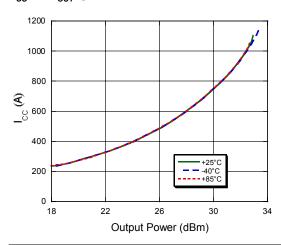
S11, S21 and S22



P1dB @ 900 MHz

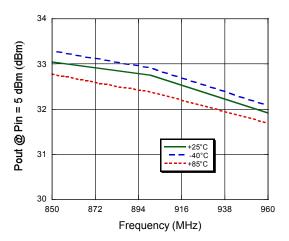


I_{CC} vs. P_{OUT} @ 900 MHz

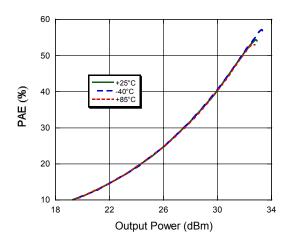


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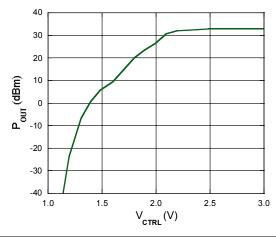
P_{OUT} @ Pin = 5 dBm



PAE @ 900 MHz



P_{OUT} vs. V_{CTRL} @ 900 MHz



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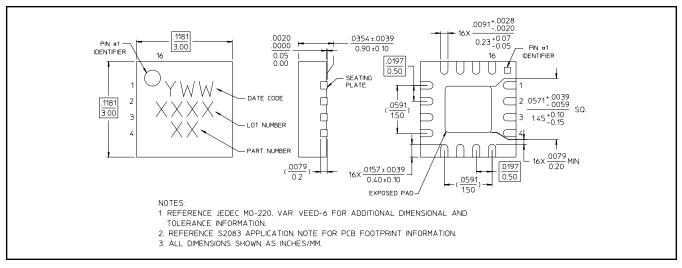




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Lead Free 3 mm 16-Lead PQFN †



[†] Reference Application Note M538 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements.

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