# Dual N-Channel 100-V (D-S) MOSFET

## **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

## **Typical Applications:**

- PoE Power Sourcing Equipment
- PoE Powered Devices
- Telecom DC/DC converters
- White LED boost converters

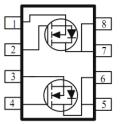
ESD Protected 2000V	

PRODUCT SUMMARY				
Vds (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
100	81 @ V <sub>GS</sub> = 10V	4.2		
100	92 @ V <sub>GS</sub> = 4.5V	4.0		



FREE





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V <sub>DS</sub>	100	V	
Gate-Source Voltage		V <sub>GS</sub>	±20	v	
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C		4.2		
Continuous Drain Current	T <sub>A</sub> =70°C	I <sub>D</sub>	3.3	А	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	30		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	3	А	
Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	PD	2.1	W	
Power Dissipation <sup>a</sup>	T <sub>A</sub> =70°C	١D	1.3	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	R <sub>eja</sub>	62.5	°C/W	
	Steady State	ιν <sub>θ</sub> ja	110	C/W	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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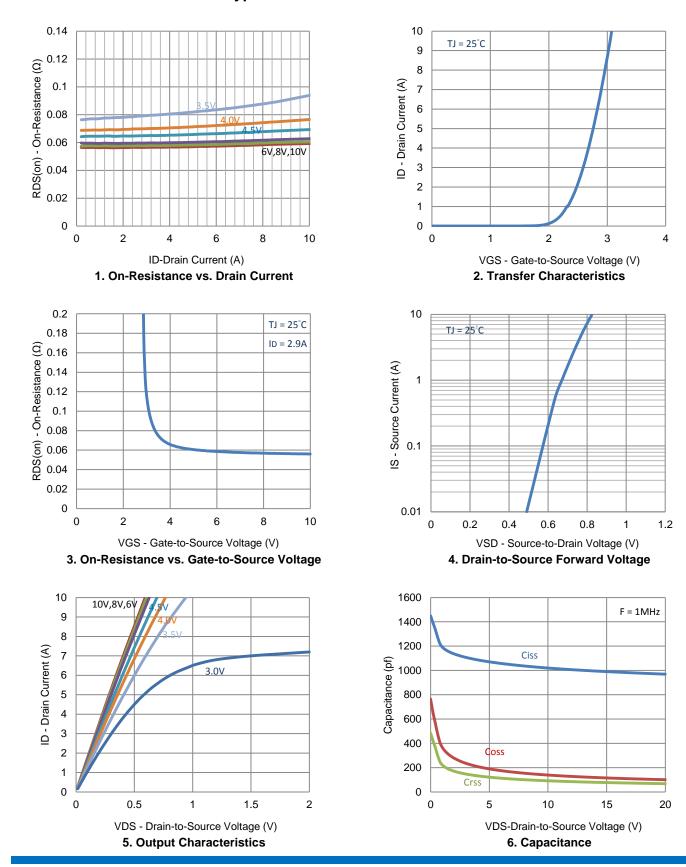
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1		3.5	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = 20 V$			±10	uA
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA I
	I <sub>DSS</sub>	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	20			А
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.9 \text{ A}$			81	mΩ
Dialit-Source Off-Resistance	r <sub>DS(on)</sub>	$V_{GS}$ = 4.5 V, $I_{D}$ = 2.6 A			92	11122
Forward Transconductance	<b>g</b> <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 2.9 \text{ A}$		20		S
Diode Forward Voltage	$V_{SD}$	$I_{S} = 1.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V
		Dynamic				
Total Gate Charge	Qg			17		
Gate-Source Charge	$Q_gs$	$V_{\text{DS}}$ = 50 V, $V_{\text{GS}}$ = 4.5 V, $I_{\text{D}}$ = 2.9 A		3.3		nC
Gate-Drain Charge	$Q_{gd}$			8.7		
Turn-On Delay Time	t <sub>d(on)</sub>			7		
Rise Time	t <sub>r</sub>	$V_{\text{DD}}$ = 50 V, $R_{\text{L}}$ = 15 $\Omega$ , $I_{\text{D}}$ = 2.9 A,		6.7		20
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GEN}$ = 10 V, $R_{GEN}$ = 6 $\Omega$		45		ns
Fall-Time	t <sub>f</sub>			23		
Input Capacitance	C <sub>iss</sub>			990		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		115		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			77		

### Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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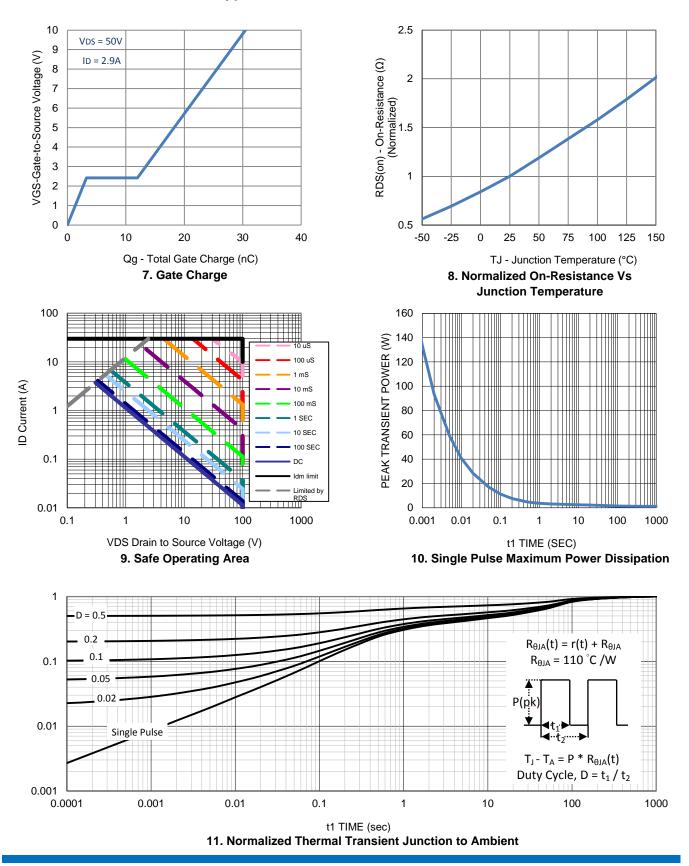


**Typical Electrical Characteristics** 

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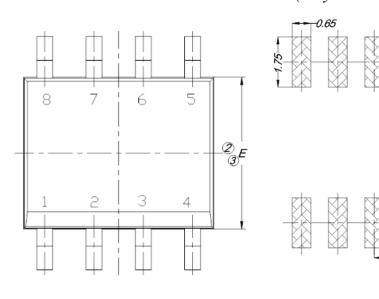
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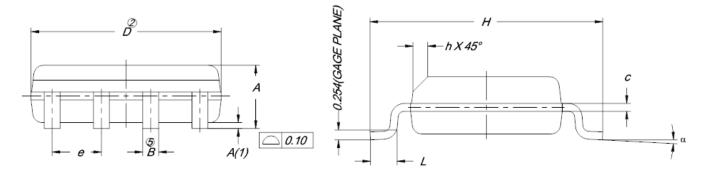
## **Package Information**

Land Pattern (Only for Reference)

5.60



	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
Α	1.35	1.55	1.75		
A(1)	0.10	0.18	0.25		
В	0.38	0.45	0.51		
С	0.19	0.22	0.25		
D	4.80	4.90	5.00		
E	3.80	3.90	4.00		
е	1.27 BSC				
н	5.80	6.00	6.20		
L	0.50	0.72	0.93		
α	0°	4°	8°		
h	0.25	0.38	0.50		



#### Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.

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