

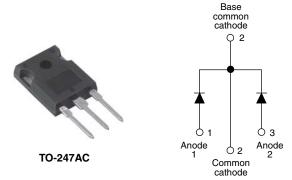


Vishay Semiconductors

HALOGEN

FREE

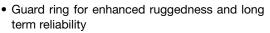
Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY					
Package	TO-247AC				
I _{F(AV)}	2 x 20 A				
V_{R}	15 V				
V _F at I _F	See Electrical table				
I _{RM} max.	600 mA at 100 °C				
T _J max.	125 °C				
Diode variation	Common cathode				
E _{AS}	10 mJ				

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance





- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



The VS-STPS40L15CW... center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	A		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	700	A		
V _F	19 Apk, T _J = 125 °C (per leg, typical)	0.25	V		
TJ		- 55 to 125	°C		

VOLTAGE RATINGS							
PARAMETER SYMBOL TEST CONDITIONS VS-STPS40L15CWPBF VS-STPS40L15CW-N3 UN							
Maximum DC reverse voltage	V_{R}	T. ₁ = 100 °C	15	15	V		
Maximum working peak reverse voltage	V_{RWM}	1j=100 C	15	13	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current per leg			20			
See fig. 5 per device	I _{F(AV)}	50 % duty cycle at T_C = 86 °C, rectangular waveform		40		
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and	700	Α	
non-repetitive surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	330		
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5 \text{mH}$		10	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	Α	

Revision: 11-Oct-11 **1** Document Number: 94331 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>



VS-STPS40L15CWPbF, VS-STPS40L15CW-N3

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS		
		19 A	T 05.00	-	0.41		
Maximum forward voltage drop per leg	V (1)	40 A	T _J = 25 °C	-	0.52		
See fig. 1	V _{FM} ⁽¹⁾	19 A	T 405.00	0.25	0.33	V	
		40 A	T _J = 125 °C	0.37	0.50		
Reverse leakage current per leg	ı (1)	T _J = 25 °C	V Data d V	-	10	mA	
See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C	V _R = Rated V _R	-	600	IIIA	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.182		82	V
Forward slope resistance	r _t			7.6		.6	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	-	2000	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8	-	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	TJ		- 55 to 125	°C		
Maximum storage temperature range	T _{Stg}		- 55 to 150	3C		
Maximum thermal resistance, junction to case per leg	D	DC operation See fig. 4	1.4			
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	0.7	°C/W		
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.24			
Approximate weight			6	g		
Approximate weight			0.21	OZ.		
Mounting torque	m	Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque maximu	m	ivon-lubricated infeads	12 (10)	(lbf \cdot in)		
Marking device		Case style TO-247AC (JEDEC)	STPS40L15CW			

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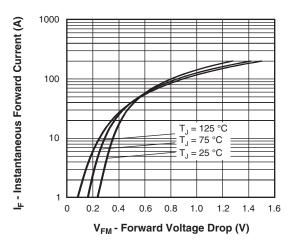


Fig. 1 - Maximum Forward Voltage Drop Characteristics

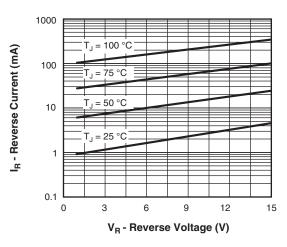


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

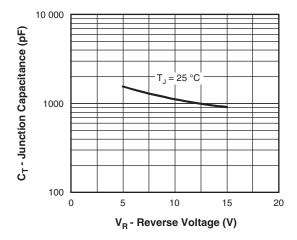


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

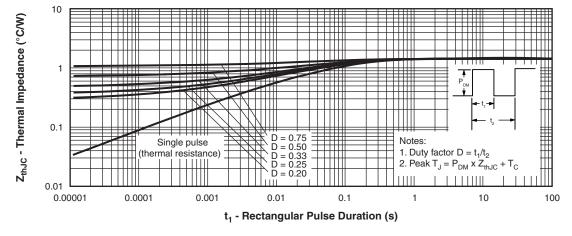


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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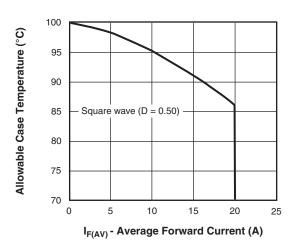


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

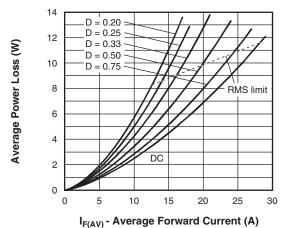


Fig. 6 - Forward Power Loss Characteristics

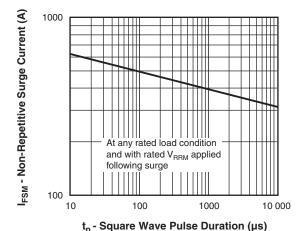


Fig. 7 - Maximum Non-Repetitive Surge Current

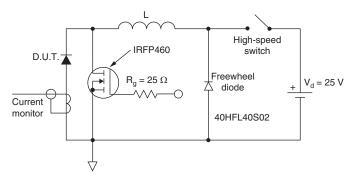


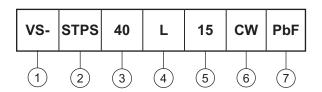
Fig. 8 - Unclamped Inductive Test Circuit

VS-STPS40L15CWPbF, VS-STPS40L15CW-N3

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ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- Schottky STPS series
- Current ratings (40 = 40 A)
- L = Low forward voltage
- Voltage code (15 = 15 V)
- 6 Package:

CW = TO-247

- 7 Environmental digit
 - PbF = Lead (Pb)-free and RoHS compliant
 - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

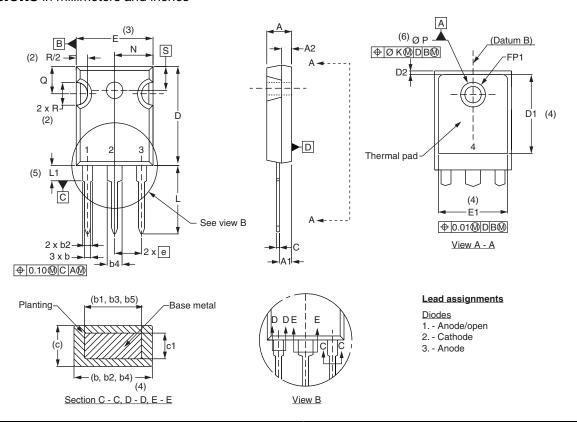
ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-STPS40L15CWPbF	25	500	Antistatic plastic tube				
VS-STPS40L15CW-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Dout moulcing information	TO-247AC PbF	www.vishay.com/doc?95226			
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007			



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DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.54		0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	BSC	0.3		
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217	'BSC	

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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