

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P1-P3-P2) : (J3-J6)  
 (P7-P6-P8) : (J1-J2)

: 1CT : 1CT ± 3%  
 : 1CT : 1CT ± 3%

2.0 INDUCTANCE (P7-P8)  
 (P1-P2)

: 350uH MIN. @ 0.1V , 100KHz, 8mA DC Bias  
 : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE P1-P3-P2 (WITH J6 AND J3 SHORT)  
 P7-P6-P8 (WITH J2 AND J1 SHORT)

: 0.3uH MAX. @ 1MHz  
 : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE (P1,P3,P2) TO (J6,J3)  
 (P8,P6,P7) TO (J2,J1)

: 30pf TYP @ 1MHz  
 : 30pf TYP @ 1MHz

5.0 DC RESISTANCE (J6-J3)=(J2-J1)

: 1.2 ohms Max.

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 ALL RESISTORS ARE ±5% TOLERANCE.

		InNet Technologies, Inc. <a href="http://www.innet-tech.com">http://www.innet-tech.com</a>	
		Stewart Connector Systems <a href="http://www.stewartconnector.com">http://www.stewartconnector.com</a>	
SHEET 1 OF 4	DRAWING NO.	SI-70013	REV X

6.0 RETURN LOSS: (P7-P8)=100 OHMS AND (P1-P2)=100 OHM REF.  
 1MHz TO 30MHz : -18dB MIN.  
 30MHz TO 60MHz : -(19-20 LOG (f/30MHz))  
 60MHz TO 80MHz : -12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).



7.0 VOLTAGE WITHSTAND:  
 (J1, J2) TO (P7, P8) : 1500 VAC  
 (J3, J6) TO (P1,P2) : 1500 VAC

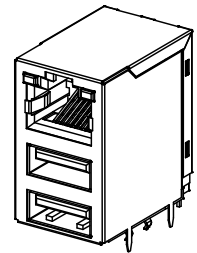
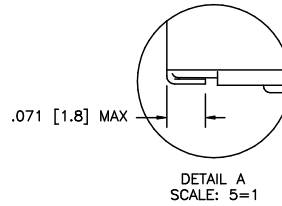
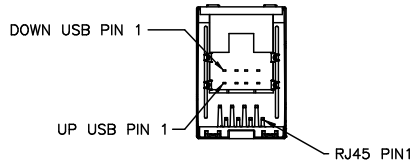
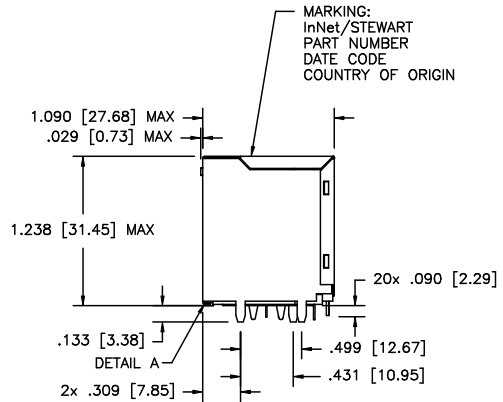
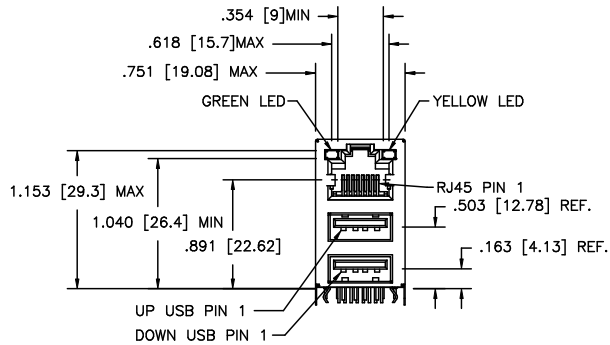
8.0 INSERTION LOSS: RS=RL=100 ohms  
 1-65MHz -1 dB MAX

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS  
 OUTPUT VOLTAGE = 1 V peak 3.0 nS MAX  
 PULSE WIDTH= 112nS 3.0 nS MAX

10.0 CROSS TALK:  
 1-65MHz -35 dB MIN

11.0 COMMON TO COMMON MODE ATTENUATION:  
 30MHz TO 100MHz -30dB MAX  
 100MHz TO 130MHz -20dB MAX



		InNet Technologies, Inc. <a href="http://www.innet-tech.com">http://www.innet-tech.com</a>
		Stewart Connector Systems <a href="http://www.stewartconnector.com">http://www.stewartconnector.com</a>
SHEET 2 OF 4	DRAWING NO. SI-70013	REV X

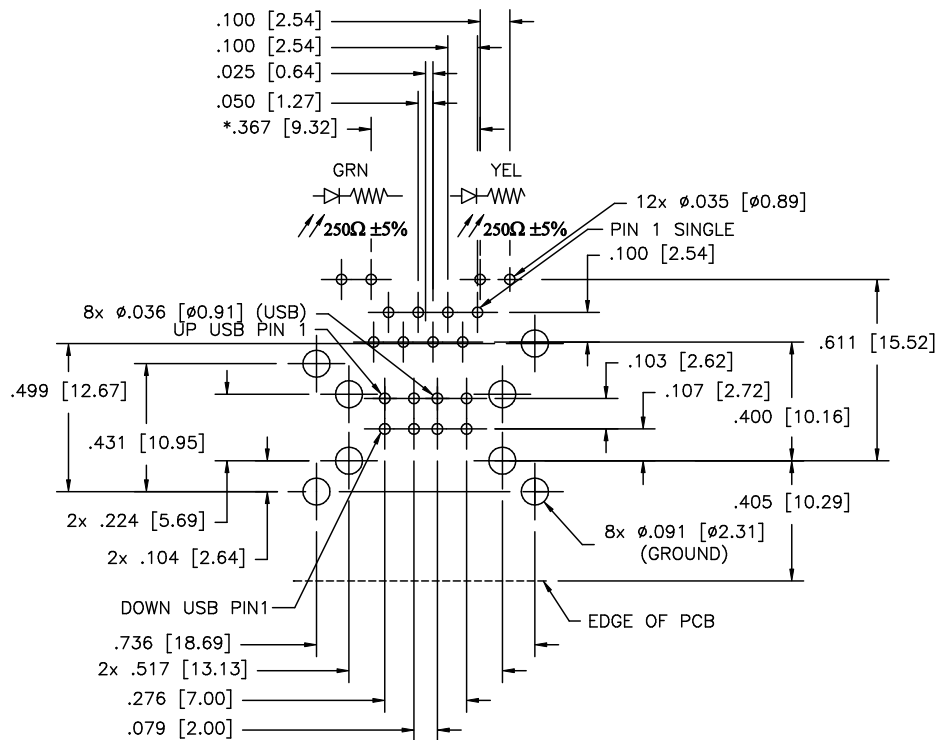


NOTES:

- 1.0 TOLERANCE COMPLY WITH FCC DIMENSION REQUIREMENTS.
- 2.0 PIN NOT ELECTRICALLY CONNECTED MAY BE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3.0 MATERIAL:  
 PLASTIC: UL94V-0  
 TERMINAL USB: PdNi AT 30 MICRINCHES  
 TERMINAL RJ45: 50 MICRINCHES Au OVER 50 MICRINCHES Ni OVER PHOSBRONZE SHIELD, BOTH USB AND RJ45: BRASS PLATED W/EITHER Ni OR TIN-LEAD
- 4.0 GENERAL TOLERANCE: ±.005 [.127]



PRELIMINARY  
DRAWING

		<b>InNet Technologies, Inc.</b> <a href="http://www.innet-tech.com">http://www.innet-tech.com</a>	
		<b>Stewart Connector Systems</b> <a href="http://www.stewartconnector.com">http://www.stewartconnector.com</a>	
SHEET	DRAWING NO.	REV	
3 OF 4	SI-70013	X6	



PC BOARD LAYOUT COMPONENT SIDE SHOWN  
 TOLERANCE: ±.003 [.08] UNLESS OTHERWISE SPECIFIED

PRELIMINARY  
 DRAWING

 <b>InNet Technologies, Inc.</b> <a href="http://www.innet-tech.com">http://www.innet-tech.com</a>	
 <b>Stewart Connector Systems</b> <a href="http://www.stewartconnector.com">http://www.stewartconnector.com</a>	
SHEET 4 OF 4	DRAWING NO. SI-70013 REV X6