

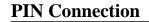
**NPN Silicon Transistor** 

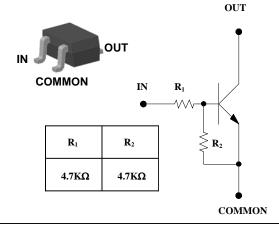
#### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density





### **Ordering Information**

Type NO.	Marking	Package Code
SRC1201E	<u>R1</u> ① ②	SOT-523
	1 Device Code 2 Year&Week Code	

### Absolute Maximum Ratings

Absolute Maximum Ratings		(Ta=25°C)		
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	50	V	
Input voltage	Vı	20,-10	V	
Output current	Ι <sub>ο</sub>	100	mA	
Power dissipation	P <sub>D</sub>	150	mW	
Junction temperature	TJ	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C	

#### **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = 50V, V_1 = 0$	-	I	500	nA
DC current gain	Gı	$V_0 = 5V$ , $I_0 = 10mA$	30	55	-	1
Output voltage	V <sub>O(ON)</sub>	I <sub>0</sub> =10mA, I <sub>1</sub> =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	$V_0 = 0.2V$ , $I_0 = 5mA$	-	1.5	2.0	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	$V_0 = 5V$ , $I_0 = 0.1mA$	1.0	1.2	-	V
Transition frequency	f <sub>T</sub> *	$V_{CE}$ =10V, $I_{C}$ =5mA, f=1MHz	-	200	-	MHz
Input current	I <sub>1</sub>	$V_1 = 5V, I_0 = 0$	-	-	1.8	mA
Input resistor (Input to base)	R <sub>1</sub>	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R <sub>2</sub>	-	3.3	4.7	6.1	KΩ

\* : Characteristic of transistor only

### **Electrical Characteristic Curves**

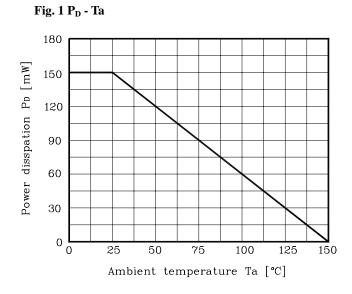
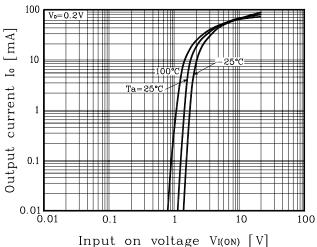


Fig. 3  $I_O$  -  $V_{I(OFF)}$ 

Fig. 2 I<sub>O</sub> - V<sub>I(ON)</sub>



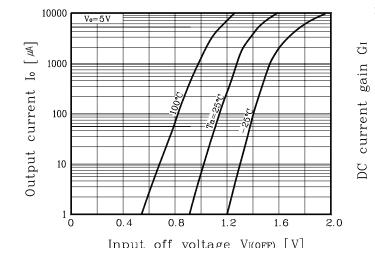
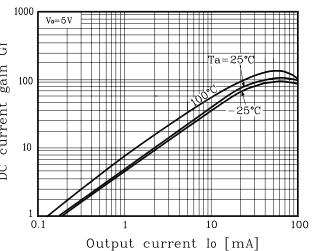
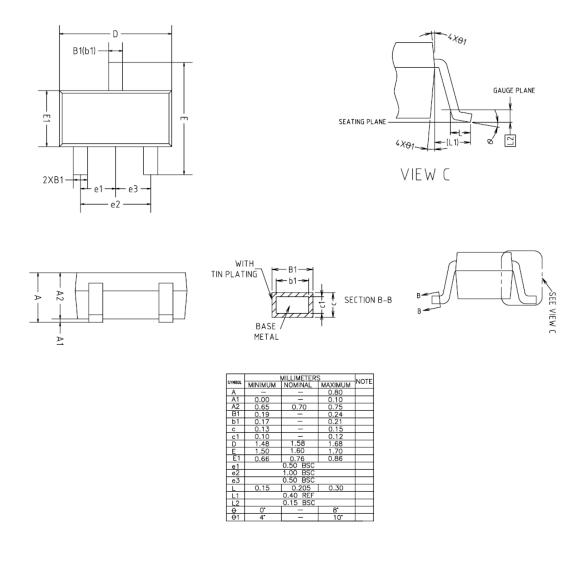


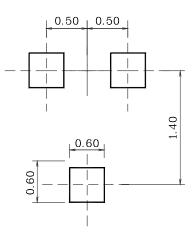
Fig. 4 G<sub>1</sub> - I<sub>0</sub>



### **Outline Dimension**



#### \*Recommend PCB solder land [Unit: mm]



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