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Shunt Regulator



ADE-204-049A (Z)

Rev.1 Sep. 2002

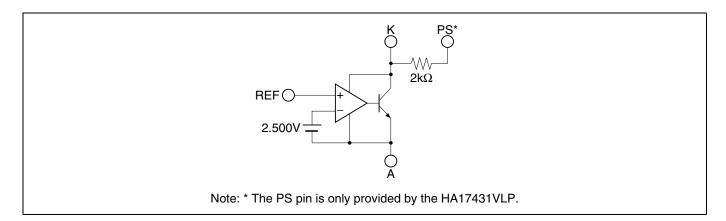
Description

The HA17431 series is temperature-compensated variable shunt regulators. The main application of these products is in voltage regulators that provide a variable output voltage. The on-chip high-precision reference voltage source can provide $\pm 1\%$ accuracy in the V versions, which have a V_{KA} max of 16 volts. The HA17431VLP, which is provided in the MPAK-5 package, is designed for use in switching mode power supplies. It provides a built-in photocoupler bypass resistor for the PS pin, and an error amplifier can be easily constructed on the supply side.

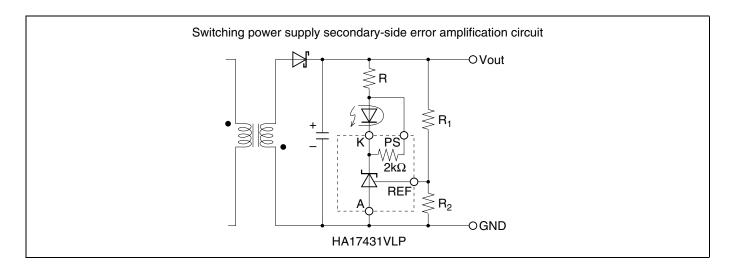
Features

- The V versions provide $2.500 \text{ V} \pm 1\%$ at Ta = 25°C
- The HA17431VLP includes a photocoupler bypass resistor (2 k Ω)
- The reference voltage has a low temperature coefficient
- The MPAK-5(5-pin), MPAK(3-pin) and UPAK miniature packages are optimal for use on high mounting density circuit boards
- Car use is provided

Block Diagram



Application Circuit Example



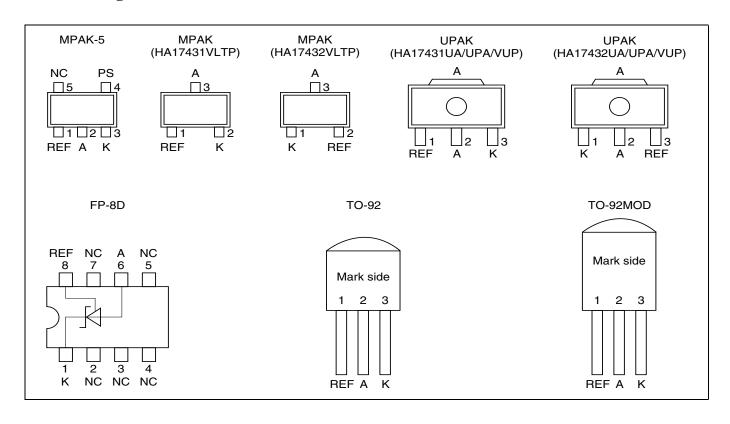
Ordering Information

		Version				Operating
Item		V Version	A Version	Normal Version	Package	Temperature Range
Reference	Accuracy	±1%	±2.2%	±4%		
voltage (at 25°C)	Max	2.525 V	2.550 V	2.595 V	-	
(at 25 0)	Тур	2.500 V	2.495 V	2.495 V	-	
	Min	2.475 V	2.440 V	2.395 V	-	
Cathode vol	tage	16 V max	40 V max	40 V max	-	
Cathode cur	rent	50 mA max	150 mA max	150 mA max	-	
Car use		HA17431VPJ			TO-92	−40 to +85°C
			HA17431PNAJ		-	
			HA17431PAJ		TO-92MOD	-
				HA17431PJ	-	
			HA17431FPAJ		FP-8D	-
				HA17431FPJ	-	
				•		

Ordering Information (cont.)

	Version		Operating			
Item	V Version	A Version	Normal Version	Package	Temperature Range	
Industrial use	HA17431VLTP		MPAK	−20 to +85°C		
	HA17432VLTP		_			
	HA17431VLP			MPAK-5	_	
	HA17431VP			TO-92	_	
		HA17431PNA	_			
	HA17431VUP			UPAK	_	
		HA17431UPA		_		
	HA17432VUP			-		
		HA17432UPA		_		
		HA17431PA		TO-92MOD	_	
	-		HA17431P	-		
		HA17431FPA		FP-8D	_	
			HA17431FP	_		
Commercial use		HA17431UA		UPAK	_	
		HA17432UA		_		
			-	-0		

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	HA17431VLP	HA17431VP	HA17431VPJ	Unit	Notes
Cathode voltage	$V_{\kappa A}$	16	16	16	V	1
PS term. voltage	V _{PS}	V _{KA} to 16	_	_	V	1,2,3
Continuous cathode current	I _K	–50 to +50	-50 to +50	-50 to +50	mA	
Reference input current	Iref	-0.05 to +10	-0.05 to +10	-0.05 to +10	mA	
Power dissipation	P _T	150 *4	500 * ⁵	500 * ⁵	mW	4, 5
Operating temperature range	Topr	-20 to +85	-20 to +85	-40 to +85	°C	
Storage temperature	Tstg	–55 to +150	-55 to +150	-55 to +150		°C

Item	Symbol	HA17431VUP/HA17432VUP	HA17431VLTP/HA17432VLTP	Unit	Notes
Cathode voltage	$V_{\kappa A}$	16	16	V	1
PS term. voltage	V _{PS}	_	_	V	1,2,3
Continuous cathode current	I _K	-50 to +50	–50 to +50	mA	
Reference input current	Iref	-0.05 to +10	-0.05 to +10	mA	
Power dissipation	P _T	800 *8	150 * ⁴	mW	4, 8
Operating temperature range	Topr	-20 to +85	–20 to +85	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Item	Symbol	HA17431PNA	HA17431P/PA	HA17431FP/FPA	HA17431UA/UPA/ HA17432UA/UPA	Unit	Notes
Cathode voltage	V _{KA}	40	40	40	40	V	1
Continuous cathode current	I _K	-100 to +150	-100 to +150	-100 to +150	-100 to +150	mA	
Reference input current	Iref	-0.05 to +10	-0.05 to +10	-0.05 to +10	-0.05 to +10	mA	
Power dissipation	P _T	500 * ⁵	800 *6	500 * ⁷	800 *8	mW	5,6,7,8
Operating temperature range	Topr	-20 to +85	-20 to +85	-20 to +85	-20 to +85	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	-55 to +125	-55 to +150	°C	

Absolute Maximum Ratings (cont.)

 $(Ta = 25^{\circ}C)$

Item	Symbol	HA17431PNAJ	HA17431PJ/PAJ	HA17431FPJ/FPAJ	Unit	Notes
Cathode voltage	$V_{\kappa A}$	40	40	40	V	1
Continuous cathode current	I _K	-100 to +150	-100 to +150	-100 to +150	mA	
Reference input current	Iref	-0.05 to +10	-0.05 to +10	-0.05 to +10	mA	
Power dissipation	P _T	500 * ⁵	800 *6	500 * ⁷	mW	5,6,7
Operating temperature range	Topr	-40 to +85	-40 to +85	-40 to +85	°C	
Storage temperature	Tstg	–55 to +150	-55 to +150	-55 to +125	°C	

Notes: 1. Voltages are referenced to anode.

- 2. The PS pin is only provided by the HA17431VLP.
- 3. The PS pin voltage must not fall below the cathode voltage. If the PS pin is not used, the PS pin is recommended to be connected with the cathode.
- 4. Ta \leq 25°C. If Ta > 25°C, derate by 1.2 mW/°C.
- 5. Ta \leq 25°C. If Ta > 25°C, derate by 4.0 mW/°C.
- 6. Ta \leq 25°C. If Ta > 25°C, derate by 6.4 mW/°C.
- 7. 50 mm \times 50 mm \times 1.5mmt glass epoxy board(5% wiring density), Ta \leq 25°C. If Ta > 25°C, derate by 5 mW/°C.
- 8. 15 mm \times 25 mm \times 0.7mmt alumina ceramic board,Ta \leq 25°C. If Ta > 25°C, derate by 6.4 mW/°C.



Electrical Characteristics

$HA17431VLP/VP/VPJ/VUP/VLTP,\,HA17432VUP/VLTP$

 $(Ta = 25^{\circ}C, I_{K} = 10 \text{ mA})$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions	Notes
Reference voltage	Vref	2.475	2.500	2.525	V	V _{KA} = Vref	
Reference voltage temperature deviation	Vref(dev)	_	10	_	mV	$V_{KA} = Vref,$ $Ta = -20^{\circ}C \text{ to } +85^{\circ}C$	1
Reference voltage temperature coefficient	∆Vref/∆Ta	_	±30	_	ppm/°C	V _{KA} = Vref, 0°C to 50°C gradient	
Reference voltage regulation	$\Delta V ref / \Delta V_{_{KA}}$	_	2.0	3.7	mV/V	V _{KA} = Vref to 16 V	
Reference input current	Iref	_	2	6	μΑ	$R_1 = 10 \text{ k}\Omega, R_2 = \infty$	
Reference current temperature deviation	Iref(dev)	_	0.5	_	μΑ	$R_1 = 10 \text{ k}\Omega, R_2 = \infty,$ $Ta = -20^{\circ}\text{C to } +85^{\circ}\text{C}$	
Minimum cathode current	lmin	_	0.4	1.0	mA	V _{KA} = Vref	2
Off state cathode current	loff	_	0.001	1.0	μА	V _{KA} = 16 V, Vref = 0 V	
Dynamic impedance	Z _{KA}	_	0.2	0.5	Ω	$V_{KA} = Vref,$ $I_{K} = 1 \text{ mA to } 50 \text{ mA}$	
Bypass resistance	$R_{\scriptscriptstyle{PS}}$	1.6	2.0	2.4	kΩ	$I_{PS} = 1 \text{ mA}$	3
Bypass resistance temperature coefficient	$\Delta R_{PS}/\Delta Ta$	_	+2000	_	ppm/°C	I _{PS} = 1 mA, 0°C to 50°C gradient	3

RENESAS

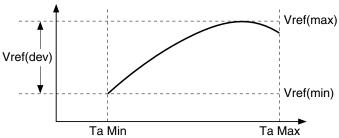
Electrical Characteristics (cont.)

HA17431PJ/PAJ/FPJ/FPAJ/P/PA/UA/UPA/FP/FPA/PNA/PNAJ, HA17432UA/UPA

 $(Ta = 25^{\circ}C, I_{K} = 10 \text{ mA})$

Item	Symbol	Min	Тур	Max	Unit	Test Cond	itions	Notes
Reference voltage	Vref	2.440	2.495	2.550	V	V _{KA} = Vref		Α
		2.395	2.495	2.595	_			Normal
Reference voltage temperature deviation	Vref(dev)	<u> </u>	11	(30)	mV	V _{KA} = Vref	Ta = -20°C to +85°C	1, 4
		_	5	(17)	_		Ta = 0°C to +70°C	1, 4
Reference voltage	$\Delta V ref/\Delta V_{_{KA}}$	_	1.4	3.7	mV/V	V _{KA} = Vref to 10 V		
regulation		_	1	2.2	_	$V_{KA} = 10 \text{ V}$	to 40 V	
Reference input current	Iref	_	3.8	6	μΑ	$R_{_1} = 10 \text{ k}\Omega$, R ₂ = ∞	
Reference current temperature deviation	Iref(dev)	_	0.5	(2.5)	μΑ	$R_1 = 10 \text{ k}\Omega$ $Ta = 0^{\circ}\text{C to}$		4
Minimum cathode current	Imin	_	0.4	1.0	mA	$V_{KA} = Vref$		2
Off state cathode current	loff	_	0.001	1.0	μΑ	$V_{KA} = 40 V,$	Vref = 0 V	
Dynamic impedance	Z _{KA}	_	0.2	0.5	Ω	$V_{KA} = Vref,$ $I_{K} = 1 mA to$	o 100 mA	

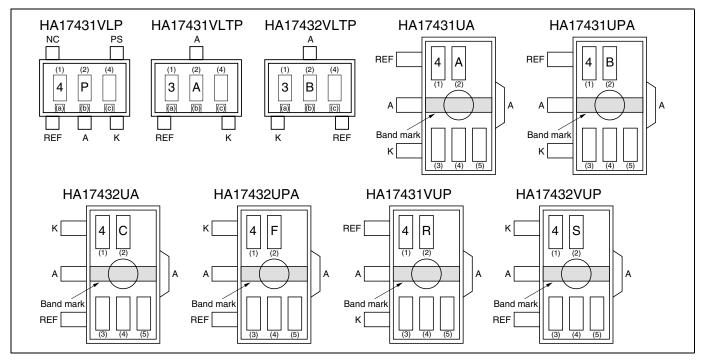
Notes: 1. Vref(dev) = Vref(max) - Vref(min)



- 2. Imin is given by the cathode current at $Vref = Vref_{(IK=10mA)} 15 \text{ mV}.$
- 3. R_{PS} is only provided in HA17431VLP.
- 4. The maximum value is a design value (not measured).

MPAK-5(5-pin), MPAK(3-pin) and UPAK Marking Patterns

The marking patterns shown below are used on MPAK-5, MPAK and UPAK products. Note that the product code and mark pattern are different. The pattern is laser-printed.



Notes: 1. Boxes (1) to (5) in the figures show the position of the letters or numerals, and are not actually marked on the package.

2. The letters (1) and (2) show the product specific mark pattern.

Product	(1)	(2)	
HA17431VLP	4	Р	
HA17431VUP	4	R	
HA17432VUP	4	S	
HA17431VLTP	3	Α	
HA17432VLTP	3	В	
HA17431UA	4	А	
HA17431UPA	4	В	
HA17432UA	4	С	
HA17432UPA	4	F	

- 3. The letter (3) shows the production year code (the last digit of the year) for UPAK products.
- 4. The bars (a), (b) and (c) show a production year code for MPAK-5 and MPAK products as shown below. After 2010 the code is repeated every 8 years.

Year	2002	2003	2004	2005	2006	2007	2008	2009
(a)	None	None	None	Bar	Bar	Bar	Bar	None
(b)	None	Bar	Bar	None	None	Bar	Bar	None
(c)	Bar	None	Bar	None	Bar	None	Bar	None

5. The letter (4) shows the production month code (see table below).

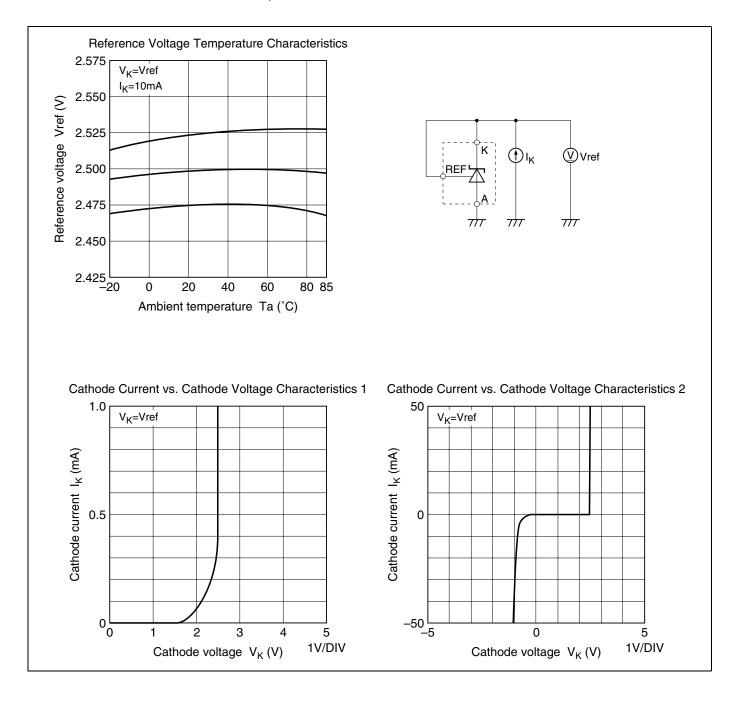
Production month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Marked code	Α	В	С	D	Е	F	G	Н	J	K	L	М

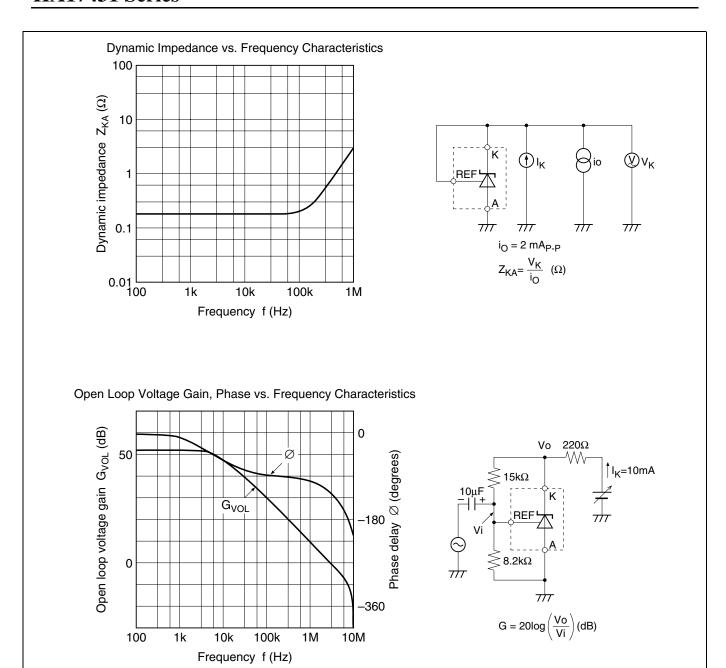
6. The letter (5) shows manufacturing code. For UPAK products.



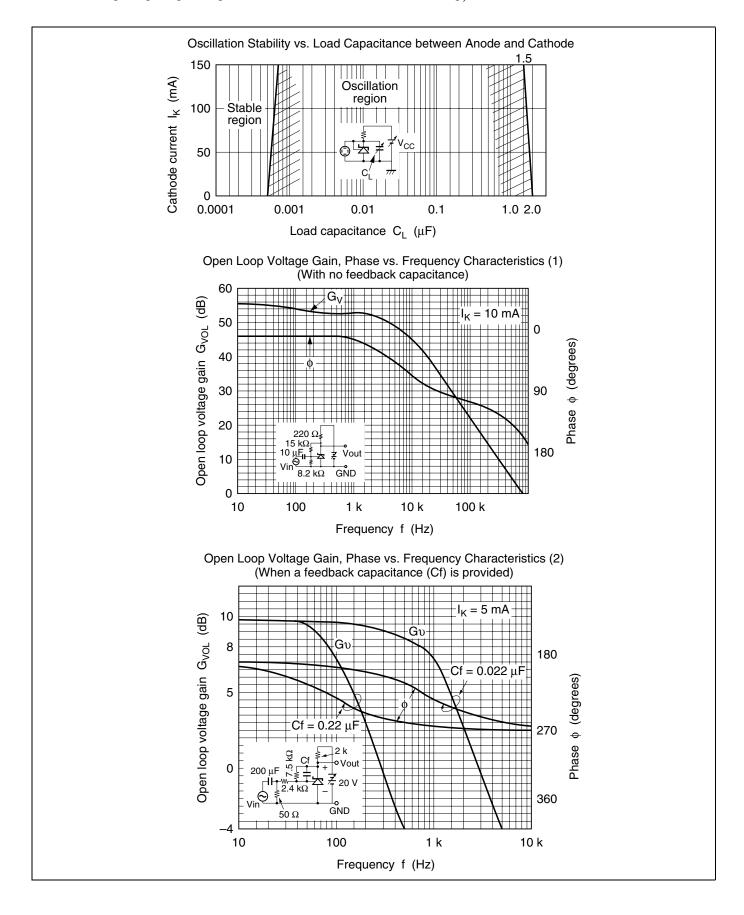
Characteristics Curves

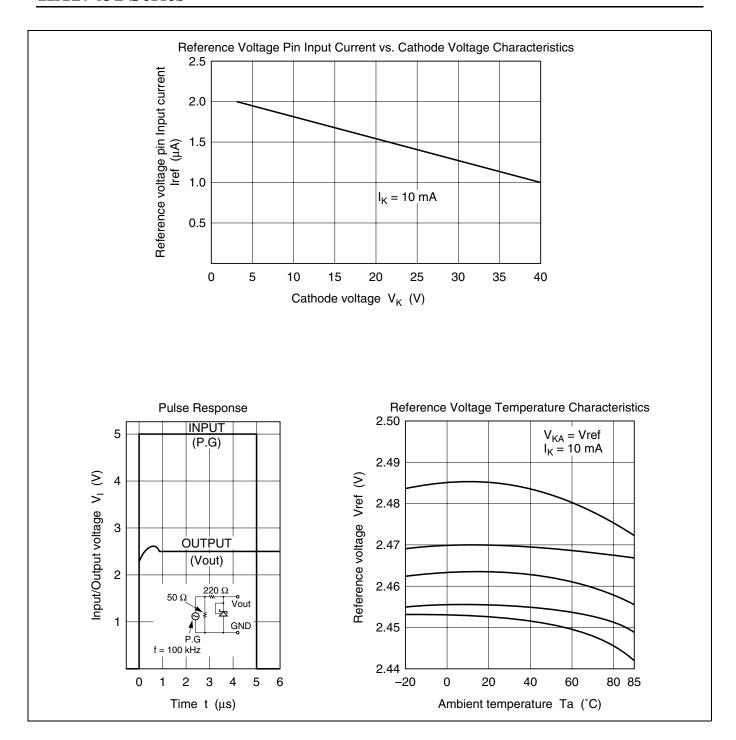
HA17431VLP/VP/VPJ/VUP/VLTP, HA17432VUP/VLTP

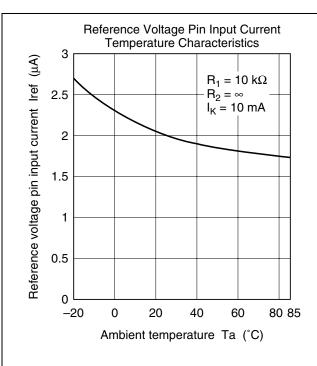


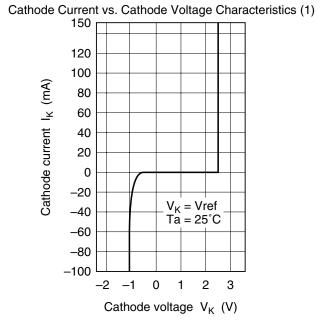


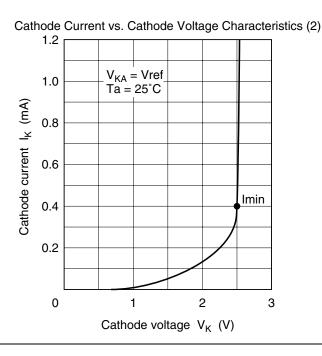
HA17431PJ/PAJ/FPJ/FPAJ/P/PA/UA/UPA/FP/FPA/PNA/PNAJ, HA17432UA/UPA

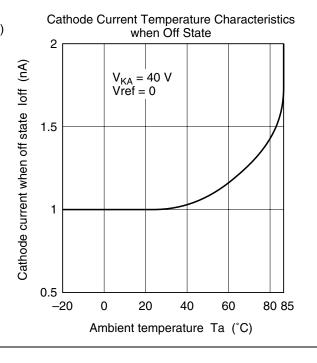












Application Examples

As shown in the figure on the right, this IC operates as an inverting amplifier, with the REF pin as input pin. The open-loop voltage gain is given by the reciprocal of "reference voltage deviation by cathode voltage change" in the electrical specifications, and is approximately 50 to 60 dB. The REF pin has a high input impedance, with an input current Iref of 3.8 μ A Typ (V version: Iref = 2 μ A Typ). The output impedance of the output pin K (cathode) is defined as dynamic impedance Z_{KA} , and Z_{KA} is low (0.2 Ω) over a wide cathode current range. A (anode) is used at the minimum potential, such as ground.

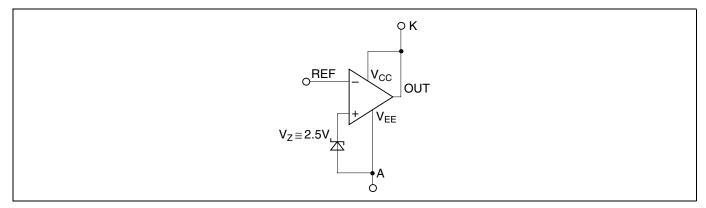


Figure 1 Operation Diagram

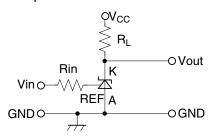
Application Hints

Application Example	Description
Reference voltage generation circuit Vin O—VVV——O Vout	This is the simplest reference voltage circuit. The value of the resistance R is set so that cathode current $I_{\kappa} \ge 1$ mA.
	Output is fixed at Vout \cong 2.5 V.
GND O REF A GND	The external capacitor C_L ($C_L \ge 3.3~\mu F$) is used to prevent oscillation in normal applications.
Variable output shunt regulator circuit	This is circuit 1 above with variable output provided.
$\begin{array}{c c} Vin \bigcirc \bigvee \bigvee & \bullet & \bullet & \bigcirc Vout \\ R & > Iref & K & \\ \hline R_1 & > & K & \\ \end{array}$	Here, Vout \cong 2.5 V $\times \frac{(R_1 + R_2)}{R_2}$
R_2 R_3 R_2 R_3	Since the reference input current Iref = 3.8 μ A Typ (V version: Iref = 2 μ A Typ) flows through R ₁ , resistance values are chosen to allow the resultant voltage drop to be ignored.
	Reference voltage generation circuit Vin O Vout REF A OGND Variable output shunt regulator circuit Vin O Vout R R ₁ Iref K REF A CL

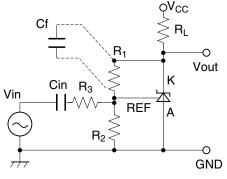
Application Hints (cont.)

No. Application Example

3 Single power supply inverting comparator circuit



4 AC amplifier circuit



Gain G =
$$\frac{R_1}{R_2 /\!\!/ R_3}$$
 (DC gain)

Cutoff frequency fc =
$$\frac{1}{2\pi \text{ Cf (R}_1 \text{ // R}_2 \text{ // R}_3)}$$

Description

This is an inverting type comparator with an input threshold voltage of approximately 2.5 V. Rin is the REF pin protection resistance, with a value of several $k\Omega$ to several tens of $k\Omega$.

 $R_{_L}$ is the load resistance, selected so that the cathode current $I_{_K}$ • 1 mA when Vout is low.

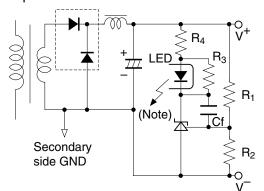
Condition	Vin	Vout	IC
C1	Less then 2.5 V	V _{CC} (V _{OH})	OFF
C2	2.5 V or more	Approx. 2 V (V _{OL})	ON

This is an AC amplifier with voltage gain $G = -R_1 / (R_2//R_3)$. The input is cut by capacitance Cin, so that the REF pin is driven by the AC input signal, centered on 2.5 $V_{\rm DC}$.

 $\rm R_2$ also functions as a resistance that determines the DC cathode potential when there is no input, but if the input level is low and there is no risk of Vout clipping to $\rm V_{cc}$, this can be omitted.

To change the frequency characteristic, Cf should be connected as indicated by the dotted line.

5 Switching power supply error amplification circuit



Note: LED : Light emitting diode in photocoupler

R3: Bypass resistor to feed IK(>Imin) when LED current vanishes

R4: LED protection resistance

This circuit performs control on the secondary side of a transformer, and is often used with a switching power supply that employs a photocoupler for offlining.

The output voltage (between V+ and V-) is given by the following formula:

$$Vout \cong 2.5 \text{ V} \times \frac{(R_1 + R_2)}{R_2}$$

In this circuit, the gain with respect to the Vout error is as follows:

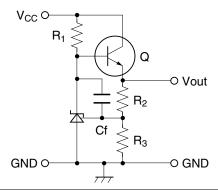
$$G = \frac{R_2}{(R_1 + R_2)} \times \begin{bmatrix} \text{HA17431 open} \\ \text{loop gain} \end{bmatrix} \times \begin{bmatrix} \text{photocoupler} \\ \text{total gain} \end{bmatrix}$$

As stated earlier, the HA17431 open-loop gain is 50 to 60 dB.

Application Hints (cont.)

No. Application Example

6 Constant voltage regulator circuit



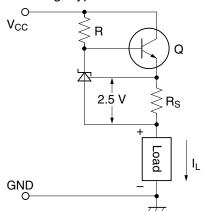
Description

This is a 3-pin regulator with a discrete configuration, in which the output voltage

Vout = 2.5 V ×
$$\frac{(R_2 + R_3)}{R_3}$$

 $\rm R_{\scriptscriptstyle 1}$ is a bias resistance for supplying the HA17431 cathode current and the output transistor Q base current.

7 Discharge type constant current circuit



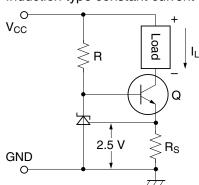
This circuit supplies a constant current of

$$I_L \cong \frac{2.5 \; V}{R_S} \;\; \text{ [A] } \; \text{into the load. Caution is required}$$

since the HA17431 cathode current is also superimposed on $I_{\rm l}$.

The requirement in this circuit is that the cathode current must be greater than lmin=1 mA. The $l_{\rm L}$ setting therefore must be on the order of several mA or more.

8 Induction type constant current circuit



In this circuit, the load is connected on the collector side of transistor Q in circuit 7 above. In this case, the load floats from GND, but the HA17431 cathode current is not superimposed on I_L , so that I_L can be kept small (1 mA or less is possible). The constant current value is the same as for circuit 7 above:

$$I_L\cong \frac{2.5\ V}{R_S}\quad [A]$$

Design Guide for AC-DC SMPS (Switching Mode Power Supply)

Use of Shunt Regulator in Transformer Secondary Side Control

This example is applicable to both forward transformers and flyback transformers. A shunt regulator is used on the secondary side as an error amplifier, and feedback to the primary side is provided via a photocoupler.

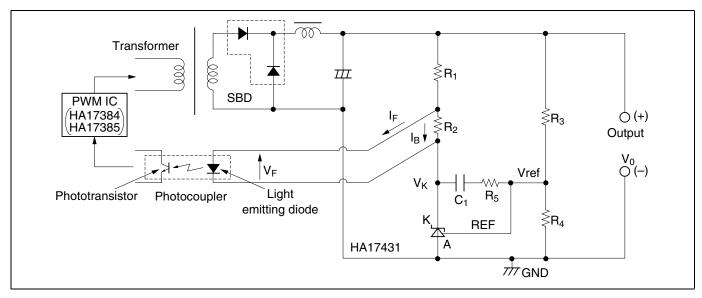


Figure 2 Typical Shunt Regulator/Error Amplifier

Determination of External Constants for the Shunt Regulator

DC characteristic determination: In figure 2, R_1 and R_2 are protection resistor for the light emitting diode in the photocoupler, and R_2 is a bypass resistor to feed I_K minimum, and these are determined as shown below. The photocoupler specification should be obtained separately from the manufacturer. Using the parameters in figure 2, the following formulas are obtained:

$$R_1 = \frac{V_0 - V_F - V_K}{I_F + I_B}$$
 , $R_2 = \frac{V_F}{I_B}$

 $V_{\rm K}$ is the HA17431 operating voltage, and is set at around 3 V, taking into account a margin for fluctuation. $R_{\rm 2}$ is the current shunt resistance for the light emitting diode, in which a bias current $I_{\rm B}$ of around 1/5 $I_{\rm F}$ flows.

Next, the output voltage can be determined by R3 and R4, and the following formula is obtained:

$$V_0 = \frac{R_3 + R_4}{R_4} \times Vref$$
, Vref = 2.5 V Typ

The absolute values of R_3 and R_4 are determined by the HA17431 reference input current Iref and the AC characteristics described in the next section. The Iref value is around 3.8 μ A Typ. (V version: 2 μ A Typ)



AC characteristic determination: This refers to the determination of the gain frequency characteristic of the shunt regulator as an error amplifier. Taking the configuration in figure 2, the error amplifier characteristic is as shown in figure 3.

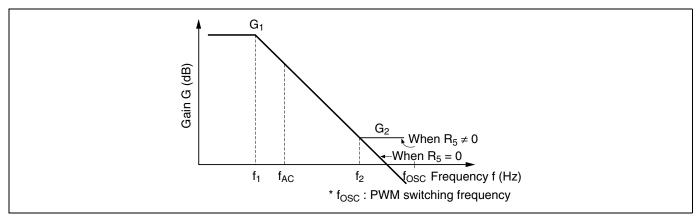


Figure 3 HA17431 Error Amplification Characteristic

In Figure 3, the following formulas are obtained:

Gain

 $G_1 = G_0 \approx 50$ dB to 60 dB (determined by shunt regulator)

$$G_2 = \frac{R_5}{R_3}$$

Corner frequencies

$$f_1 = 1/(2\pi C_1 G_0 R_3)$$

$$f_2 = 1/(2\pi C_1 R_5)$$

 G_0 is the shunt regulator open-loop gain; this is given by the reciprocal of the reference voltage fluctuation $\Delta V ref/\Delta V_{KA}$, and is approximately 50 dB.

Practical Example

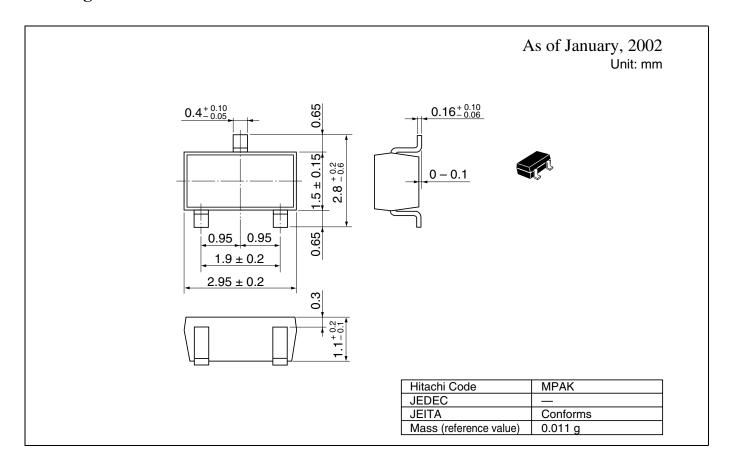
Consider the example of a photocoupler, with an internal light emitting diode $V_F = 1.05$ V and $I_F = 2.5$ mA, power supply output voltage $V_2 = 5$ V, and bias resistance R_2 current of approximately 1/5 I_F at 0.5 mA. If the shunt regulator $V_K = 3$ V, the following values are found.

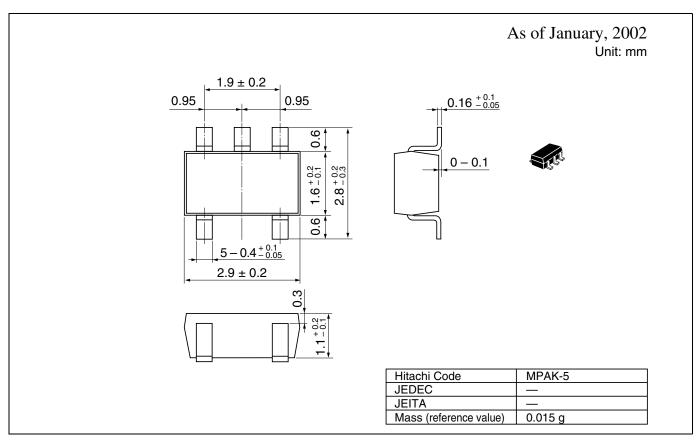
R₁ =
$$\frac{5V - 1.05V - 3V}{2.5\text{mA} + 0.5\text{mA}}$$
 = 316(Ω) (330Ω from E24 series)
R₂ = $\frac{1.05V}{0.5\text{mA}}$ = 2.1(kΩ) (2.2kΩ from E24 series)

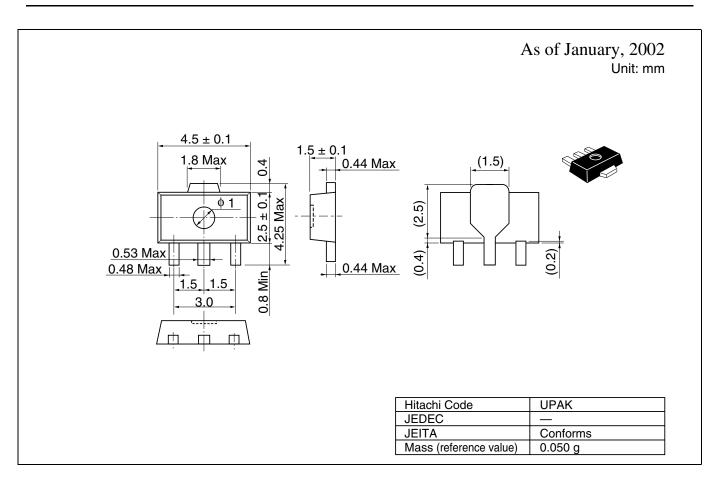
Next, assume that $R_3 = R_4 = 10 \text{ k}\Omega$. This gives a 5 V output. If $R_5 = 3.3 \text{ k}\Omega$ and $C_1 = 0.022 \,\mu\text{F}$, the following values are found.

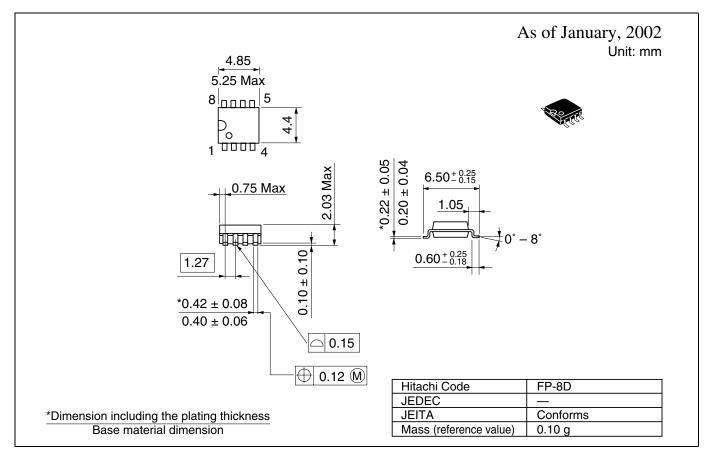
$$\begin{split} G_{_2} &= 3.3 \text{ k}\Omega \text{ } / \text{ } 10 \text{ k}\Omega = 0.33 \text{ times (-10 dB)} \\ f_{_1} &= 1 \text{ } / \text{ } (2 \times \pi \times 0.022 \text{ } \mu\text{F} \times 316 \times 10 \text{ k}\Omega) = 2.3 \text{ (Hz)} \\ f_{_2} &= 1 \text{ } / \text{ } (2 \times \pi \times 0.022 \text{ } \mu\text{F} \times 3.3 \text{ k}\Omega) = 2.2 \text{ (kHz)} \end{split}$$

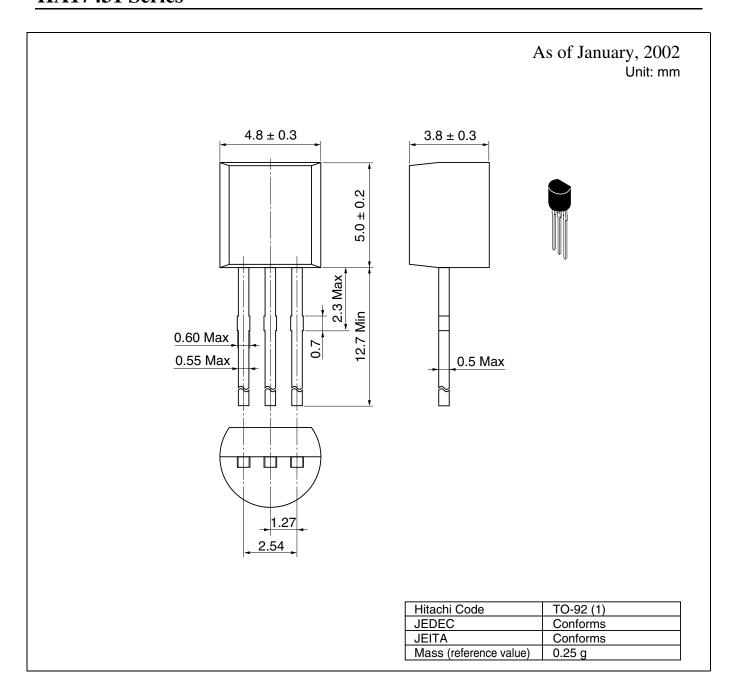
Package Dimensions

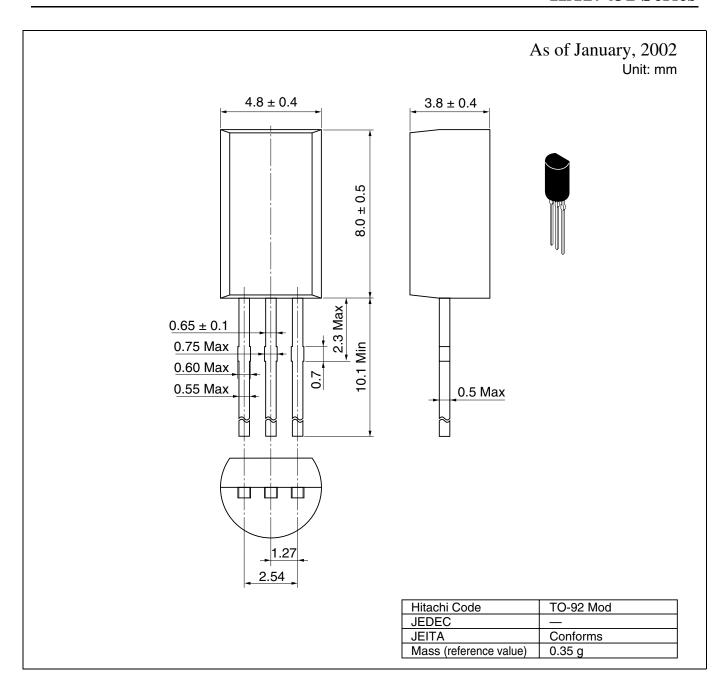












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