TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# **TA2008AN**

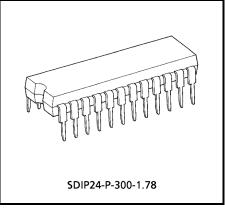
# 5V AM / FM 1 Chip Tuner IC (for digital tuning system)

The TA2008AN is the AM / FM 1 chip tuner IC, which is designed for radio cassette players and music centers.

This is suitable for digital tuning system applications.

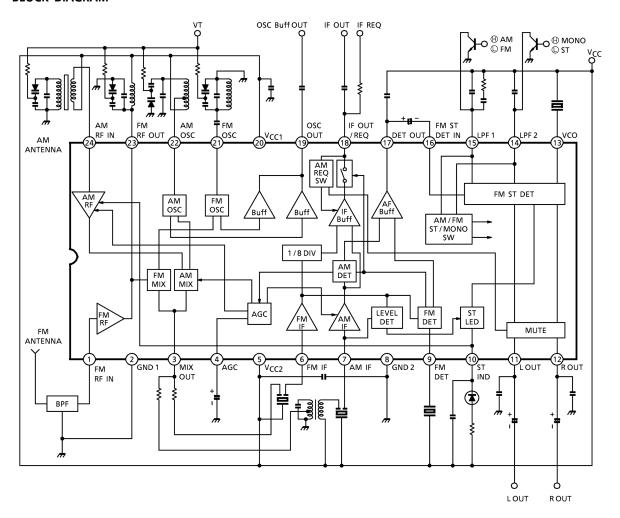
#### **Features**

- Suitable for combination with digital tuning system which is included IF counter.
- One terminal type AM / FM IF count output (auto stop signal) for IF counter of digital tuning system.
  - FM: 1.3375MHz (1 / 8 dividing)
  - AM: 450kHz
- Built-in mute circuit for IF count output.
- For adopting ceramic discriminator and ceramic resonator, it is not necessary to adjust the FM quad detector circuit and FM stereo detector vco circuit.
- Built-in one terminal type AM / FM local oscillator buffer output for digital tuning system applications.
- Operating supply voltage range: V<sub>CC</sub> = 3.5~14V (Ta = 25°C)



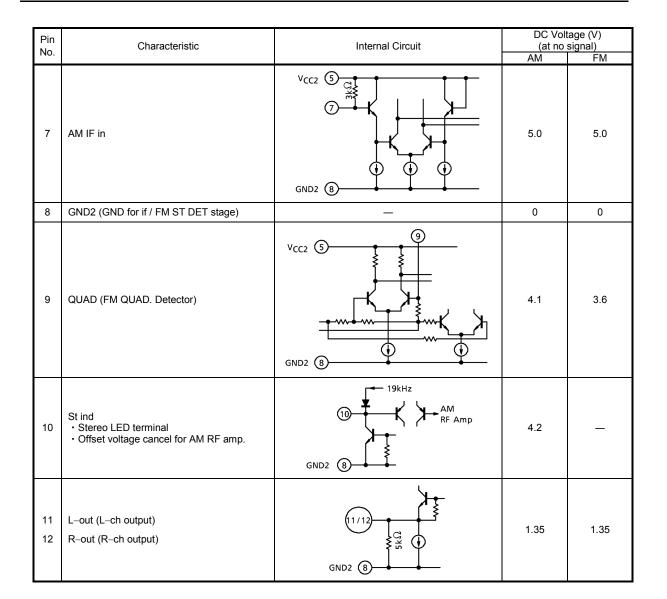
Weight: 1.2g (typ.)

#### **BLOCK DIAGRAM**



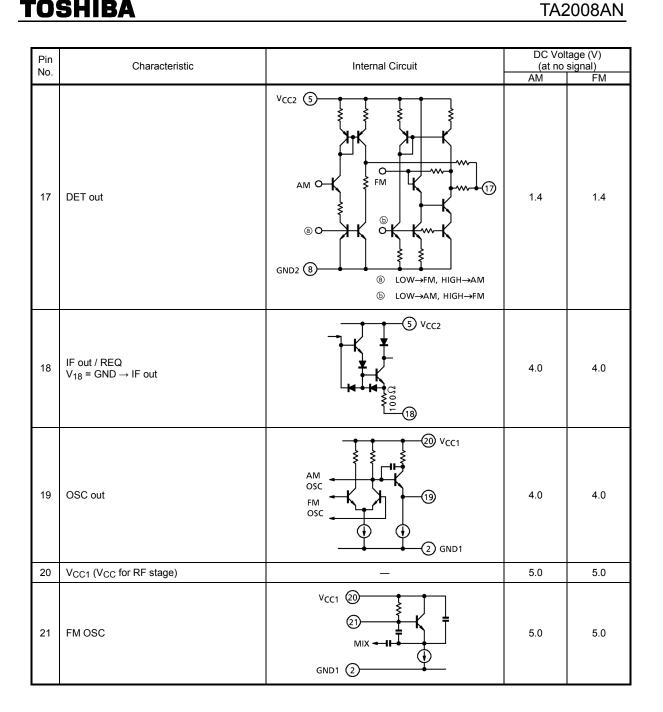
# **Explanation Of Terminals**

Pin	Characteristic	Internal Circuit	DC Voltage (V) (at no signal)			
No.			AM	FM		
1	FM-RF in	FM-RF OUT  23  U U U U U U U U U U U U U U U U U U U	0	0.8		
2	GND1 (GND for RF stage)	_	0	0		
3	Mix out	VCC1 20 AM MIX FM MIX GND1 2 3	0.3	0.8		
4	AGC	V <sub>CC2</sub> (5)				
5	V <sub>CC2</sub> (V <sub>CC</sub> for IF / FM ST DET stage)	_	5.0	5.0		
6	FM IF in	VCC2 (5) (3) (6) (8) (6) (7) (7) (7) (7) (7) (7) (7) (7) (7) (7	5.0	5.0		



**TOSHIBA** 

Pin No.	Characteristic	Internal Circuit	DC Voltage (V) (at no signal)		
INO.			AM	FM	
13	VCO	V <sub>CC2</sub> 5 3 3 3 4 5 6 6 7 6 7 6 7 6 7 6 7 6 7 6 7 6 7 6 7	5.0	4.1	
14	LPF2  • LPF terminal for synchronous detector.  • VCO stop terminal  V <sub>14</sub> = GND → VCO stop	GND2 8	5.0	3.4	
15	LPF1  • LPF terminal for phase detector  • Bias terminal for AM / FM SW circuit  V <sub>15</sub> = GND → AM  V <sub>15</sub> = open → FM	GND2 8	0	2.8	
16	FM ST DET in	(16) M M M M M M M M M M M M M M M M M M M	1.4	1.4	



Pin No.	Characteristic	Internal Circuit	DC Voltage (V) (at no signal)		
140.			AM	FM	
22	AM OSC	20 Vcc1 22 GND1	5.0	5.0	
23	FM RF out	cf. pin (1)	5.0	5.0	
24	AM RF in	VCC1 20 AGC 24 GND2 2	5.0	5.0	

# Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	15	V	
LED current	I <sub>LED</sub>	10	mA	
LED voltage	V <sub>LED</sub>	15	V	
Power dissipation	P <sub>D</sub> *	1200	mW	
Operating temperature	T <sub>opr</sub>	-25~75	°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

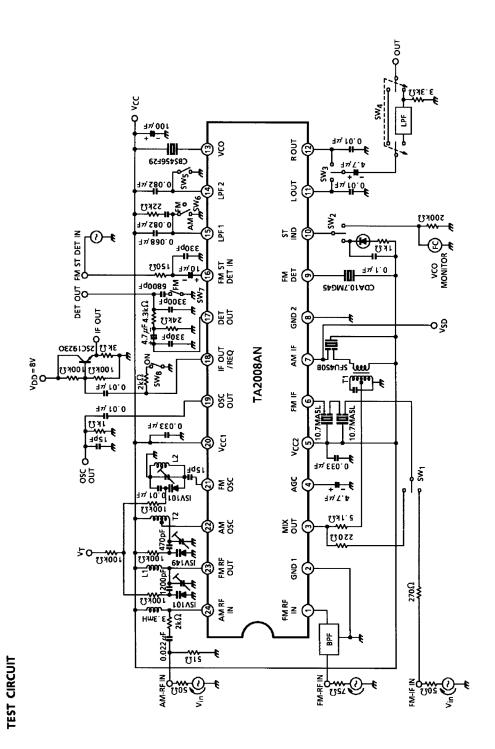
 $<sup>^*</sup>$  : Derated above Ta = 25°C in the proportion of 9.6mW / °C

## **Electrical Characteristics**

Unless Otherwise Specified, Ta = 25°C,  $V_{CC}$  = 5V, SW8: Off, F / E: f = 98MHz,  $f_m$  = 1kHz FM IF: f = 10.7MHz,  $\Delta f$  = ±22.5kHz,  $f_m$  = 1kHz AM: f = 1MHz, MOD = 30%,  $f_m$  = 1kHz FM ST DET:  $f_m$  = 1kHz

Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit	
Suppl	ly current	I <sub>CC (FM)</sub>	— V <sub>in</sub> = 0, FM mode		_	27	36	- mA	
Оиррі	y current	I <sub>CC (AM)</sub>	_	V <sub>in</sub> = 0. AM mode		18	25		
./E	Input limiting voltage	V <sub>in</sub> (lim)	_	–3dB limiting with respect to V <sub>OD</sub> level at Vin = 60dBμV EMF	_	11	l	dBµV EMF	
Ш	Local OSC buffer output voltage	V <sub>OSC</sub> (buff) FM	_	f <sub>OSC</sub> = 108.7MHz	90	180	l	mV <sub>rms</sub>	
	Input limiting voltage $V_{in}$ (lim.) IF $-\frac{-3dB}{v_{in}}$ with respect to $V_{OD}$ level at $V_{in}$ = 80dB $\mu$ V EMF		with respect to V <sub>OD</sub> level at	40	45	50	dBµV EMF		
	Recovered output voltage	V <sub>OD</sub>	_	V <sub>in</sub> = 80dBμV EMF	50	75	100	mV <sub>rms</sub>	
	Signal to noise ratio	S/N	_	V <sub>in</sub> = 80dBµV EMF	_	70	_	dB	
	Total harmonic distortion	THD	— V <sub>in</sub> = 80dBμV EMF			0.3	l	%	
FM IF	AM rejection ratio	AMR	_	V <sub>in</sub> = 80dBμV EMF	_	50	-	dB	
	SD output sensitivity	V <sub>SD</sub>	_	$V_{SD} = V_{CC} - 0.1V$	53	58	63	dBµV EMF	
	IF count output frequency	f1 / 8 IF (FM)	_	V <sub>in</sub> = 80dBμV EMF, SW8: On	1.3373	1.3375	1.3377	MHz	
	IF count output voltage	V1 / 8 IF (FM)	_	V <sub>in</sub> = 80dBμV EMF,SW8: On	350	500	_	$mV_{p-p}$	
	IF count output sensitivity	IF sens (FM)	_	SW8: On	49	54	59	dBµV EMF	

Characteristic		Symbol	Test Cir-	Test Condition		Min.	Тур.	Max.	Unit		
	Gain		G <sub>V</sub>	cuit —	V <sub>in</sub> = 26dBµV EMF		20	45	80	mV <sub>rms</sub>	
	Recovered output voltage		V <sub>OD</sub>	_	V <sub>in</sub> = 60dBµV EMF		45	65	90	mV <sub>rms</sub>	
	Signal to nois	se ratio	S/N	_	V <sub>in</sub> = 60dBµV EMF		_	42	_	dB	
AM	Total harmor distortion	nic	THD	_	V <sub>in</sub> = 60dBµV EMF		_	1.0	_	%	
4	Local OSC b output voltag		V <sub>OSC</sub> (buff) AM	l	f <sub>OSC</sub> = 1.45MHz		90	150	ı	mV <sub>rms</sub>	
	IF count outp voltage	out	V <sub>IF</sub> (AM)	1	V <sub>in</sub> = 60dBµV EMF,	SW8: On,	350	500	ı	mV <sub>p-p</sub>	
	IF count outp sensitivity	out	IF sens (AM)	ı	SW8: On		35	40	45	dBµV EMF	
Pin (1	in (17) output resistance		7) output resistance R17 –			FM mode		ı	0.75	ı	kΩ
1 111 (1	r) output resis	starioc	7		AM mode	-	15.5	-	K22		
	Input resistance		R <sub>IN</sub>	l			ı	24	ı	kΩ	
	Output resistance		R <sub>OUT</sub>	l			1	5	1	kΩ	
	Max. Composite signal input voltage		V <sub>in max</sub> (stereo)	-	L + R = 90%, P = 10%, SW4: LPF on f <sub>m</sub> = 1kHz, THD = 3%		_	800	_	mV <sub>rms</sub>	
	Separation			_	P = 20mV <sub>rms</sub> SW4: LPF on	f <sub>m</sub> = 100Hz	_	42	_	dB	
			Sep.			f <sub>m</sub> = 1kHz	35	42	_		
						f <sub>m</sub> = 10kHz	_	42	_		
╽	Total harmonic	Monaural	THD (monaural)		V <sub>in</sub> = 200mV <sub>rms</sub>		_	0.1	_	%	
FM St DET	distortion	Stereo	THD (stereo)		L + R = 180mV <sub>rms</sub> , P = 20mV <sub>rms</sub> , SW4: LPF on,		ı	0.1	ı	/0	
ᇤ	Voltage gain		G <sub>V</sub>	_	V <sub>in</sub> = 200mV <sub>rms</sub>		-2	0	2	dB	
	Channel balance		C. B.	_	V <sub>in</sub> = 200mV <sub>rms</sub>		-2	0	2	dB	
	Stereo LED	On	V <sub>L (ON)</sub>		Pilot input		_	8	15	mV <sub>rms</sub>	
	sensitivity	Off	V <sub>L (OFF)</sub>		r not input		2	6	_		
	Stereo LED hysteresis		V <sub>H</sub>	_	To LED turn off from LED turn on		_	2	_	mV <sub>rms</sub>	
	Capture range		C. R.	_	P = 15mV <sub>rms</sub>		_	±1.3	_	%	
	Signal to noise ratio		S/N		V <sub>in</sub> = 200mV <sub>rms</sub>			80		dB	
	Muting attenuation		MUTE	_	V <sub>in</sub> = 200mV <sub>rms</sub>	_	80	_	dB		



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### **Coil Data**

0 1111	Test	Test	Test	L	Co	_		Tu	rns		Wire	5.6
Coil No.	Freq.	req. (µH)		Qo	1–2	2–3	1–3	3–6	(mm $\phi$ )	Reference		
L1 FM RF	100MHz			100				$2\frac{1}{2}$	0.5 UEW	Within core		
L1 FM OSC	100MHz			100				$2\frac{1}{2}$	0.5 UEW	Within core		
T1 AM mix	455kHz		180	48↑	47	111	158	4–6 20	0.06 UEW	(T): A7LCS-12064N		
T2 AM OSC	796kHz	268		125	15	89			0.06 UEW	(S): 2157–2239–213A (T): A7BRS–11998Y		

(S): Sumida electric co., ltd.

T2: AM OSC

(T): Toko co., Itd.

 $\begin{array}{c} \mathsf{L}_1 \,:\, \mathsf{FM} \;\; \mathsf{RF} \\ \mathsf{L}_2 \,:\, \mathsf{FM} \;\; \mathsf{OSC} \end{array}$ 

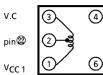
T1: AM MIX

C.F

pin3

GND 2

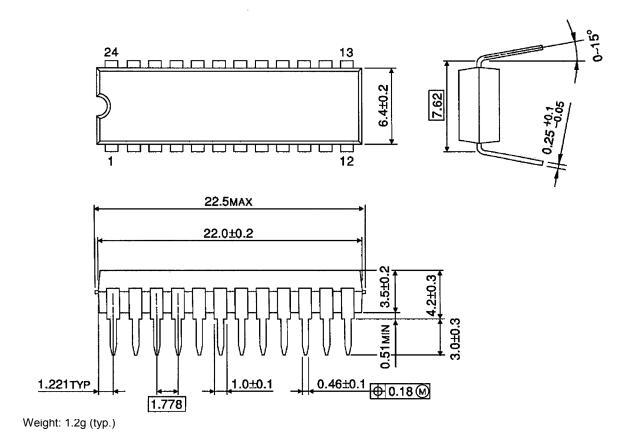
V.C



# **Package Dimensions**

SDIP24-P-300-1.78

Unit: mm



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