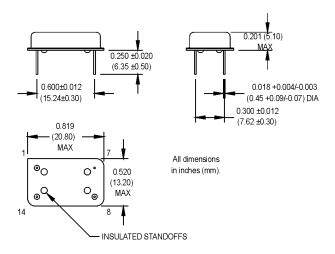
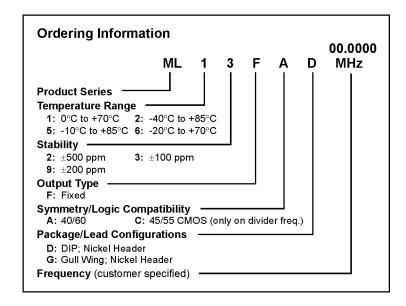
ML Series Micropower CMOS Oscillators







See page 146 for gull wing configuration.



Available Stabilities vs. Temperature

T	2	3	9
1	Α	S	Α
2	Α	N	N
5	Α	Ν	Ν
6	Α	N	N

Pin Connections

PIN	FUNCTION(S)		
1	N/C		
7	Circuit/Case Ground		
8	Output		
14	+Vdd		

Divider Output Frequencies

2048 Hz	128 Hz	4 Hz
1024 Hz	64 Hz	2 Hz
512 Hz	32 Hz	
256 Hz	8 Hz	

	PARAMETER	Symbol	Min.	Тур.	Мах.	Units	Condition	
	Frequency Range	F	2 Hz		32.768	kHz		
			See "Divide	See "Divider Output Frequencies" table for available frequencies				
	Frequency Stability	∆F/F	(See Order	ring Informa	ation)			
	Operating Temperature	TA	(See Order	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C		
ons	Input Voltage	Vcc	3.0	5.0	6.0	٧	Except as Noted	
Electrical Specifications	Input Current ¹	ldd	li	(15	μА	Vdd = 3.0 V	
] jj	32.768 kHz only	!	\	Į į	25	μΑ	Vdd = 5.0 V	
Sp.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	35	μ Α	Vdd = 6.0 V	
cal	Symmetry (Duty Cycle)		40	50	60	%	½ Vdd	
ij	Load ²				15	pF		
Ë	Rise/Fall Time ³	Tr/Tf	\	·	\	(
	< 32.768 kHz	1	۱ ۱	۱ ۱	50	ns		
	32.768 kHz		Li	<u></u> i	10	ns		
	Logic "1" Level	Voh	80% Vdd	·		٧		
	Logic "0" Level	Vol		\	20% Vdd	٧		
	Startup Time	Ts		500		ms	@ 32.768 kHz	
Ē	Mechanical Shock	Per MIL-S	STD-202, Metl	hod 213, Co	ondition C			
Environmental	Vibration	Per MIL-S	TD-202, Metl	hod 201 & 2	204			
	Reflow Solder Conditions		See page 147					
۱ <u>۲</u>	Hermeticity	Per MIL-S	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm.cc/s of helium)					
ᇤ	Solderability		Per EIAJ-STD-002					

- 1. Supply current for divided output is slightly higher that listed.
- See load circuit diagram #2 on page 148.
 Rise/Fall times are measured between 20% Vdd and 80% Vdd.

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