

SINGLE VIDEO DRIVER

### **FEATURES**

- Single Video Line Driver Chip
- R<sub>L</sub>=150  $\Omega$  (75  $\Omega$  Back-Terminated Cable)
- · Power-Down Standby Mode
- Tiny 2.9 x 1.6 mm 6-Lead SOT23 Package
- Low Power Dissipation: 32 mW
- Flat Response f<sub>IN</sub> = 100 kHz to 10 MHz (typical)
- Single +5 Volt Power Supply

#### **APPLICATIONS**

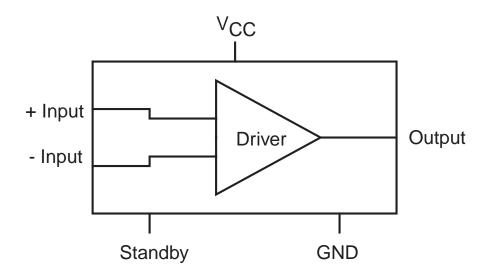
- Digital Video Disk
- Video Line Driver for Encoders
- Digital Video Tape Recorders
- · Video Cassette Recorders
- PC Multimedia
- · Consumer Video

# **GENERAL DESCRIPTION**

The SPT9404 is a single video line driver chip that takes a standard video signal as an analog input and provides a buffered analog output for driving a 150  $\Omega$  load (75  $\Omega$  backterminated cable). The standard video input signal (1  $V_{P\text{-}P}$ ) is typically amplified 6 dB using external components to produce a 2  $V_{P\text{-}P}$  into an AC-coupled 150  $\Omega$  load. (See the typical interface circuit diagram.)

The SPT9404 features a standby mode which draws only 96  $\mu$ W of power. Nominal power dissipation (no input) is typically 32 mW. It requires a single +5 V supply, operates over the commercial temperature range (0 to +70 °C) and is available in a tiny surface mount (2.9 x 1.6 mm) 6-lead SOT-23 package.

## **BLOCK DIAGRAM**



# Signal Processing Technologies, Inc.

4755 Forge Road, Colorado Springs, Colorado 80907, USA Phone: (719) 528-2300 FAX: (719) 528-2370

# ABSOLUTE MAXIMUM RATINGS (Beyond which damage may occur)(1) 25 °C

Supply Voltages	Temperature
VCC+6.0 V	Operating Temperature 0 to +70 °C
Maximum Power Dissipation	Storage Temperature55 to +150 °C
P <sub>D</sub> 150 mW	
Thermal Impedance (T <sub>A</sub> =+25 °C and above)	
ΘCA1.2 mW/°C	

**Note:** 1. Operation at any Absolute Maximum Rating is not implied. See Electrical Specifications for proper nominal applied conditions in typical applications.

# **ELECTRICAL SPECIFICATIONS**

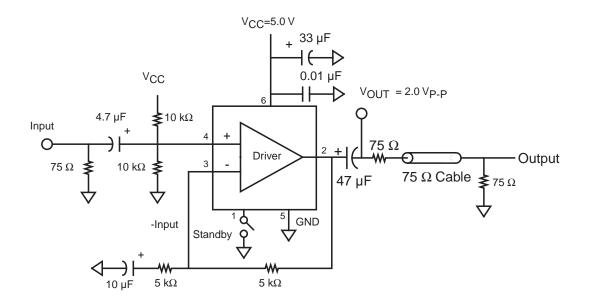
 $T_A$  = +25 °C,  $V_{CC}$  = +5.0 V,  $V_{IN}$  = 1.0  $V_{P-P}$  video signal, voltage gain of +2,  $R_L$  = 150  $\Omega$ , unless otherwise specified.

PARAMETERS	TEST CONDITIONS	TEST LEVEL	MIN	SPT9404 TYP	MAX	UNITS
Power Supply						
Supply Current (ICC)	No Input	1		6.3	8.5	mA
V <sub>CC</sub> Voltage		IV	4.5	5.0	5.5	V
Power Dissipation		1		31.5	42.5	mW
Standby Current	Pin 1 Grounded	1		24	50	μΑ
Standby Power Dissipation	Pin 1 Grounded	1		96	250	μW
Digital Input						
Digital Input (Low)	Standby Pin 1	1	0.0	0.1	0.3	V
Digital Input (High)	Standby Pin 1	1	1.8	2.0	Vcc	V
Dynamic Performance						
Voltage Gain	f <sub>IN</sub> = 1.0 MHz	1	5.7	6.0	6.3	dB
Total Harmonic Distortion	$f_{IN} = 1.0 \text{ kHz}$	1		0.2	1.0	%
Open Loop Gain		V		40		dB
Bandwidth		V		20		MHz
Slew Rate		V		70		V/μs
Frequency Response	$f_{IN} = 1 \text{ to 5 MHz}$	V		0.0		dB
Voltage Output Maximum (V <sub>OM</sub> )		I	1.0	1.2		V <sub>RMS</sub>

TEST LEVEL CODES	<b>TEST LEVEL</b>	TEST PROCEDURE
All electrical characteristics are subject to the following	1	100% production tested at the specified temperature.
conditions:  All parameters having min/max specifications are guaranteed. The Test Level column indicates the specific device testing actually performed during production and Quality Assurance inspection. Any blank section in the data column indicates that the specification is not tested at the specified condition.	II	100% production tested at $T_A$ = +25 °C, and sample tested at the specified temperatures.
	III	QA sample tested only at the specified temperatures.
	IV	Parameter is guaranteed (but not tested) by design and characterization data.
	V	Parameter is a typical value for information purposes only.
	VI	100% production tested at $T_A$ = +25 °C. Parameter is guaranteed over specified temperature range.

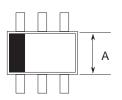


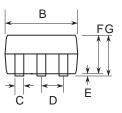
Figure 1 - Typical Interface Circuit

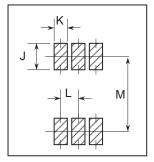


# **PACKAGE OUTLINE**

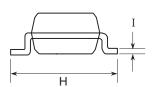
6-Lead SOT23







Suggested Pad Layout



	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
Α	0.055	0.071	1.4	1.8
В	0.106	0.122	2.7	3.1
С	0.008	0.016	0.2	0.4
D	0.037 typ		0.95 typ	
E	0.000	0.004	0.0	0.1
F	0.035	0.051	0.9	1.3
G		0.071 max		1.4 max
Н	0.098	0.122	2.5	3.1
I	0.001	0.009	0.03	0.23
J	0.039 typ		1.0 typ	
K	0.028 typ		0.7 typ	
L	0.037 typ		0.95 typ	
М	0.094 typ		2.4 typ	

#### PIN ASSIGNMENTS

# Standby 1 6 VCC Output 2 5 GND - Input 3 4 + Input

### **PIN FUNCTIONS**

Name	Function
- Input	Inverting Signal Input
	(typically 1 V <sub>P-P</sub> , AC coupled)
+ Input	Non-Inverting Signal Input
l	(typically 1 V <sub>P-P</sub> , AC coupled)
Output	Buffered Output
	(typically 2.0 $V_{P-P}$ , $R_L = 150 \Omega$ , AC coupled)
Standby	Power Down Standby Mode Select
	(Low = Standby, Internal Pull-Up)
V <sub>CC</sub>	+5.0 V Supply
GND	Ground

# ORDERING INFORMATION

PART NUMBER	TEMPERATURE RANGE	PACKAGE TYPE
SPT9404SCL	0 to +70 °C	6-Lead SOT23

Signal Processing Technologies, Inc. reserves the right to change products and specifications without notice. Permission is hereby expressly granted to copy this literature for informational purposes only. Copying this material for any other use is strictly prohibited.

WARNING - LIFE SUPPORT APPLICATIONS POLICY - SPT products should not be used within Life Support Systems without the specific written consent of SPT. A Life Support System is a product or system intended to support or sustain life which, if it fails, can be reasonably expected to result in significant personal injury or death.

Signal Processing Technologies believes that ultrasonic cleaning of its products may damage the wire bonding, leading to device failure. It is therefore not recommended, and exposure of a device to such a process will void the product warranty.

