# Three quadrant triacs high commutation

# BTA216X series C

### **GENERAL DESCRIPTION**

Glass passivated high commutation triacs in a plastic envelope intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. These devices will commutate the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

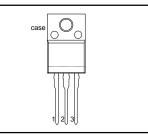
## QUICK REFERENCE DATA

**PIN CONFIGURATION** 

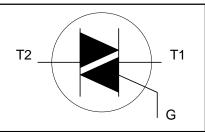
SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V <sub>drm</sub>	BTA216X- Repetitive peak off-state voltages	<b>500C</b> 500	<b>600C</b> 600	<b>800C</b> 800	V
I <sub>T(RMS)</sub> I <sub>TSM</sub>	RMŠ on-state current Non-repetitive peak on-state current	16 140	16 140	16 140	A A

#### PINNING - SOT186A

PIN	DESCRIPTION			
1	main terminal 1			
2	main terminal 2			
3	gate			
case	isolated			



## SYMBOL



#### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V <sub>drm</sub>	Repetitive peak off-state voltages		-	<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	>
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave;	-		16		A
I <sub>TSM</sub>	Non-repetitive peak on-state current	$T_{mb} \le 38 \ ^{\circ}C$ full sine wave; $T_j = 25 \ ^{\circ}C$ prior to surge t = 20 ms	-		140		A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 16.7 ms t = 10 ms	-		150 98		A A <sup>2</sup> s
dl <sub>⊤</sub> /dt	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A}; dI_G/dt = 0.2 \text{ A}/\mu s$			100		A/μs
I <sub>GM</sub> V <sub>GM</sub>	Peak gate current Peak gate voltage		-		2 5		A V
P <sub>GM</sub>	Peak gate power		-		5		W W
P <sub>G(AV)</sub>	Average gate power	over any 20 ms period	-		0.5		vv
T <sub>stg</sub> T <sub>j</sub>	Storage temperature Operating junction temperature		-40 -		150 125		Ĵ Ĵ

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

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## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Thermal resistance junction to heatsink	full or half cycle with heatsink compound without heatsink compound	-	-	4.0 5.5	K/W K/W
R <sub>th j-a</sub>	Thermal resistance junction to ambient	in free air	-	55	-	K/W

#### STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
I <sub>GT</sub>	Gate trigger current <sup>2</sup>	$V_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$					
01			T2+ G+	2	18	35	mA
			T2+ G-	2	21	35	mA
			T2- G-	2	34	35	mA
l I,	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$					
-	, C		T2+ G+	-	-	20	mA
			T2+ G-	-	-	30	mA
			T2- G-	-	-	20	mA
I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$		-	-	15	mA
V <sub>T</sub>	On-state voltage	$I_{T} = 20 \text{ A}$		-	1.2	1.5	V
V <sub>GT</sub>	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$		-	0.7	1.5	V
		$V_{\rm D} = 400$ V; $I_{\rm T} = 0.1$ A; $T_{\rm i} = 125$ °	C	0.25	0.4	-	V
I <sub>D</sub>	Off-state leakage current	$V_D = V_{DRM(max)}; T_j = 125 °C$		-	0.1	0.5	mA

#### **DYNAMIC CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	UNIT
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}$ ; $T_j = 125$ °C; exponential waveform; gate open circuit	1000	-	V/µs
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; \text{ T}_{j} = 125 \text{ °C}; \text{ I}_{T(RMS)} = 16 \text{ A}; \text{ without snubber; gate open circuit}$	3	14	A/ms
t <sub>gt</sub>	Gate controlled turn-on time	$I_{TM} = 20 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	μs

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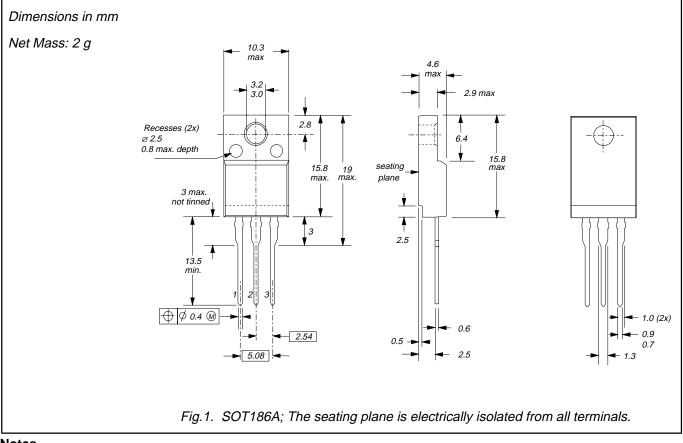
**<sup>2</sup>** Device does not trigger in the T2-, G+ quadrant.

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#### Preliminary specification

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## **MECHANICAL DATA**



Notes

Refer to mounting instructions for F-pack envelopes.
Epoxy meets UL94 V0 at 1/8".

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#### DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	specification This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.				
Application information				
Where application inform	ation is given, it is advisory and does not form part of the specification.			
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