

SANYO Semiconductors **DATA SHEET**

An ON Semiconductor Company

Monolithic Digital IC

LB1973JA — Two-channel H-Bridge Driver

Overview

The LB1973JA is a two-channel H-bridge driver that supports for low saturation draive operation. It is optimal for H-bridge drive of stepping motors (AF and zoom) in portable equipment such as camera cell phones.

Features

- Two-channel H-bridge driver
- 2ch simultaneous connection is possible
- Parallel input interface

- 2 phase excitation, 1-2 phase excitation drive are possible
- The range of the operation voltage is wide.(1.8V to 7.5V)
- Built-in thermal protection

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		-0.3 to +8.0	V
Output voltage	V _{OUT} max		- V _{SF} to V _{CC} +V _{SF}	V
Input voltage	V _{IN} max		-0.3 to +8.0	V
Spark killer Di order direction electric	I _{SF} max		1000	mA
Ground pin source current	I _{GND}	Per channel	1000	mA
Allowable power dissipation	Pd max	*Mounted on a bord	800	mW
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

^{*} Mounted on a Specified board : 114.3mm×76.1mm×1.6mm, glass epoxy

Allowable Operating Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		1.8 to 7.5	٧
High-level input voltage	V _{IH}	R _{IN} = 1kΩ	1.3 to 7.5	V
Low-level input voltage	V _{IL}	R _{IN} = 1kΩ	-0.3 to +0.5	V

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Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

LB1973JA

Electrical Characteristics at Ta = 25°C, $V_{CC} = 1.9V$

				Datings		
Parameter	Symbol	Conditions	Ratings min typ max			Unit
Source current	rce current I _{CCO} 1 V _{CC} = 1.9V,IN1 to IN4 = Low level		111111	typ 0.01	111aX 1	μА
Source current				0.01	1	
	I _{CCO} 2			-		μА
	I _{CC} 1	IN1 = High level,IN2 to IN4 = Low level		18	25	mA
	lCC2	IN1 = High level,IN2 to IN4 = Low level, V _{CC} = 3V		19	27.5	mA
Output saturation voltage1 (single connection)	V _{OUT} 11	I _{OUT} = 270mA,V _{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V _{OUT} = Upper Tr and Under Tr IN1 = High level, IN2 to IN4 = Low level Supplementation: Standard similar as for IN2 to IN4 = High level		0.2	0.3	V
	V _{OUT} 12	I _{OUT} = 350mA,V _{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V _{OUT} = Upper Tr and Under Tr IN1 = High level, IN2 to IN4 = Low level Supplementation: Standard similar as for IN2 to IN4 = High level		0.25	0.4	V
Output saturation voltage2 (parallel connection)	V _{OUT} 21			0.12	0.2	V
	V _{OUT} 22	I _{OUT} = 500mA,V _{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V _{OUT} = Upper Tr and Under Tr OUT1-3,OUT2-4 short. IN1 and IN3 = High level,IN2 and IN4 = Low level Supplementation: Standard similar as for IN2 and IN4 = High level		0.2	0.35	V
Output electric current with the parasitic element	I _{PA}	V _{IN} = 1.9 to 3.6V, Ta = -20 to 85°C *1			9	mA
Input current	I _{IN}	V _{IN} = 1.9V		32	70	μΑ
Themal shutdown operation temperature	Ttsd	*2: Design guarantee		140		°C
Temperature hysteresis width	ΔΤ	*2: Design guarantee		20		°C
Spark killer Diode						
Reverse current	I _S (leak)	V _{CC} -OUT = 8V, V _{IN} = Low level			10	μА
Forword voltage	V _{SF}	I _{SF} = 400mA, V _{IN} = Low level			1.7	V

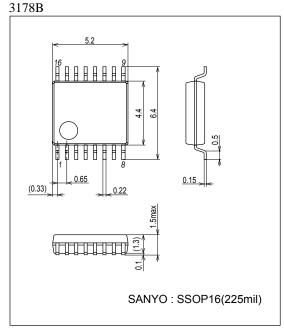
^{*1:} Output electric current with the parasitic element_IpA: The current value that the off ch(-free) output is pulled at the time of one side ch drive by a parasitic element

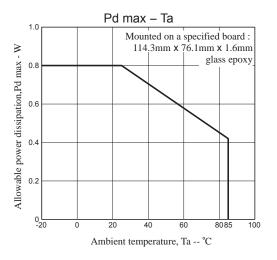
^{*2:} Design guarantee value and does not measure

^{*} VSF: The current order direction voltage true in a time

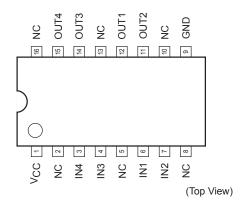
Package Dimensions

unit: mm (typ)





Pin Assignment

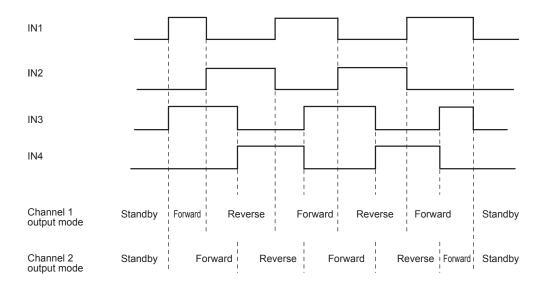


Truth Table

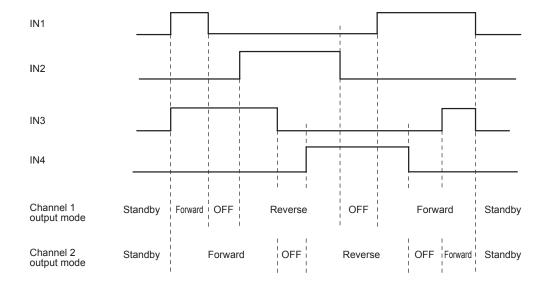
Input			Output			Mada			
IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4	Mode	
Low	Low	Low	Low	Off	Off	Off	Off	Standby mode	
High	Low		-	High	Low	-		Channel 1, forward	
Low	High	-		Low	High		i	Channel 1, reverse	
		High	Low	-		High	Low	Channel 2, forward	
	-	Low	High		-	Low	High	Channel 2, reverse	
High	High	-	Ī	The least sector of facility front binds be and in ordinary to a sector of					
-	-	High	High	The logic output for the first high-level input is produced.					

Stepping motor control example

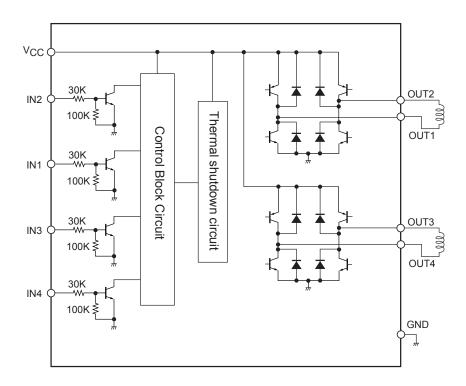
(1) Timing chart for 2-phase drive



(2) Timing chart for 1-2 phase drive



Block Diagram



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