

D SERIES

SMD VIDEO DELAY LINES

- Surface mount compatible
- Video performance
- Small size, low cost
- 50, 75 or 200 ohms

This range of SMD delay lines is suitable for automatic placement and reflow, and so do not add the excessive production costs associated with through hole devices. They are designed to delay video bandwidth signal with minimal distortion. The equalised lines allow small delay differences between signals to be matched and give excellent response to pulse and bar test signals. They may also be cascaded to give wider ranges of delay times.

Part number	Delay time	Group delay ripple	Impedance
DA01A	1 ns	< 0.5 ns	50 Ω
DA02A	2 ns	< 0.5 ns	50 Ω
DA05A	5 ns	< 1.0 ns	50 Ω
DA10A	10 ns	< 1.0 ns	50 Ω
DB01A	1 ns	< 0.5 ns	75 Ω
DB02A	2 ns	< 0.5 ns	75 Ω
DB05A	5 ns	< 1.0 ns	75 Ω
DB10A	10 ns	< 1.0 ns	75 Ω
DC01A	1 ns	< 0.5 ns	200 Ω
DC02A	2 ns	< 0.5 ns	200 Ω
DC05A	5 ns	< 1.0 ns	200 Ω
DC10A	10 ns	< 1.0 ns	200 Ω

Common specifications:

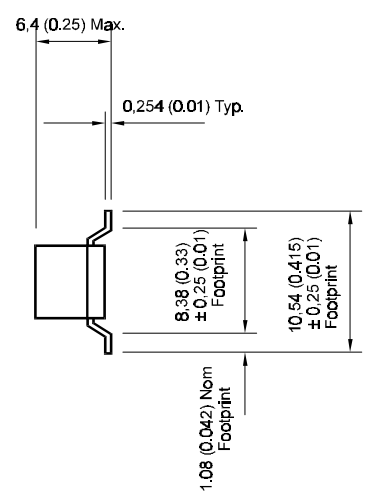
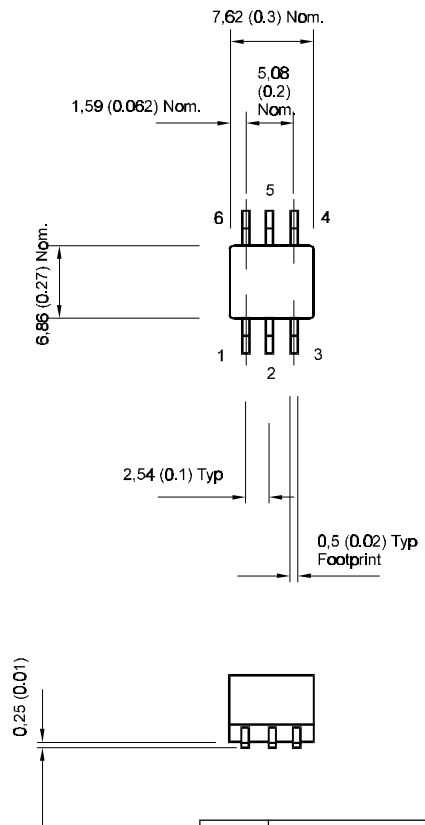
<i>Tolerance on delay time at 200 kHz</i>	± 2%
<i>Bandwidth</i>	> 5.5 MHz
<i>Amplitude ripple</i>	< 0.1 dB
<i>Return loss</i>	> 27 dB
<i>Pulse and bar K-rating(2T)</i>	< 0.5%
<i>Luma/Chroma Gain Inequality(20T)</i>	< 1%
<i>Luma/Chroma Delay Inequality(20T)</i>	< 5 ns
<i>Temperature range</i>	0 to 70°C
<i>Aqueous Washable</i>	Yes
<i>SMD Reflow Limitations as</i>	Data Sheet Fara309
<i>Package</i>	DR00142A

Other delay times and impedances supplied upon request.

PACKAGE DETAIL



All dimensions in millimetres (Inches). Gen Tolerance ± 0.05 (0.002) unless otherwise stated. DO NOT SCALE © Faraday Technology Ltd Croft Road Newcastle-U-Tyne ST5 0QZ England Tel (044) 01782 661501 Fax 630101



Notes;
 Pins Mat; Alloy 42 Lead Frame 90/10 Tin plated.
 Pins Size; 0,254 (0.01) x 0,5 (0.02)
 Pin Assignments; Pin 1 = Input
 Pin 6 = Output
 Pin 4 = Ground.
 Pin 5 is used internally. Do not connect to this pin.

Template: D100142



FARADAY TECHNOLOGY LIMITED

Change Note No:	Drawn: B A Knapper	Auth: C Snell	Title:	Drg No:
	Date: 07/04/98	Date: 07/04/98	FILTER ASSY OUTLINE	DR00142A