■ MN101C62D, MN101C62F

Гуре	MN101C62D [ES (Engineering Sample) available]	MN101C62F (under development)			
ROM (×8-bit)	64 K	96 K			
RAM (×8-bit)	2 K	4 K			
Package	LQFP080-P-1414A *Lead-free				
Minimum Instruction Execution Time					
	* The lower limit for operation guarantee for flash memor				
Interrupts	• Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 4 • Timer 6 • Time base • Timer 7 (2 systems) • Timer 8 (2 systems) • Automatic transfer completion • Serial 0 (2 systems) • Serial 1 (2 systems) • Serial 2 • A/D conversion finish • Key interrupt				
Timer Counter		frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation XI oscillation clock frequency; external clock input			
	Timer counter 1:8-bit × 1 (square-wave output, event council Clock source	frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC ncy; 1/1 of XI oscillation clock frequency; external			
	Timer counter 0, 1 can be cascade-connected.				
	time, serial baud rate timer) Clock source	ent, simple pulse width measurement, generation of real frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation XI oscillation clock frequency; external clock input are register 2			
	Timer counter 3: 8-bit × 1 (square-wave output, event count, generation of remote of clock source	frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC ncy; 1/1 of XI oscillation clock frequency; external			
	Timer counter 2, 3 can be cascade-connected.				
		frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation XI oscillation clock frequency; external clock input			
		quency; 1/1, 1/128, 1/8192 of OSC oscillation clock 1/8192 of XI oscillation clock frequency are register 6			

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Timer Counter (Continue)

Timer counter 7: 16-bit \times 1

(square-wave output, 16-bit PWM output (cycle / duty continuous variable), event count, synchronous output evevt, pulse width measurement, input capture, generation of real time)

Interrupt source ······ coincidence with compare register 7 (2 lines)

Watchdog timer

Interrupt source ------ 1/65536, 1/262144, 1/1048576 of system clock frequency

Timer counter 8:16-bit $\times 1$

(square-wave output, 16-bit PWM output (cycle / duty continuous variable), event count, synchronous output evevt, pulse width measurement, input capture, generation of real time)

Interrupt source coincidence with compare register 7 (2 lines)

Watchdog timer

Interrupt source ----- 1/65536, 1/262144, 1/1048576 of system clock frequency

Serial Interface

Serial 0: synchronous type/UART (full-duplex) × 1

Serial 1: synchronous type/UART (full-duplex) × 1

 $Clock\ source • \cdots • 1/2,\ 1/4\ of\ system\ clock\ frequency;\ pulse\ output\ of\ timer\ counter\ 2\ or\ 3;$

1/2,1/8 of timer counter 2 output;

1/2, 1/4, 1/16, 1/64 of OSC oscillation clock frequency

Serial 2 : synchronous type / single-master I²C

A/D Inputs 10-bit \times 8-ch. (with S/H)

Special Ports

Buzzer output, remote control carrier signal output, high-current drive port

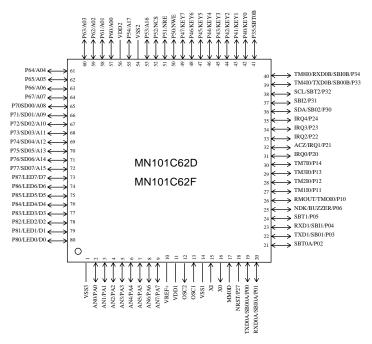
Electrical Characteristics

Supply current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	Offile
Operatingsupplycurrent	IDD1	fosc = 20 MHz, VDD = 5 V		18	30	mA
	IDD2	fx = 32 kHz, VDD = 3 V		30	60	μА
Supply current at HALT	IDD3	fx = 32 kHz, VDD = 3 V, Ta = 25°C		6	8	μА
	IDD4	fx = 32 kHz, VDD = 3 V, Ta = 85°C			30	μА
Supply ourrent at STOP	IDD5	VDD = 5 V, Ta = 25°C			2	μА
Supply current at STOP	IDD6	VDD = 5 V, Ta = 85°C			50	μА

See the next page for pin assignment and support tool.

Pin Assignment



LQFP080-P-1414A *Lead-free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101C62-LQFP080-P-1414A-M (under development)	
Flash Memory Built-in Type	Туре	MN101CF62G (under development)
	ROM (× 8-bit)	128 K
	RAM (× 8-bit)	10 K
	Minimum instruction execution time	0.1 μs (at 4.5 V to 5.5 V, 20 MHz)
		$0.25~\mu s$ (at $3.0V$ to $5.5~V,~8~MHz)$
		62.5 µs (at 3.0 V to 5.5 V, 32 kHz)
	Package	LQFP080-P-1414A *Lead-free

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