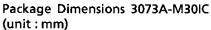


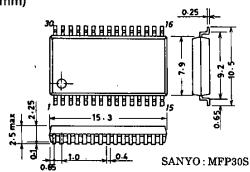
The LA6532M is a 4-channel BTL-use driver designed for compact disc pickup actuation.

Functions and Features

- \cdot BTL-use 4-channel power amp
- \cdot I_O max 700mA×2400mA×2 (with voltage limiter)
- · With muting function

Maximum Ratings at Ta = 25°C					unit		
Maximum Supply Voltage				9	v		
Allowable Power Dissipation	Pd max			0.9	W		
Differential Input Voltage	V_{ID}			8	V		
Common-Mode Input Voltage	V_{ICM}			8	V		
Maximum Input Voltage	V_{INB} max	Buffer amp		8	v		
Muting Pin Voltage	$V_{ m Mute}$	-		8	V		
Operating Temperature	Topr		-20 to +75		°C		
Storage Temperature	Tstg		-55 to +	150	$^{\circ}\mathrm{C}$		
Operating Conditions at Ta = 25°C					unit		
Maximum Supply Voltage	${ m v_{cc}}$			5	V		
Load Resistance	R_L	Pins 3-4,12-13,18-19,27-28		8	Ω		
Operating Characteristics at Ta = 25°C, V _{CC} = 5.0V				typ	max	unit	
No-Loaded Current Dissipation	1 I _{CC} 1	Note 1	25	40	60	mΑ	
No-Loaded Current Dissipation	$12 I_{CC}$	Note 2	5	9	2 0	mA	
No-Loaded Current Dissipation	13 I _{CC} 3	Note 3	25	40	60	mΑ	
No-Loaded Current Dissipation	4 I _{CC} 4	Note 4	5	9	20	mΑ	
Output Offset Voltage 1	$V_{ m OF}1$	Note 5 Amp 1-2,7-8	 50		50	mV	
Output Offset Voltage 2	$V_{ m OF}2$	Note 5 Amp 3-4,5-6	-30		30	mV	
Continued on next page							





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Con	tinued	from	preceding	nage.
COL		11 () 111	DICCOULLE	Duec.

Buffer 1 Input-Output	W1	Buffer amp 1	min	typ	max	unit
· · · · · · · · · · · · · · · · · · ·	ABIOT	buller amp 1	-30		30	mV
Voltage Difference	17 0	D 65 0				
Buffer 2 Input-Output	VBIOZ	Buffer amp 2	0.5	0.6	0.8	V
Voltage Difference						
Amp 2 Input-Output	$V_{IO}2$	Amp 2	0.5	0.6	0.8	V
Voltage Difference						
Amp 7 Input-Output	$V_{IO}7$	Amp 7	0.5	0.6	0.8	V
Voltage Difference						
Input Bias Current	$I_{\mathbf{B}}$	Note 6		100	500	nΑ
Buffer Input Voltage Range	V_{BICM}	Buffer amp	1.5	V_{CC}	-1.5	v
Common-Mode Input Voltage Range	V_{ICM}		1.0	V_{CC}	-1.5	v
Output Source Voltage	$V_{O}1$	$R_L = 8.0\Omega 700 \text{mA amp (Note 7)}$	3.4	3.6		V
Output Sink Voltage	$V_{O}2$	$R_L = 8.0\Omega$ 700mA amp (Note 8)		1.0	1.4	V
Output Source Voltage	v_{o3}	$R_L = 8.0\Omega 400 \text{mA} \text{ amp (Note 7)}$	2.8	3.4		V
Output Sink Voltage	$V_{O}4$	$R_L = 8.0\Omega 400 \text{mA amp (Note 8)}$		1.6	2.2	V
Closed-Circuit Voltage Gain	V_{G}			6.0		dB
Output Limiting Voltage	V_{OL}	Amp 3, amp 6		5.0		v
Muting Pin OFF-State Voltage	V_{Mute}			2.2		V
Muting Pin OFF-State Current	I_{Mute}			80		Α

Note 1 Muting OFF. Buffer 22kΩ across V_{IN} and V_O. V_{IN+} pin grounded

Note 2 Muting ON. Buffer $22k\Omega$ across V_{IN-} and V_{O} . V_{IN+} pin grounded

Note 3 Muting OFF. Buffer 22k Ω across $V_{\rm IN-}$ and $V_{\rm O}.$ $V_{\rm IN+}$ pin connected to 1/2V $_{\rm CC}$

Note 4 Muting ON. Buffer $22k\Omega$ across V_{IN-} and V_O . V_{IN+} pin connected to $1/2V_{CC}$

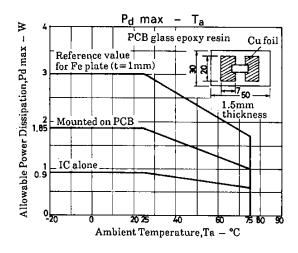
Note 5 For bridge amp, represents the difference between outputs.

Note 6 All V_{IN} connected to $1/2V_{CC}$. $100k\Omega$ connected to the input. Measure the voltage difference. V_{IN} and V_{O} connected through $100k\Omega$. Measure the voltage difference between pins.

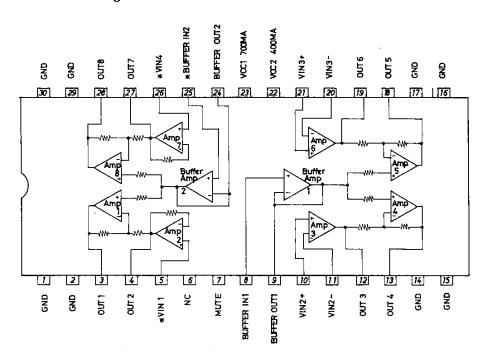
Note 7 Voltage (source) relative to GND when 8Ω load is connected across outputs of bridge amp

Note 8 Voltage (sink) relative to GND when 8Ω load is connected across outputs of bridge amp

※ : Be carefull in handling the LA6532M, because dielectric breakdown is liable to occur.



Equivalent Circuit Block Diagram



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