

#### Description

The function of this low reset Type IC is to accurately reset systems after detecting the supply voltage at the time of switching power on and instantaneous power off in various CPU and other logic system. Further, this IC, with its super low consumption current is most suited as a voltage check circuit for a number of products which use batteries.

#### Features

- Super low current consumption ( $I_{CCL} = 1.0$ uA Typ.)
- High current of output transistor (I<sub>OL</sub>=20mA Typ.)
- Hysteresis circuit built in ( $\triangle V_S$ =100mV Typ.)
- It has on delay function to supplement the constant of outer C and R.

#### Applications

- Reset circuits for microcomputers, CPU and MPU.
- Reset circuit for logic circuitry.
- Battery voltage check circuit.
- Circuit for changing over to backup battery.
- Level detecting circuit.

### **Ordering Information**



KSI-9046-000

### **Equivalent Circuit Diagram**



### Maximum ratings

Maximum ratings		(Ta=25°C)		
Characteristic	Symbol Ratings		Unit	
Supply voltage	Vcc	-0.3 ~ +10	V	
Power Dissipation (Package Limitation)	PD	500	mW	
Operating Temperature	Topr	-20~+75	°C	
Storage Temperature	Tstg	-40 ~ +125	°C	

### **Electrical Characteristics**

(Vcc=5V, Ta=25°C) Test Characteristic **Test Condition** Min. Symbol Typ. Max. Unit Circuit Detecting Voltage VS \* See Table 1 1 40 Hysteresis Voltage  $\Delta VS$ 1  $RL=470 \Omega, VCC=L \rightarrow H \rightarrow L$ 100 300 mVTemperature Coefficient  $VS/\,\Delta\,T$ 1 RL=470 Ω, Ta= -20~75 °C - $\pm 0.01$ -%/℃ Of detecting voltage Low Level Output voltage VOL 1 RL=470 \, VCC= VS Min 0.1 0.4 V -Circuit current at ON ICCL 1 RL= $\infty \Omega$ , VCC= VS Min 100 180 μΑ Circuit current at OFF ICCH 1 RL=∞ Ω,VCC=VS Max +0.1V 2.5 \_ 1.0 μΑ Threshold Operating Voltage 1 RL=4.7 kΩ, VOL  $\leq$  0.4V 1.4 1.6 V Vopr -Output Current at ON I IOL 1 1 RL=0 \, VCC= VS Min 10 20 mA -RL=0 \, VCC= VS Min, Output Current at ON II IOL 2 1 5 \_ mA Ta= -20~75 ℃ RL=4.7 kΩ, CL=100 pF L→H Transmission delay time tPLH 2 100 500 μs H→L Transmission delay time tPHL 2 RL=4.7 kΩ, CL=100 pF 10 20 μs

### \* Table 1

**Electrical Characteristics** 

(Vcc=5V, Ta=25°C)

Characteristic	Symbol	Test Circuit	Test Condition		Min.	Тур.	Max.	Unit
Detecting voltage VS		1	RL=470 $\Omega$ VCC=H→L VOL≤0.4V	S72N45	4.30	4.5	4.70	
				S72N42	4.00	4.2	4.40	
				S72N39	3.70	3.9	4.10	
				S72N36	3.40	3.6	3.80	
	VS			\$72N33	3.10	3.3	3.50	
	vo			S72N31	2.90	3.1	3.30	
				S72N29	2.75	2.9	3.05	
				S72N27	2.55	2.7	2.85	
				S72N25	2.35	2.5	2.65	
				S72N23	2.15	2.3	2.45	



### **Application Circuit**

### (1) Battery Low Indicator



### (Note)

(1) Connecting of LED and R2 obtains a voltage drop indicator.

(2) Connecting of C1 and selection of time constant with C1 and R1 set the power on delay time.

### **Electrical Characteristic Curves**



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Fig. 9  $C_L - t_{PLH}$ 



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