Silicon N-Channel MOS FET

# HITACHI

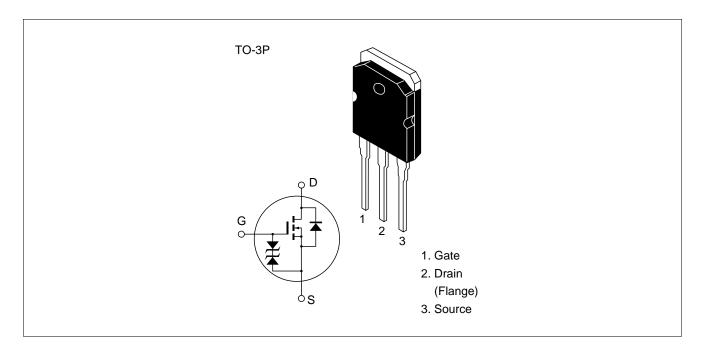
## Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

#### Outline





## Absolute Maximum Ratings (Ta = $25^{\circ}$ C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1161	V <sub>DSS</sub>	450	V
	2SK1162		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	10	А
Drain peak current		l★1 D(pulse)	30	А
Body to drain diode reverse	e drain current	I <sub>DR</sub>	10	А
Channel dissipation		Pch* <sup>2</sup>	100	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at  $T_c = 25^{\circ}C$ 

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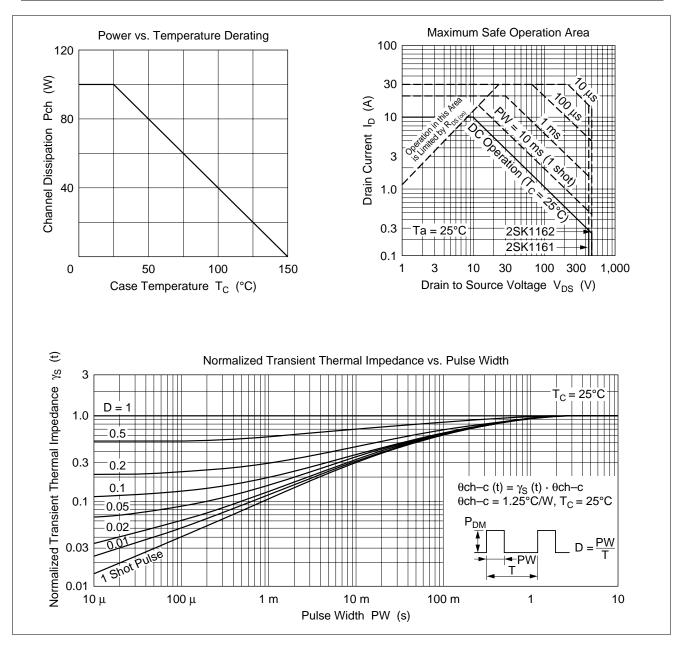
## **Electrical Characteristics** (Ta = 25°C)

Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1161	$V_{(BR)DSS}$	450	_	_	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
breakdown voltage	2SK1162	-	500	_			
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current		I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage	2SK1161	I <sub>DSS</sub>	_	_	250	μA	$V_{\rm DS} = 360 \text{ V}, \text{ V}_{\rm GS} = 0$
drain current	2SK1162	-					$V_{\rm DS} = 400 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff	voltage	$V_{\text{GS(off)}}$	2.0		3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static Drain to source	2SK1161	$R_{DS(on)}$	—	0.6	0.8	Ω	$I_{\rm D} = 5$ A, $V_{\rm GS} = 10$ V * <sup>1</sup>
on state resistance	2SK1162	-	—	0.7	0.9	-	
Forward transfer adm	ittance	yfs	4.0	7.0	_	S	$I_{\rm D} = 5$ A, $V_{\rm DS} = 10$ V $^{*1}$
Input capacitance		Ciss	—	1050		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance		Coss	_	280	_	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	40		pF	-
Turn-on delay time		t <sub>d(on)</sub>	_	15		ns	$I_{\rm D} = 5 \text{ A}, V_{\rm GS} = 10 \text{ V},$
Rise time		t,	—	60	—	ns	$R_{L} = 6 \Omega$
Turn-off delay time		t <sub>d(off)</sub>	—	90		ns	
Fall time		t <sub>f</sub>	_	45	_	ns	-
Body to drain diode fo voltage	orward	$V_{DF}$	_	1.0	—	V	$I_{F} = 10 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time		t <sub>rr</sub>	_	350	—	ns	$I_{F} = 10 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 100 \text{ A}/\mu\text{s}$
Note: 1 Pulse test							

Note: 1. Pulse test

See characteristic curves of 2SK1157, 2SK1158.

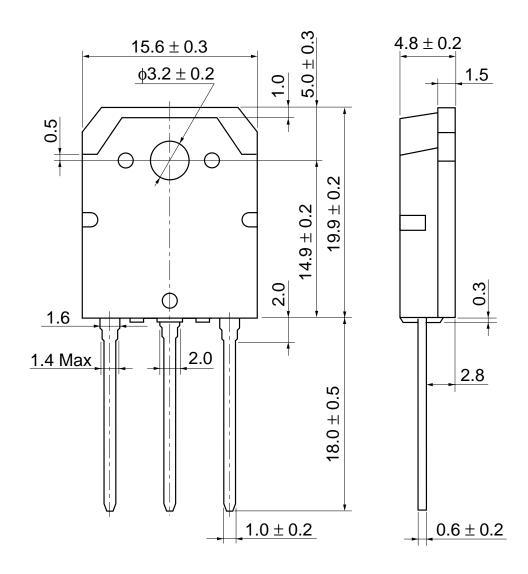
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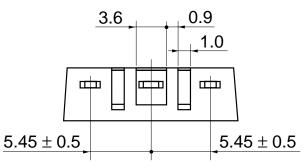


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Unit: mm



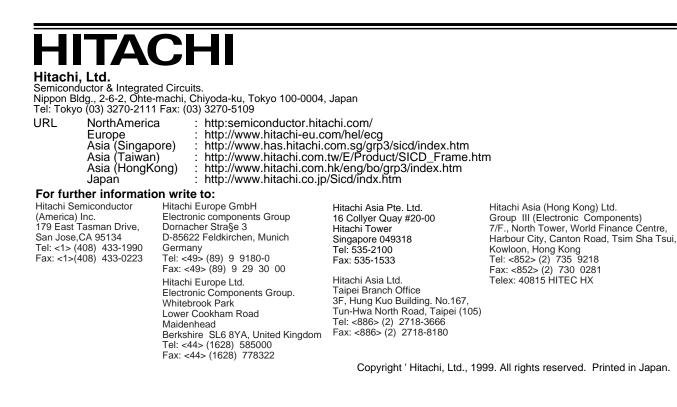


Hitachi Code	TO-3P		
JEDEC			
EIAJ	Conforms		
Weight (reference value)	5.0 g		

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