

5408/DM5408/DM7408 Quad 2-Input AND Gates

General Description

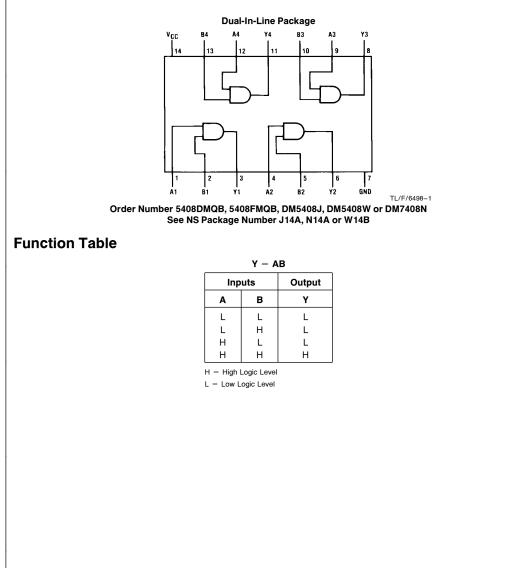
Features

This device contains four independent gates each of which performs the logic AND function.

 Alternate Military/Aerospace device (5408) is available. Contact a National Semiconductor Sales Office/Distributor for specifications. 5408/DM5408/DM7408 Quad 2-Input AND Gates

June 1989

Connection Diagram



©1995 National Semiconductor Corporation TL/F/6498

RRD-B30M105/Printed in U. S. A.

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54 and 54	-55°C to +125°C
DM74	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM5408			DM7408			Units
Cymbol		Min	Nom	Max	Min	Nom	Max	onnto
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High Level Input Voltage	2			2			V
VIL	Low Level Input Voltage			0.8			0.8	V
I _{OH}	High Level Output Current			-0.8			-0.8	mA
I _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min$, $I_I = -12 \text{ mA}$				-1.5	V
V _{OH}	High Level Output Voltage	$\begin{array}{l} V_{CC}=\text{Min}, I_{OH}=\text{Max} \\ V_{IL}=\text{Max} \end{array}$		2.4	3.4		V
V _{OL}	Low Level Output Voltage	$\label{eq:V_CC} \begin{split} V_{CC} &= \text{Min}, \text{I}_{OL} = \text{Max} \\ V_{IH} &= \text{Min} \end{split}$			0.2	0.4	V
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
IIH	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.6	mA
los	Short Circuit	V _{CC} = Max	DM54	-20		-55	mA
	Output Current (Note 2)	DM74	-18		-55		
ICCH	Supply Current with Outputs High	V _{CC} = Max			11	21	mA
ICCL	Supply Current with Outputs Low	V _{CC} = Max			20	33	mA

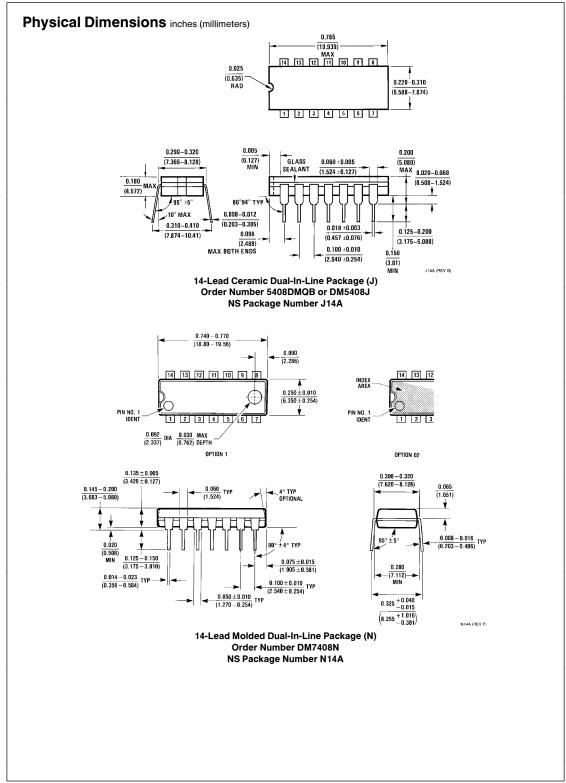
Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 400 \Omega$		27	ns
t _{PHL}	Propagation Delay Time High to Low Level Output			19	ns

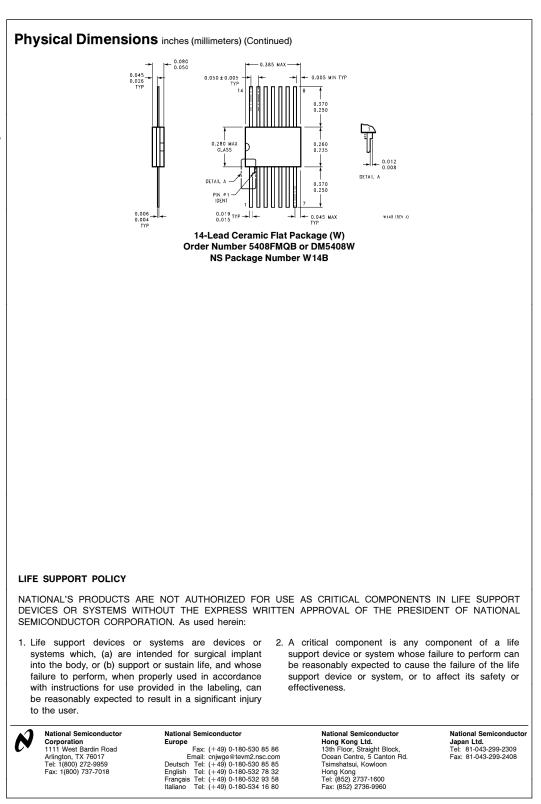
Note 1: All typicals are at V_{CC}\,=\,5V,\,T_{A}\,=\,25^{\circ}C.

Note 2: Not more than one output should be shorted at a time.

2



3



National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications