TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

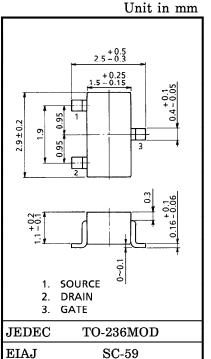
2 S K 3 6 8

AUDIO FREQUENCY AND HIGH VOLTAGE AMPLIFIER APPLICATIONS CONSTANT CURRENT APPLICATIONS

- High Breakdown Voltage : V_{GDS} = -100V (Min.)
- High Input Impedance : $I_{GSS} = -1.0nA (Max.) (V_{GS} = -80V)$
- Small Package

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Gate-Drain Voltage	V _{GDS}	-100	V
Gate Current	IG	10	mA
Drain Power Dissipation	PD	150	mW
Junction Temperature	Тј	125	°C
Storage Temperature Range	T _{stg}	$-55 \sim 125$	°C



Marking	
	Type Name
	IDSS Rank
KAIY	
вв	

TOSHIBA	2-3F1B

Weight : 0.012g

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

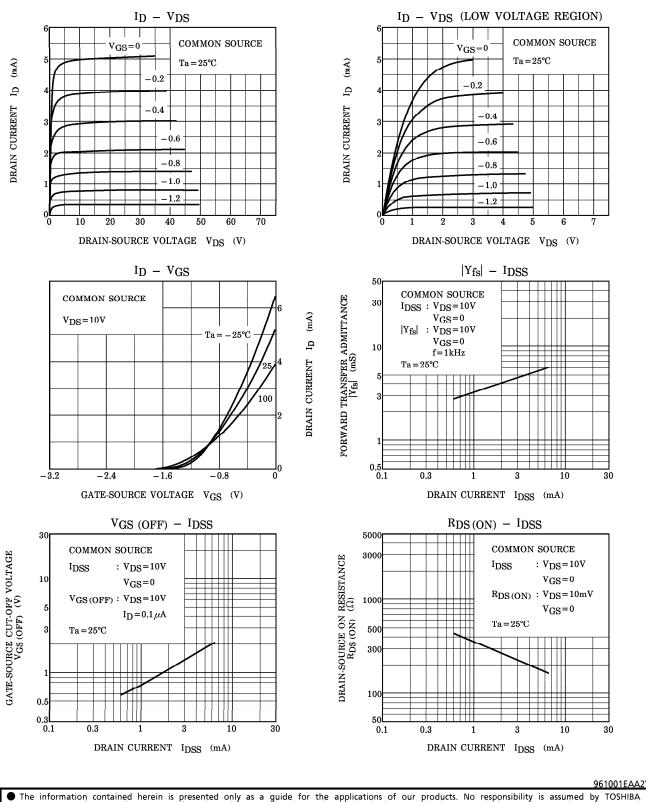
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Cut-off Current	IGSS	$V_{GS} = -80V, V_{DS} = 0$	_		-1.0	nA
Gate-Drain Breakdown Voltage	V (BR) GDS	$V_{DS}=0, I_{G}=-100 \mu A$	-100	_	_	v
Drain Current	IDSS (Note)	$V_{DS} = 10V, V_{GS} = 0$	0.6		6.5	mA
Gate-Source Cut-off Voltage	V _{GS (OFF)}	$V_{DS} = 10V, I_D = 0.1 \mu A$	-0.4		-3.5	V
Forward Transfer Admittance	Y _{fs}	V_{DS} =10V, V_{GS} =0, f=1kHz	1.5	4.6	_	mS
Input Capacitance	Ciss	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$	_	13	—	pF
Reverse Transfer Capacitance	C _{rss}	V _{DG} =10V, I _D =0, f=1MHz	_	3	—	pF
Noise Figure	NF	$V_{DS}=10V, V_{GS}=0$ $R_{G}=100k\Omega, f=100Hz$		0.5	_	dB

Note : IDSS Classification $O: 0.6 \sim 1.4 \text{mA}$, $Y: 1.2 \sim 3.0 \text{mA}$, $GR(G): 2.6 \sim 6.5 \text{mA}$

961001EAA2

● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

ŋ



The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others. The information contained herein is subject to change without notice.

