

LM748 Operational Amplifier

General Description

The LM748 is a general purpose operational amplifier with external frequency compensation.

The unity-gain compensation specified makes the circuit stable for all feedback configurations, even with capacitive loads. It is possible to optimize compensation for best high frequency performance at any gain. As a comparator, the output can be clamped at any desired level to make it compatible with logic circuits.

The LM748C is specified for operation over the 0°C to +70°C temperature range.

Features

- Frequency compensation with a single 30 pF capacitor
- Operation from ±5V to ±20V
- Continuous short-circuit protection
- Operation as a comparator with differential inputs as
- high as $\pm 30V$ ■ No latch-up when common mode range is exceeded
- Same pin configuration as the LM101

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Absolute Maximum Rating	S		
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.		Input Voltage (Note 2)	±15V
		Output Short-Circuit Duration (Note 3)	
		Operating Temperature Range:	
Supply Voltage	±22V	LM748C	0°C to +70C
Power Dissipation (Note 1)	500 mW	Storage Temperature Range	-65°C to +150°C
Differential Input Voltage	$\pm 30V$	Lead Temperature (Soldering, 10 sec.)	+ 300°C

Electrical Characteristics (Note 4)

Parameter	Conditions	Min	Тур	Max	Units
Input Offset Voltage	$T_{A}=$ 25°C, $R_{S}\leq$ 10 k Ω		1.0	5.0	mV
Input Offset Current	$T_A = 25^{\circ}C$		40	200	nA
Input Bias Current	$T_A = 25^{\circ}C$		120	500	nA
Input Resistance	$T_A = 25^{\circ}C$	300	800		kΩ
Supply Current	$T_A = 25^{\circ}C, V_S = \pm 15V$		1.8	2.8	mA
Large Signal Voltage Gain	$\begin{array}{l} T_A=25^{\circ}\text{C}, V_S=\pm15\text{V}\\ V_{OUT}=\pm10\text{V}, \text{R}_L\geq2\text{k}\Omega \end{array}$	50	160		V/mV
Input Offset Voltage	${\sf R}_{\sf S} \le$ 10 k Ω			6.0	mV
Average Temperature Coefficient of Input Offset Voltage	${\sf R}_{\sf S} \le 50 \Omega$		3.0		μV/°C
	$R_{S} \leq 10 \ k\Omega$		6.0		μV/°C
Input Offset Current	$T_A = 0^{\circ}C \text{ to } + 70^{\circ}C$			300	nA
	$T_A = -55^{\circ}C \text{ to } + 125^{\circ}C$			500	nA
Input Bias Current	$T_A = 0^{\circ}C \text{ to } + 70^{\circ}C$			0.8	μΑ
	$T_A = -55^{\circ}C \text{ to } + 125^{\circ}C$			1.5	μΑ
Supply Current	$T_A = +125^{\circ}C, V_S = \pm 15V$		1.2	2.25	mA
	$T_A = -55^{\circ}C \text{ to } + 125^{\circ}C$		1.9	3.3	mA
Large Signal Voltage Gain	$\label{eq:VS} \begin{array}{l} V_S = \ \pm 15 V, V_{OUT} = \ \pm 10 V \\ R_L \geq 2 \ k \Omega \end{array}$	25			V/mV
Output Voltage Swing	$V_{S}=\pm15V,$ $R_{L}=10~k\Omega$	±12	±14		v
	$V_{S}=\pm15V,$ $R_{L}=2k\Omega$	±10	±13		V
Input Voltage Range	$V_{S} = \pm 15V$	±12			v
Common-Mode Rejection Ratio	$R_{S} \leq 10 \ k\Omega$	70	90		dB
Supply Voltage Rejection Ratio	$R_S \le 10 \ k\Omega$	77	90		dB

Note 1: For operating at elevated temperatures, the device must be derated based on a maximum junction to case thermal resistance of 45°C per watt, or 150°C per watt junction to ambient. (See Curves).

Note 2: For supply voltages less than \pm 15V, the absolute maximum input voltage is equal to the supply voltage.

Note 3: Continuous short circuit is allowed for case temperatures to $+125^{\circ}$ C and ambient temperatures to $+70^{\circ}$ C.

Note 4: These specifications apply for $\pm5V$ \leq V_S \leq +15V and 0°C \leq T_A \leq $+70^{o}C$, unless otherwise specified.





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