

OGY Thermocouple Cold Junction Compensator and Matched Amplifier

FEATURES

- 0.75°C Initial Accuracy (A Version)
- **■** Extremely Low Warmup Drift
- Preset Outputs for Type E, J, K, R, S, T
- Single 5V to ± 20V Operation
- 480μA Typical Supply Current

RPPLICATIONS

■ Thermocouple Cold Junction Compensation

DESCRIPTION

The LTK001 is a thermocouple amplifier supplied with a matched cold junction compensator. By separating the amplifier and compensator functions, the problem of compensator temperature rise is virtually eliminated. The compensator is a selected version of the LT1025 cold junction compensator. The amplifier, which is also available separately as LTKA0x has been specially selected for thermocouple applications. It has low supply current to minimize warmup drift, very low offset voltage ($<35\mu V$), high gain,

and extremely low input bias currents (<600pA) to allow high impedance input filters to be used without degrading offset voltage or drift.

Matching of the kits is accomplished by separating the compensators and amplifiers according to the polarity of their initial (room temperature) errors. This eliminates the need to sum the errors of the two components to find the worst-case error.

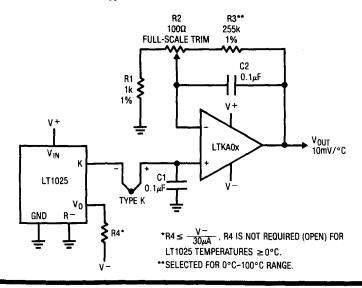
The LTK001 has direct thermocouple outputs of $60.9\mu\text{V}/^{\circ}\text{C}$ (E), $51.7\mu\text{V}/^{\circ}\text{C}$ (J), $40.6\mu\text{V}/^{\circ}\text{C}$ (K, T), and $5.95\mu\text{V}/^{\circ}\text{C}$ (R, S). It also has a 10mV/ $^{\circ}\text{C}$ output which can be scaled to match any arbitrary thermocouple.

The amplifier in the LTK001 kit is available in an 8-pin plastic miniDIP for 0°C to 70°C operation and an 8-pin TO-39 metal can for -55°C to +125°C operation. The compensator is available in 8-pin plastic miniDIP for 0°C to 70°C operation and 8-pin ceramic miniDIP for -55°C to +125°C operation.

For multiple thermocouple applications using one compensator, amplifiers may be ordered separately (LTKA0x), still matched to the compensator.

For typical performance curves and applications circuits consult the LT1025 data sheet.

Type K 10mV/°C Thermometer

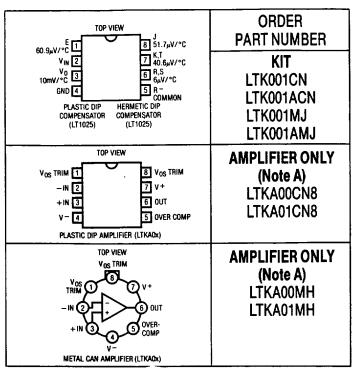




ABSOLUTE MAXIMUM RATINGS

Amplifier (LTKA0x)
Supply Voltage (Total V + to V -)40V
Differential Input Current (Note 1) ± 10mA
Common-Mode Input Voltage Equal to Supplies
Output Short Circuit Duration Indefinite
Compensator (LT1025)
Supply Voltage (V _{IN} to Ground Pin)
Output Voltage (Forced)5V
Output Short Circuit Duration Indefinite
Both Devices
Operating Temperature Range
LTK001AMJ, LTK001MJ – 55°C to 125°C
LTK001ACN, LTK001CN
Storage Temperature Range 65°C to 150°C
Lead Temperature Range (Soldering, 10 sec.) 300°C

PACKAGE/ORDER INFORMATION



Note A: The polarity of the amplifier is indicated by the 0 or 1 in the part number. An LT1025 with a 0 identifier is properly matched with an LTKA00 while an LT1025 with a 1 identifier should be used with an LTKA01.

ELECTRICAL CHARACTERISTICS — MATCHED AMPLIFIER AND COMPENSATOR $T_A = 25$ °C, $V_S = \pm 15$ V (Amplifier), $V_S = 5$ V (Compensator)

DARAMETER	COMPITIONS	LTK001A			LTK001				
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS	
Total Temperature Error @25°C		Type E			0.75			2.5	°
(Note 2)		Type J			0.75			2.5	°
	:	Type K, T			0.86			2.5	°C
		Type R, S	(Note 11)		5.0			5.0	°C
Slope Error (Notes 3 and 8)	0°C≤T _i ≤70°C	Type E			0.05			0.09	°CI°C
	·	Type J			0.06		-	0.09	°C/°C
		Type K, T			0.07			0.10	°C/°C
		Type R, S			0.28			0.32	°C/°C
Total Temperature Error at Temperature Extremes (Note 8)	0°C≤T _i ≤70°C	Type E			2.0			5	°C
	·	Type J			2.1			5	°C
		Type K, T			2.6			5.2	°C
		Type R, S	(Note 11)		16			16	°C
	-55°C≤T _j ≤125°C	Type E			6			8.5	°C
		Type J			6			8.5	°C
		Type K, T			6.3			9	°C
		Type R, S	(Note 11)		30			30	°C
Temperature Error Change with Supply Voltage (Note 4)					0.1			0.1	°C/V
Supply Current		<u> </u>		480	900		480	900	μА

ELECTRICAL CHARACTERISTICS COMPENSATOR (LT1025) $v_s = 5V$ unless otherwise specified

	CONDITIONS			COMPENSATOR (
PARAMETER				MIN TYP	MAX	UNITS
Temperature Error at 10mV/°C Output (Note 8)	T _i = 25°C	LTK001A	\top	0.3	0.5	°C
	1	LTK001		0.5	2.0	°C
	Full Temper	rature Span	•	See Curve on LT1025		
Temperature Error at Individual Outputs (Note 9)	LTK001A	E, J, K, T		0.4	0.75	°C
		R, S		0.4	1.5	°C
	LTK001	E, J, K, T		0.8	2.4	°C
		R, S		1.2	3.5	°C
	Full Tempe	rature Span	•	See Curve on LT1025	Data Sheet	
Supply Current	4V≤V _{IN} ≤3	6V		80	100	μА
	0°C≤T _i ≤7	0°C	•		150	μA
	-55°C≤T		•		200	μА
Change in Supply Current	4V ≤ V _{IN} ≤ 36V		- - -	0.01	0.05	μAN
Line Regulation (Note 10)	4V ≤ V _{IN} ≤ 36V 10mV/°C Output		•	0.003	0.02	°C/V
Load Regulation (Note 10)	0≤I ₀ ≤1mA 10mV/°C Output		•	0.04	0.2	°C
Divider Impedance		E		2.5		kΩ
		J		2.1		kΩ
		K, T	$\neg \neg$	4.4		kΩ
		R, S		3.8		kΩ

ELECTRICAL CHARACTERISTICS AMPLIFIER (LTKAOx) $V_S = \pm 15V$, $V_{CM} = 0$, $T_j = 25^{\circ}$ C, unless otherwise specified

PARAMETER	CONDITIONS			AI MIN	UNITS		
Input Offset Voltage	CONDITIONS		- 	141114	10	35	μV
Input Offset Voltage Drift with Temperature	(Note 5)	•		0.3	1.5	μV/°C	
Input Bias Current	0°C≤T _A ≤70°C -55°C≤T _A ≤12	•		± 200 ± 300	± 600 ± 1500	pA pA	
Input Bias Current Drift with Temperature	(Note 5)		•		1	5	pA/°C
Input Offset Current	0°C≤T _A ≤70°C -55°C≤T _A ≤125°C		•		± 100 ± 200	±500 ±700	pA pA
Input Offset Current Drift with Temperature	(Note 5)		•		0.6	4	pA/°C
Large Signal Voltage Gain	$R_1 = 10k\Omega$		•	400	2000		V/mV
Common-Mode Rejection Ratio	$V_{CM} = \pm 13.5V$		•	106	130		dB
Power Supply Rejection Ratio	± 2.5V ≤ V _S ≤ ± 20V (Note 4)		•	106	125		dB
Common-Mode Input Voltage Range	Notes 5 and 6	Above V		0.75			V
		Below V+				1.0	V
Output Voltage Swing (Notes 5 and 7)	Referred to Supplies	I _{OUT} = 0.1mA			0.8		V
		I _{OUT} = 1mA			1.1		V
Supply Current			•		400	800	μА
Supply Voltage Range	Total V ⁺ to V ⁻ Voltage		•	4.5		40	V

The • denotes the specifications which apply over the full operating temperature range.

Note 1: The inputs of the LTKA0x amplifier are clamped with diodes, so a differential voltage rating does not apply.

Note 2: Total temperature error is the overall error at 25°C taking into account the offset of the amplifier, the offset at the compensator 10mV/°C output, and the error in the compensator divider network. Warmup drift is not included.

Note 3: Slope error is the increase in total temperature error as ambient temperature is increased. It is guaranteed by design and by other tests, but is not tested directly.

Note 4: This is a worst-case limit assuming that any or all supply voltages change.

Note 5: Guaranteed, but not tested.

Note 6: By referring common-mode range to the supplies, the range referred to ground can be quickly calculated for any given supply voltage. With a single 5V supply, for instance, which has a worst-case low value of 4.7V, the upper common-mode limit is 4.7V - 1V = 3.7V. The lower common-mode limit is 0V + 0.75V = 0.75V. With $\pm 15V$ supplies, the limits would be + 14V and - 14.25V, respectively. Common-mode range has a temperature sensitivity of $\approx 2mV/^{\circ}C$.

Note 7: Absolute output voltage swing is calculated by subtracting the

given limits from actual supply voltage. These limits indicate the point where offset voltage has changed suddenly by 5_µV.

Note 8: Temperature error is defined as the deviation from the following formula:

 $V_{OUT} = \alpha(T) + \alpha\beta(T - 25^{\circ}C)^{2}$

 α = Typical thermoucouple Seebeck coefficient as follows, E = 60.9 μ V/°C, J = 51.7 μ V/°C, K, T = 40.6 μ V/°C, R, S = 5.95 μ V/°C. α = 10mV/°C at the 10mV output.

 β = Nonlinearity coefficient built into the LT1025 to help compensate for the nonlinearities of thermocouples. β = 5.5 × 10⁻⁴, generating 0.34°C bow for 25°C temperature change, and 1.36°C bow for 50°C change.

Note 9: Temperature error at the individual outputs is the sum of the 10mV/°C output error plus the resistor divider error.

Note 10: Line and load regulation do not take into account the effects of self-heating. Output changes due to self-heating can be calculated as follows:

 ΔV_{OUT} (Line) = $\Delta V_{IN}(I_q + I_{load})$ (150°C/W) ΔV_{OUT} (Load) = (ΔI_{load}) (V_{IN}) (150°C/W) I_q = LT1025 supply current

Load regulation is $30\mu A \le l_0 \le 1 \text{mA}$ for $T_A \le 0$ °C.

Note 11: Larger errors with type R and S thermoucouples are due mostly to $35\mu V$ offset of the amplifier. This error can be reduced to $5\mu V$ max with the LTC1050 or LTC1052 operational amplifiers.

PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

H Package J Package N Package 8 Lead TO-5 Metal Can 8 Lead Molded DIP 8 Lead Hermetic DIP 0.405 (10.287) MAX 0.400 (10.160) MAX 6 8 7 6 5 (1.270)(4.191 - 4.699)REFERENCE SEATING GAUGE PLANE 0.500 - 0.750(12.70 - 19.05) 1 2 3 4 1 2 3 4 0 00 (1.397) 0.130 ± 0.005 (3.302 ± 0.127) 0.045 - 0.065(1.143 - 1.651)0.016 - 0.0210.015 0.027 - 0.045 (0.686 - 1.143) 0.027 **–** 0.034 (1.143 ± 0.381) 0.100 ± 0.010 (2.540 ± 0.254 0.100 ± 0.010 (2.540 ± 0.254) $\frac{0.018 \pm 0.003}{(0.457 \pm 0.076)}$ BSC 2 794 - 4 064 0.008 - 0.018 (0.203 - 0.460)0.325 + 0.025 NOTE: LEAD DIAMETER IS UNCONTROLLED BETWEEN THE REFERENCE PLANE AND SEATING PLANE. 0.385 ± 0.025 (9.779 ± 0.635) θ_{JA} θ_{iC} $\theta_{j\text{A}}$ IMAX θ_{iA} $\mathsf{\Gamma}_{\mathsf{IMAX}}$ 150°C/W 100°C/W 100°C 130°C/W