Dual Channel, High Speed, High Current Line Driver w/3-State

Features

- 3-State output
- 3V and 5V input compatible
- Clocking speeds up to 10 MHz
- 20 ns Switching/delay time
- 2A Peak drive
- Low, matched output impedance—5Ω
- Low quiescent current—2.5 mA
- Wide operating voltage— 4.5V--16V

Applications

- Parallel bus line drivers
- EPROM and PROM programming
- Motor controls
- Charge pumps
- Sampling circuits
- Pin drivers
- Bridge circuits

Ordering Information

Part No.	Temp. Range	Pkg.	Outline #
EL7232CN	-40°C to +85°C	8-Pin P-DIP	MDP0031
EL7232CS	-40°C to +85°C	8-Pin SO	MDP0027

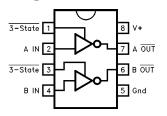
Truth Table

3-State	Input	Output
1	0	1
1	1	0
0	0	Open
0	1	Open Open

General Description

The EL7232C 3-state drivers are particularly well suited for ATE and microprocessor based applications. The low quiescent power dissipation makes this part attractive in battery applications. The 2A peak drive capability, makes the EL7232C an excellent choice when driving high speed capacitive lines, as well. The input circuitry provides level shifting from TTL levels to the supply rails. The EL7232C is available in 8-pin P-DIP and 8-lead SO packages.

Connection Diagram



7232-1

Manufactured under U.S. Patent Nos. 5,334,883, #5,341,047

January 1996 Rev.

Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.

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Absolute Maximum Ratings

Supply (V+ to Gnd) 16.5V Operating Junction Temperature 125°C

Input Pins -0.3V to +0.3V above V^+ Power Dissipation

Combined Peak Output Current 4A SOIC 570 mW Storage Temperature Range -65° C to $+150^{\circ}$ C PDIP 1050 mW

Ambient Operating Temperature -40°C to $+85^{\circ}\text{C}$

Important Note:

All parameters having Min/Max specifications are guaranteed. The Test Level column indicates the specific device testing actually performed during production and Quality inspection. Elantec performs most electrical tests using modern high-speed automatic test equipment, specifically the LTX77 Series system. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level Test Procedure

 $\begin{tabular}{ll} I&100\%&production tested and QA sample tested per QA test plan QCX0002.\\ II&100\%&production tested at $T_A=25^\circ$C and QA sample tested at $T_A=25^\circ$C$,} \end{tabular}$

 $T_{MAX} \ {\rm and} \ T_{MIN} \ {\rm per} \ {\rm QA} \ {\rm test} \ {\rm plan} \ {\rm QCX0002}.$ III QA sample tested per QA test plan QCX0002.

IV Parameter is guaranteed (but not tested) by Design and Characterization Data.

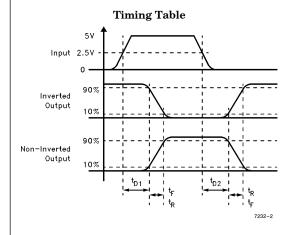
Parameter is typical value at $T_A = 25$ °C for information purposes only.

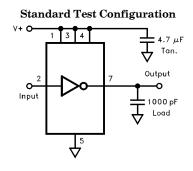
DC Electrical Characteristics T_A = 25°C, V = 15V unless otherwise specified

Parameter	Description	Test Conditions	Min	Тур	Max	Test Level	Units
Input							
v_{IH}	Logic "1" Input Voltage		2.4			I	v
I_{IH}	Logic "1" Input Current	@V+		0.1	10	I	μΑ
$V_{\rm IL}$	Logic "0" Input Voltage				0.8	I	v
I_{IL}	Logic "0" Input Current	@0V		0.1	10	I	μΑ
V_{HVS}	Input Hysteresis			0.3		v	v
Output							
R _{OH}	Pull-Up Resistance	$I_{OUT} = -100 \text{ mA}$		3	6	I	Ω
R _{OL}	Pull-Down Resistance	$I_{OUT} = +100 \text{ mA}$		4	6	I	Ω
$I_{ m OFF}$	3-State Output Leakage	$V_{OUT} = V + V_{OUT} = 0V$	0.2		10	I	μΑ
I_{PK}	Peak Output Current	Source Sink		2.0 2.0		IV	A
I_{DC}	Continuous Output Current	Source/Sink	100			I	mA
Power Supply							
I _S	Power Supply Current	Inputs High		1	2.5	I	mA
$\overline{v_{\rm S}}$	Operating Voltage		4.5		16	I	v

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AC Electrical Characteristics $\tau_A = 25^{\circ}C$, $V = 15V$ unless otherwise specified							
Parameter	Description	Test Conditions	Min	Тур	Max	Test Level	Units
Switching Chara	cteristics						
t_R	Rise Time	$C_{ m L} = 500 \ m pF$ $C_{ m L} = 1000 \ m pF$		7.5 10		IV	ns
t _F	Fall Time	$C_{L} = 500 \text{ pF}$ $C_{L} = 1000 \text{ pF}$		10 13	20	IV	ns
t _{D-ON}	Turn-On Delay Time			18	25	IV	ns
t _{D-OFF}	Turn-Off Delay Time			20	25	IV	ns

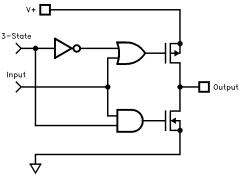




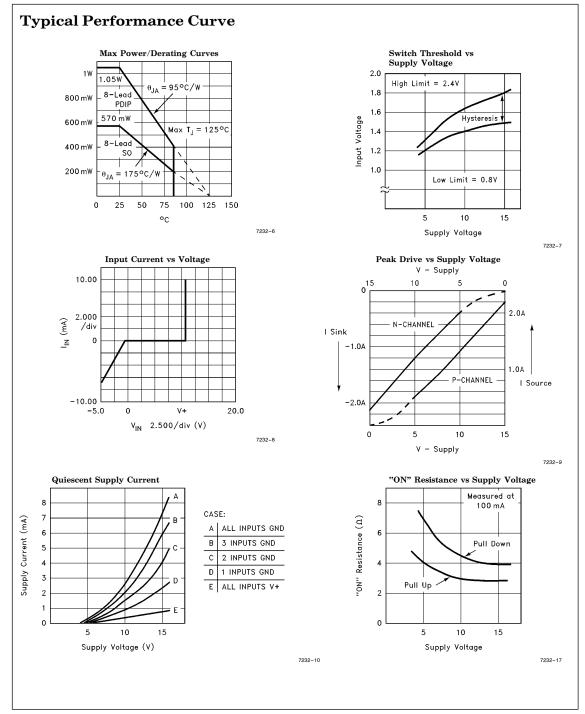
7232-4

7232-3

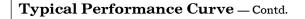
Simplified Schematic

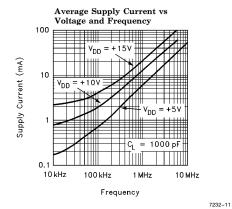


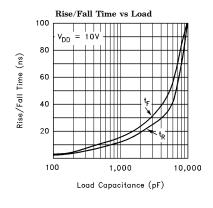
Dual Channel, High Speed, High Current Line Driver w/3-State

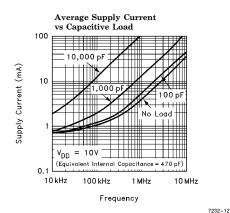


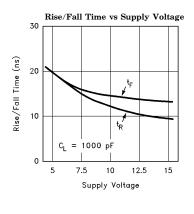
Dual Channel, High Speed, High Current Line Driver w/3-State





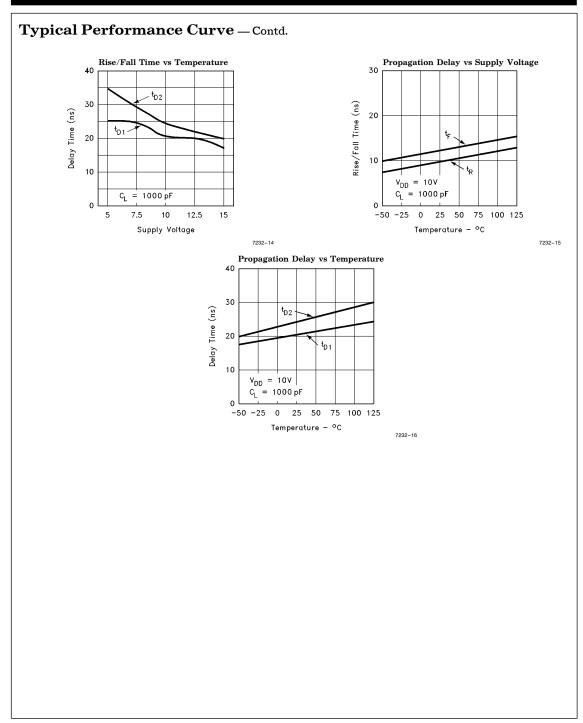






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Dual Channel, High Speed, High Current Line Driver w/3-State



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