

Addendum to Datasheet

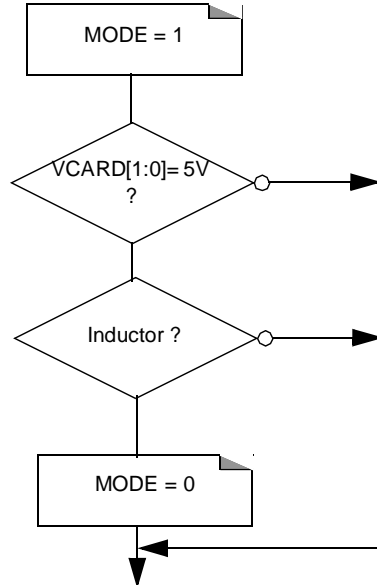
This document is an addendum for the Smart Card Interface block concerning the “Initialization Procedure” section of the AT8xC5122/3 datasheet, available on the Atmel web site.

Table 1. DCCKPS.7 (S:BFh) : MODE bit management

LI pin	MODE bit	CVCC DC/DC Output voltage		
		5V	3V	1.8V
With Inductor	Start	0	1 / 0 ⁽¹⁾	1 / 0 ⁽¹⁾
	After VCARDOK = 1	0	1	1
No inductor	Start	1	1	1
	After VCARDOK = 1	1	1	1

Notes: 1. 1 is recommended and 0 is allowed.

Figure 1. MODE bit start procedure



Refer to `scib_hw_dcdcstart` function in `scib_lib.c` & `config.h` from `scib_hid_usb_2_2_4.zip` package.



Smart Card Reader ICs

AT83C5122
AT83EC5122
AT85C5122
AT85EC5122
AT89C5122
AT83C5123
AT83EC5123

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