

GaAs INTEGRATED CIRCUIT μ PG2156TB

L-BAND 4 W SINGLE CONTROL HIGH POWER SPDT SWITCH

DESCRIPTION

The μ PG2156TB is an L-band single control SPDT GaAs FET switch which was developed for digital cellular or cordless telephone application. The device can operate from 800 MHz to 2.5 GHz, having the low insertion loss and high linearity.

FEATURES

Low insertion loss
 Lins = 0.45 dB TYP. @ V_{cont} = +2.6 V/0 V, f = 1.0 GHz, IN-OUT1

: Lins = 0.35 dB TYP. @ V_{cont} = +2.6 V/0 V, f = 1.0 GHz, IN-OUT2

: Lins = 0.45 dB TYP. @ V_{cont} = +2.6 V/0 V, f = 2.0 GHz, IN-OUT1/2

High power switching
 : Pin (0.1 dB) = 37 dBm TYP. @ Vcont = +2.6 V/0 V, f = 1.0 GHz, IN-OUT1/2

• 6-pin super minimold package (2.1 × 2.0 × 0.9 mm)

APPLICATION

· GSM Triple/Quad band digital cellular

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2156TB-E4	μPG2156TB-E4-A	6-pin super minimold (Pb-Free)	G4V	 Embossed tape 8 mm wide Pin 4, 5, 6 face the perforation side of the tape Qty 3 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: μ PG2156TB

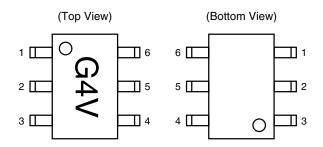
Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

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PIN CONNECTIONS



Pin No.	Pin Name	
1	OUT2	
2	GND	
3	OUT1	
4	V _{DD}	
5	IN	
6	Vcont	

ABSOLUTE MAXIMUM RATINGS ($T_A = +25$ °C)

Parameter	Symbol	Ratings	Unit
Supply Voltage	V _{DD}	+8.0	٧
Control Voltage	V _{cont}	+8.0	V
Input Power	Pin	+38	dBm
Operating Ambient Temperature	TA	-45 to +85	°C
Storage Temperature	Tstg	-55 to +150	°C

RECOMMENDED OPERATING RANGE (TA = +25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{DD}	+2.4	+2.6	+5.0	V
Control Voltage (High)	V _{cont (H)}	+2.4	+2.6	+5.0	V
Control Voltage (Low)	V _{cont (L)}	0	0	+0.2	V

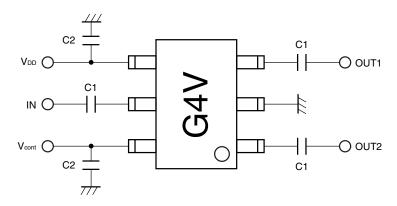


ELECTRICAL CHARACTERISTICS (TA = +25°C, VDD = +2.6 V, Vcont = +2.6 V/0 V, Zo = 50 Ω , off chip DC blocking capacitors value: 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	Pass	MIN.	TYP.	MAX.	Unit
Insertion Loss	Lins	f = 0.8 to 1.0 GHz	IN-OUT1	-	0.45	0.65	dB
			IN-OUT2	-	0.35	0.55	dB
		f = 1.0 to 2.0 GHz	IN-OUT1/2	-	0.45	0.65	dB
		f = 2.0 to 2.5 GHz	IN-OUT1/2	-	0.50	-	dB
Isolation	ISL	f = 0.8 to 1.0 GHz	IN-OUT1/2	21.0	23.0	-	dB
		f = 1.0 to 2.0 GHz	IN-OUT1/2	15.5	17.5	-	dB
		f = 2.0 to 2.5 GHz	IN-OUT1/2	-	16.0	-	dB
Input Return Loss	RLin	f = 0.8 to 1.0 GHz	IN-OUT1	10	15	-	dB
			IN-OUT2	15	20	-	dB
		f = 1.0 to 2.0 GHz	IN-OUT1/2	15	20	-	dB
		f = 2.0 to 2.5 GHz	IN-OUT1/2	-	20	-	dB
Output Return Loss	RLout	f = 0.8 to 1.0 GHz	IN-OUT1	10	15	-	dB
			IN-OUT2	15	20	-	dB
		f = 1.0 to 2.0 GHz	IN-OUT1/2	15	20	-	dB
		f = 2.0 to 2.5 GHz	IN-OUT1/2	-	20	-	dB
0.1 dB Loss Compression	Pin (0.1 dB)	f = 1.0 GHz	IN-OUT1/2	35	37	-	dBm
Input Power		f = 2.0 GHz	IN-OUT1/2	35	37	-	dBm
2nd Harmonics	2f0	f = 1.0 GHz, Pin = +35 dBm	IN-OUT1/2	65	75	-	dBc
		f = 2.0 GHz, P _{in} = +33 dBm	IN-OUT1/2	65	70	-	dBc
3rd Harmonics	3f0	f = 1.0 GHz, P _{in} = +35 dBm	IN-OUT1/2	65	75	-	dBc
		f = 2.0 GHz, Pin = +33 dBm	IN-OUT1/2	65	75	-	dBc
Switching Speed	tsw			_	1	5	μS
Control Current	Icont	RF Non		-	5	50	μΑ
Supply Current	loo	RF Non		_	40	200	μΑ

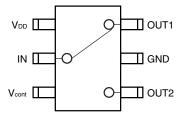
EVALUATION CIRCUIT

Off chip DC blocking capacitors value C1 = 56 pF, C2 = 1 000 pF (Bypass), using NEC standard evaluation board.



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

TRUTH TABLE

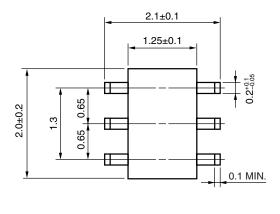


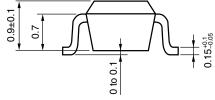
Vcont	IN-OUT1	IN-OUT2
High	OFF	ON
Low	ON	OFF

NEC μ PG2156TB

PACKAGE DIMENSIONS

6-PIN SUPER MINIMOLD (UNIT: mm)





RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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NEC μ PG2156TB

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

▶ For further information, please contact

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