Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L²-π-MOSV)

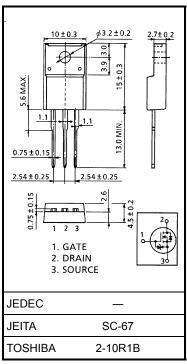
2SK2882

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance: RDS (ON) = 0.08Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 17 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \,\mu\text{A}$ (max) ($V_{DS} = 150 \,\text{V}$)
- Enhancement mode: $V_{th} = 0.8 \sim 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	150	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	150	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	18	А	
	Pulse (Note 1)	I_{DP}	54		
Drain power dissipation	n (Tc = 25°C)	P_{D}	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	176	mJ	
Avalanche current		I _{AR}	18	Α	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 50~V,~T_{ch} = 25^{\circ}C$ (initial), $L = 0.8~mH,~R_G = 25~\Omega,~I_{AR} = 18~A$

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

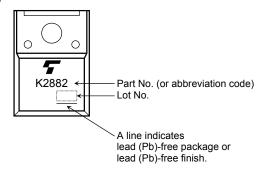
Electrical Characteristics (Ta = 25°C)

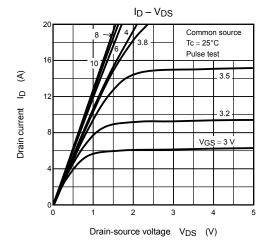
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 150 V, V _{GS} = 0 V	_	_	100	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	150	_	_	V
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	8.0	_	2.0	V
Drain-source ON resistance		Per (a)	V _{GS} = 4 V, I _D = 9 A	_	0.09	0.18	Ω
		R _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$	_	0.08	0.12	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 9 A	10	17	_	S
Input capacitance	;	C _{iss}			1380	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	200	_	pF
Output capacitance		Coss		_	610	_	pF
Switching time	Rise time	t _r	$V_{GS} = 9 \text{ A} $ $V_{GS} = 0 \text{ V}$ $V_{DD} \approx 100 \text{ V}$ $V_{DD} \approx 100 \text{ V}$ $V_{DD} \approx 100 \text{ V}$	_	12	_	- ns
	Turn-on time	t _{on}			24	_	
	Fall time	t _f		l	56	_	
	Turn-off time	t _{off}		l	130	_	
Total gate charge (gate-source plus gate-drain)		Qg	N 420 V V 40 V L 40 A		57		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 120 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 18 \text{ A}$	_	43	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	14	_	nC

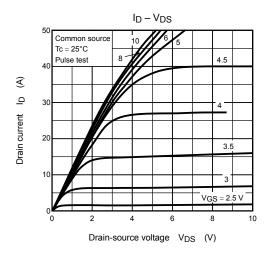
Source-Drain Ratings and Characteristics (Ta = 25°C)

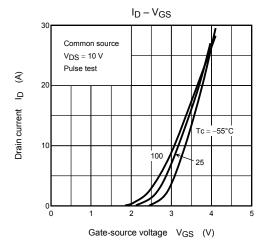
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	18	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	54	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 18 \text{ A}, V_{GS} = 0 \text{ V}$	1		-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 18 \text{ A}, V_{GS} = 0 \text{ V}$	_	185	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	1.3	_	μС

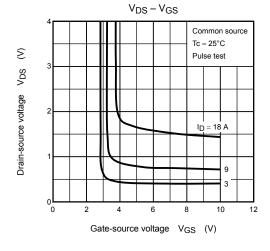
Marking

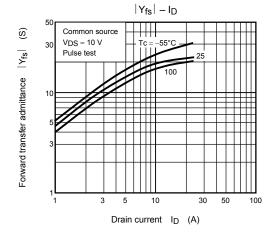


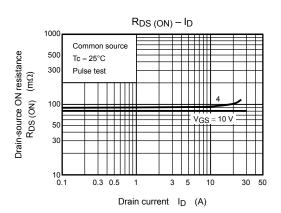


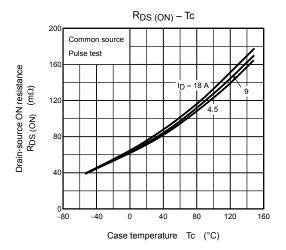


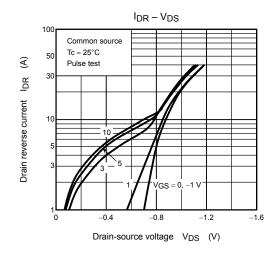


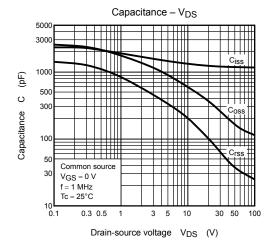


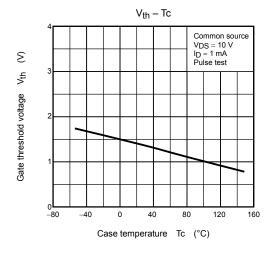


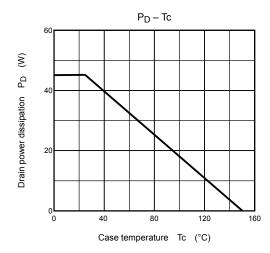


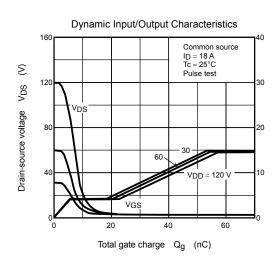


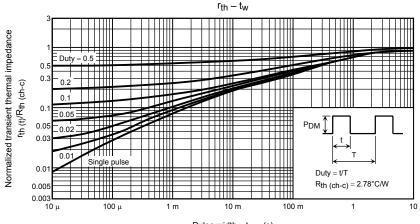




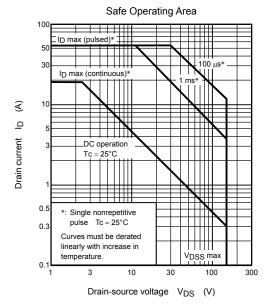


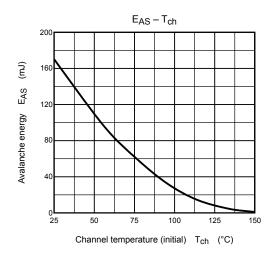


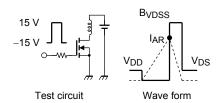




Pulse width t_W (s)







$$R_G = 25~\Omega$$

$$V_{DD} = 50~V,~L = 0.8~mH$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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20070701-EN

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