TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

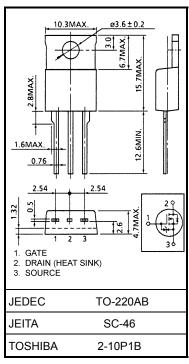
2SK2866

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS (ON)} = 0.54 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 9.0 \text{ S} (typ.)$
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 600 V)
- Enhancement mode : $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	Ι _D	10	А	
	Pulse (Note 1)	I _{DP}	40	А	
Drain power dissipatio	n (Tc = 25°C)	PD	125	W	
Single pulse avalanch	e energy (Note 2)	E _{AS}	363	mJ	
Avalanche current		I _{AR}	10	А	
Repetitive avalanche e	energy (Note 3)	E _{AR}	12.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature r	ange	T _{stg}	-55 to 150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	1.0	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 6.36 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

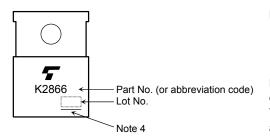
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V		—	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V		_	_	V
Drain cut-off current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600		_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A		0.54	0.75	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.0	9.0	_	S
Input capacitance		C _{iss}			2040	—	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		210	—	
Output capacitance		Coss			630	—	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \stackrel{I_{D}=5A}{_{VOUT}} V_{OUT}$ R_{L} $=40\Omega$ $V_{DD}=200V$ $Duty \leq 1\%, t_{w}=10\mu s$	_	22	_	- ns
	Turn-on time	t _{on}		_	58	_	
	Fall time	t _f		_	36	_	
	Turn-off time	t _{off}			190	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	45	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 10 A		25		nC
Gate-drain ("miller") Charge		Q _{gd}			20	_	

Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	10	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	40	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	I	1300		ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 Å / µs	_	16	_	μC

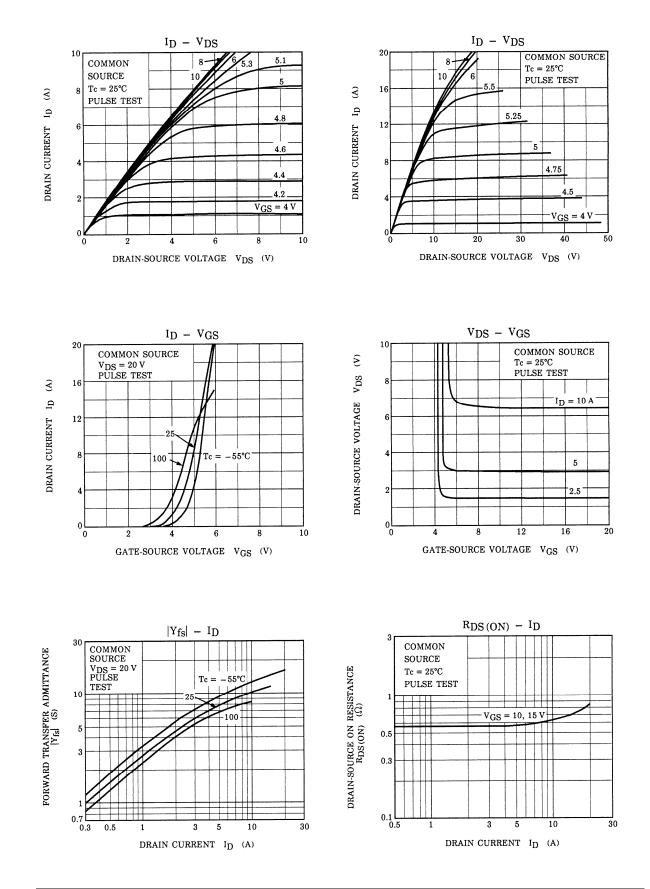
Marking



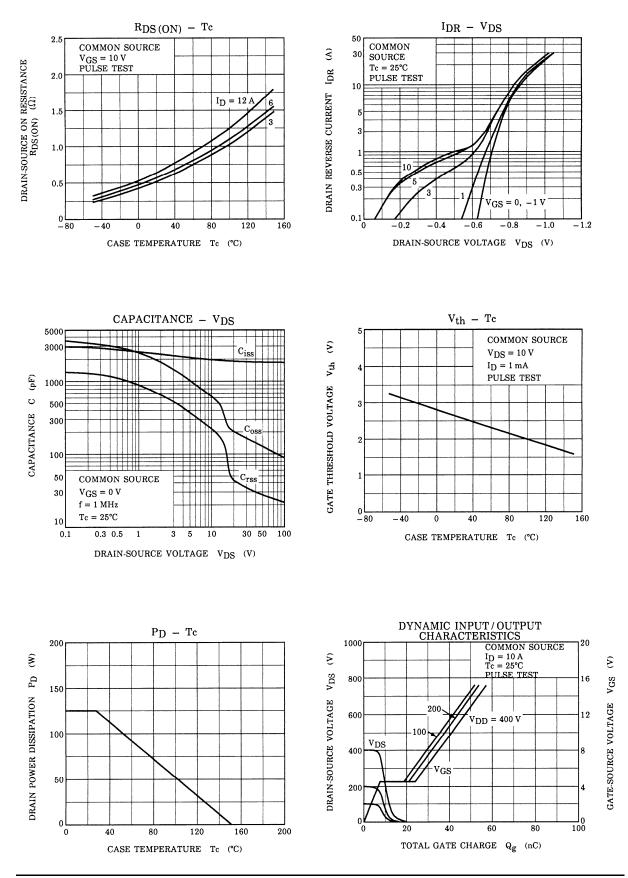
Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

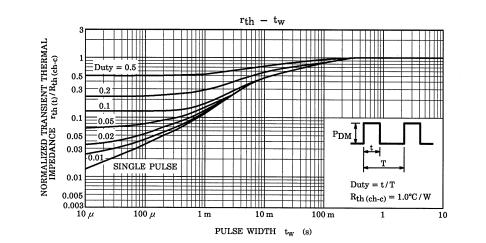
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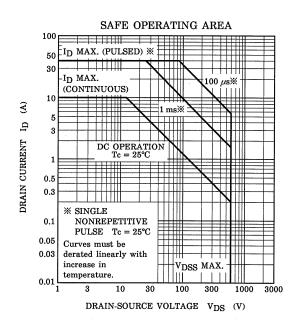
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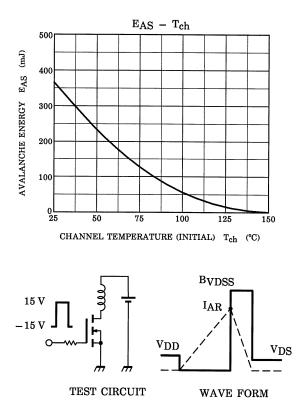


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