TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ($L^2-\pi$ -MOSV)

2SK2789

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance $: R_{DS}(ON) = 66 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance $|Y_{fs}| = 16 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- Enhancement mode $: V_{th} = 0.8 \sim 2.0 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Character	istics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	100	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	100	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	I _D	27	А
	Pulse (Note 1)	I _{DP}	108	А
Drain power dissipation	on (Tc = 25°C)	PD	60	W
Single pulse avalanch	e energy (Note 2)	E _{AS}	193	mJ
Avalanche current		I _{AR}	27	А
Repetitive avalanche	energy (Note 3)	E _{AR}	6	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature r	ange	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

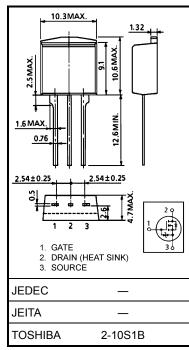
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.08	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

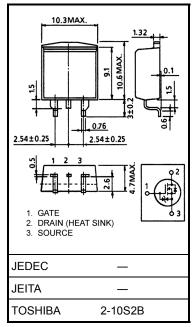
Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 428 µH, I_{AR} = 27 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 1.5 g (typ.)





Unit: mm

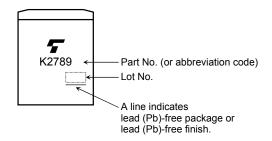
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V		—	±10	μA	
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V		_	100	μA	
Drain-source bi	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	—	_	V	
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V	
Drain-source ON resistance		_	V _{DS} = 4 V, I _D = 15 A		0.09	0.13	Ω	
		R _{DS (ON)}	V _{DS} = 10 V, I _D = 15 A		0.066	0.085		
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	8	16	_	S	
Input capacitand	Input capacitance C _{iss}				1100	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		180	—	pF	
Output capacitance		C _{oss}			400	—		
Switching time	Rise time	tr	$V_{GS}_{0V} \int I_{D} = 15A$ $V_{GS}_{0V} \int R_{L} = 3.3\Omega$ $K_{L} = 3.3\Omega$	_	20	_		
	Turn-on time	t _{on}		_	30	_	ns	
	Fall time	t _f		_	50	_		
	Turn-off time	t _{off}	V_{DD} = 50V Duty ≤1%, t _w =10 μ s	_	140	_		
Total gate charge (gate-source plus gate-drain)		Qg		_	50	_		
Gate-source charge		Q _{gs}	V _{DD} ≈ 80 V, V _{GS} = 10 V, I _D = 27 V		34	_	nC	
Gate-drain ("miller") Charge		Q _{gd}			16	_		

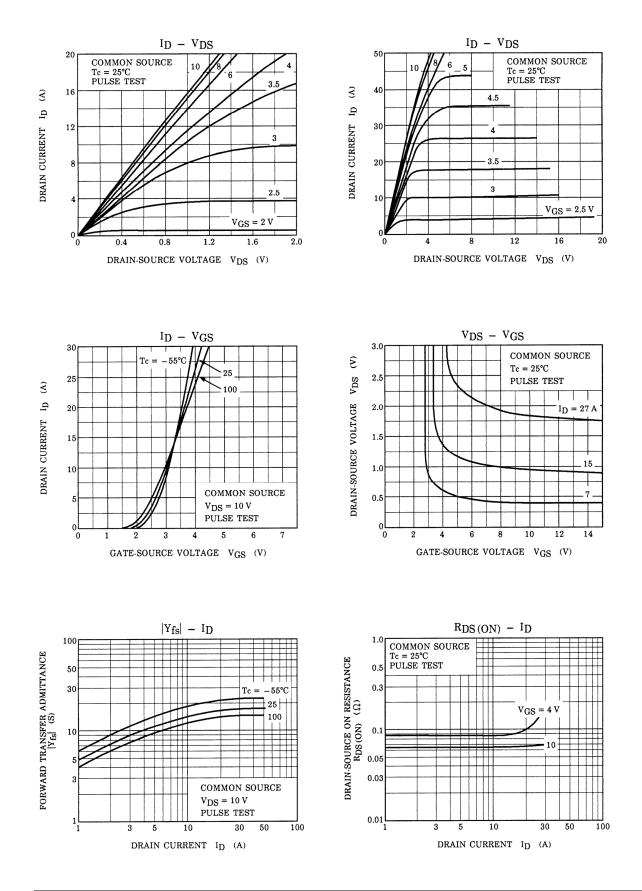
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	27	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	108	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 27 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 27 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	_	155	_	ns
Reverse recovery charge	Qrr		_	0.31	_	μC

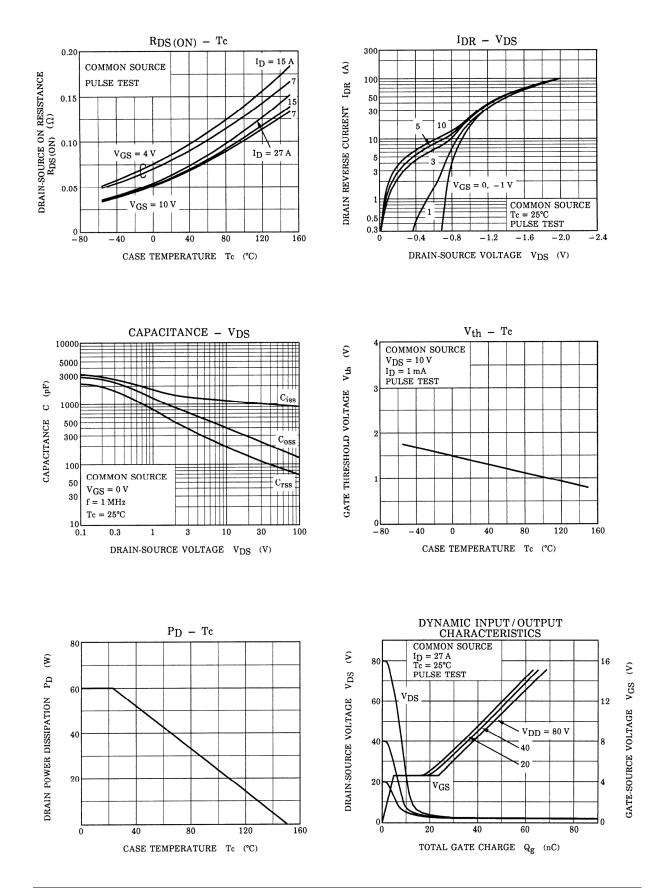
Marking

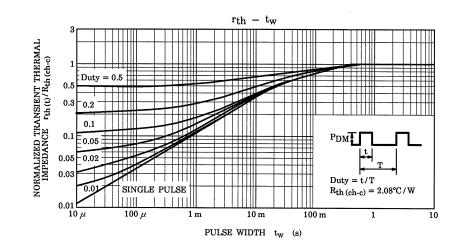


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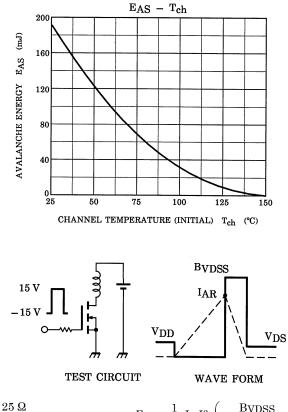


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SAFE OPERATING AREA 300 ID MAX. (PULSED) \times 100 100 μs% 50 ID MAX. 111 (CONTINUOUS) Ð 30 Ω 10 DRAIN CURRENT 5 DC OPERATION $Tc = 25^{\circ}C$ 3 1 **※ SINGLE NONREPETITIVE** PULSE $Tc = 25^{\circ}C$ 0.5 Curves must be derated VDSS 0.3 linearly with increase in MAX. 0.1∟ 0.3 temperature. 10 1 3 30 100 300 DRAIN-SOURCE VOLTAGE V_{DS} (V)



 $\begin{array}{l} \mathrm{R_{G}=25\ \Omega} \\ \mathrm{V_{DD}=25\ V,\ L=428\ \mu H} \end{array} \qquad \qquad \mathrm{EAS}=\frac{1}{2}\cdot\mathrm{L}\cdot\mathrm{I}^{2}\cdot\left(\frac{\mathrm{BVDSS}}{\mathrm{BVDSS}-\mathrm{V_{DD}}}\right) \end{array}$

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