TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7MBD3245AFK

Octal Bus Switch

The TC7MBD3245AFK provides eight bits of high-speed TTL-compatible bus switching in a standard '245 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

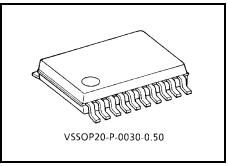
The device is organized as one 8-bit switch. When output enable (\overline{OE}) is low, the switch is on and port A is connected to port B. When \overline{OE} is high, the switch is open and a high-impedance state exists between the two ports.

The device is enable to realize the shift of signal level from $5~\mathrm{V}$ to $3.3~\mathrm{V}$.

All inputs are equipped with protection circuits against static discharge.

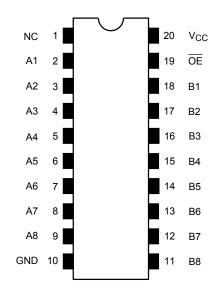
Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed: $t_{pd} = 0.32 \text{ ns (max)}$
- Low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- Compatible with TTL outputs (control inputs)
- Low Power Dissipation: Icc = 10 μA (max.)
- Package: VSSOP (US20)
- Pin compatible with the 74xx245 type.
- Functionally equivalent to (FST/CBT) 3245.



Weight: 0.03 g (typ.)

Pin Assignment (top view)



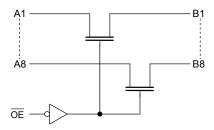
NC-No Internal Connection



Truth Table

Inputs	Function		
ŌĒ	1 diletion		
L	A port = B port		
Н	Disconnect		

System Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit	
Power supply range	V _{CC}	-0.5~7.0	V	
DC input voltage	V _{IN}	-0.5~7.0	٧	
DC switch voltage	VS	-0.5~7.0	٧	
Input diode current	I _{IK}	-50	mA	
Continuous channel circuit	IS	128	mA	
Power dissipation	P _D	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	−65~150	°C	

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V_{IN}	0~5.5	V
Switch voltage	VS	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

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Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 1)	Max	Unit	
Input voltage	"H" level	V _{IH}	_		4.5~5.5	2.0	_	_	V	
Input voltage	"L" level	V _{IL}	_		4.5~5.5	_	_	8.0	V	
High-level outp	ut voltago		IOH - 1A		4.75	2.3	2.8	3.2		
riigii-ievei outp	(Note 2)	VoH	V _{IS} = V _{CC}	$IOH = -1 \mu A$		2.5	3.0	3.4	V	
	(14016-2)		VIS = VCC		5.25	2.7	3.2	3.6		
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μΑ	
Power off leaka	age current	l _{OFF}	A, B, $\overline{OE} = 0 \sim 5.5 \text{ V}$		0	_	_	±1.0	μΑ	
Off-STATE leaf (switch off)	kage current	I _{SZ}	A, B = 0~5.5 V, OE = V _{CC}		4.5~5.5	_	_	±1.0	μΑ	
	lio.	I _{IS} = 64 mA	4.5	_	5	9				
			$V_{IS} = 0 \text{ V}$ $I_{IS} = 30 \text{ mA}$	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		4.75	_	5	8	
ON resistance	(Note 3)	Da		4.5	_	5	9	Ω		
ON resistance (Note 3) R _{ON}	NON	1 S = 30 IIIA	4.75	_	5	8	52			
		V _{IS} = 2.3 V, I _{IS} = 15 mA		4.5	_	35	65			
					4.75	_	35	50		
Quiescent supp	oly current	ICC	VIN = VCC or GND,I _{OUT} = 0		5.5	_	_	10	μΑ	
Increase in I _{CC}	per input	Δl _{CC}	V _{IN} = 3.4 V (one input)		5.5	_		2.5	mA	

Note 1: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$.

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note)	4.5	_	0.32	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		7.0	ns
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5	_	7.0	ns

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

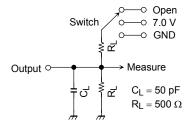
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{OE} = V_{CC}$ (Note)	5.0	10	pF

Note: This parameter is guaranteed by design.



AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	GND		

Figure 1

AC Waveform

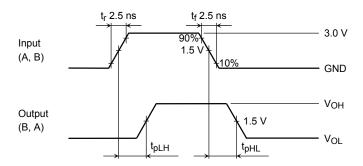


Figure 2 t_{pLH}, t_{pHL}

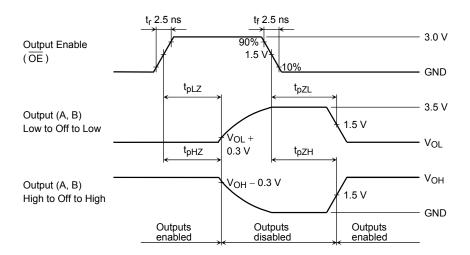
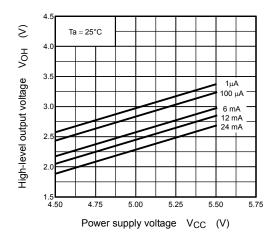
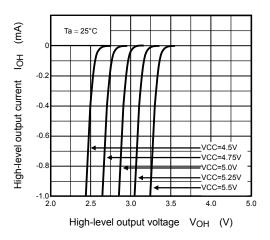
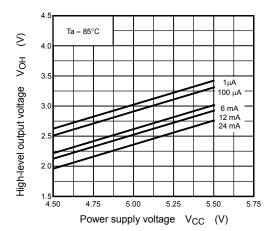


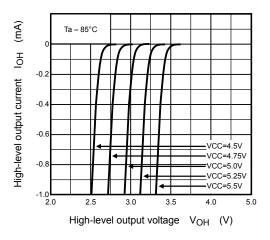
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

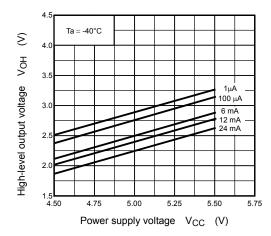
V_{OH} - V_{CC} Characteristics (typ.)











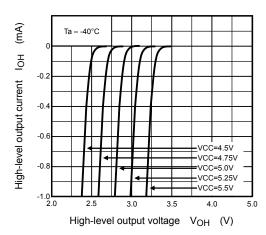
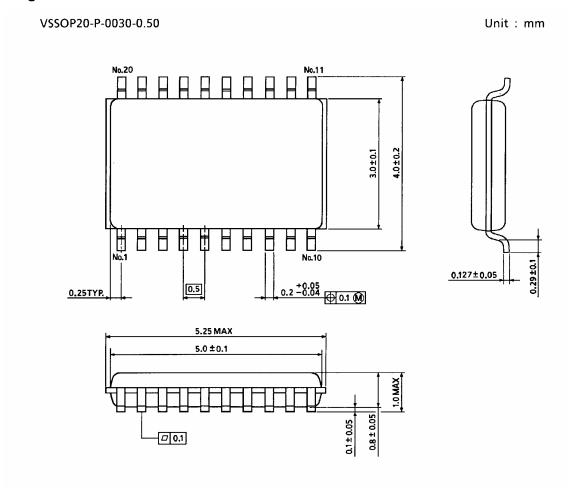


Figure 4

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Package Dimensions



Weight: 0.03 g (typ.)

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20070701-EN GENERAL

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