Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK2598

## Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance  $: RDS (ON) = 0.18 \Omega (typ.)$
- High forward transfer admittance  $|Y_{fs}| = 13 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 250 \ V)$
- Enhancement mode :  $V_{th} = 1.5 \sim 3.5 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	250	V	
Drain-gate voltage (Ro	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	250	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	ID	13	А	
	Pulse (Note 1)	I <sub>DP</sub>	52	A	
Drain power dissipation	n (Tc = 25°C)	PD	60	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	148	mJ	
Avalanche current		I <sub>AR</sub>	13	А	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	6	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

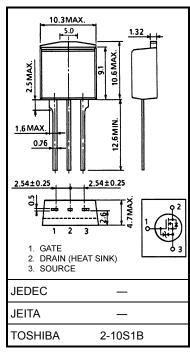
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.08	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

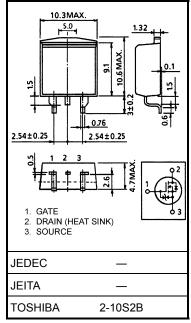
Note 2: V\_DD = 50 V, T\_ch = 25 °C (initial), L = 1.48 mH, R\_G = 25  $\Omega$ , I<sub>AR</sub> = 13 A

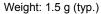
Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 1.5 g (typ.)





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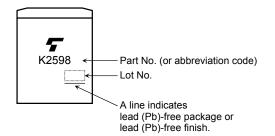
#### Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		—	±10	μA
Drain cut-off cu	cut-off current I <sub>DSS</sub> V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V		V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V		—	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V		—	—	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	—	3.5	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A	_	0.18	0.25	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	6	13	—	S
Input capacitand	ce	C <sub>iss</sub>		_	1800	_	
		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		130	—	pF
Output capacitance		C <sub>oss</sub>		_	500	_	
Switching time	Rise time	tr	$ \begin{array}{c} 10 \text{ V} \\ \text{V}_{\text{GS}} \\ 0 \text{ V} \\ \text{C}_{\text{C}} \\ \text{V}_{\text{C}} \\ $	_	15	_	
	Turn-on time	t <sub>on</sub>		_	25	_	
	Fall time	t <sub>f</sub>		_	10	_	ns
	Turn-off time	toff	Duty $\leq 1\%$ , t <sub>w</sub> = 10 µs	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	40	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 200 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 13 A		25	—	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			15	_	

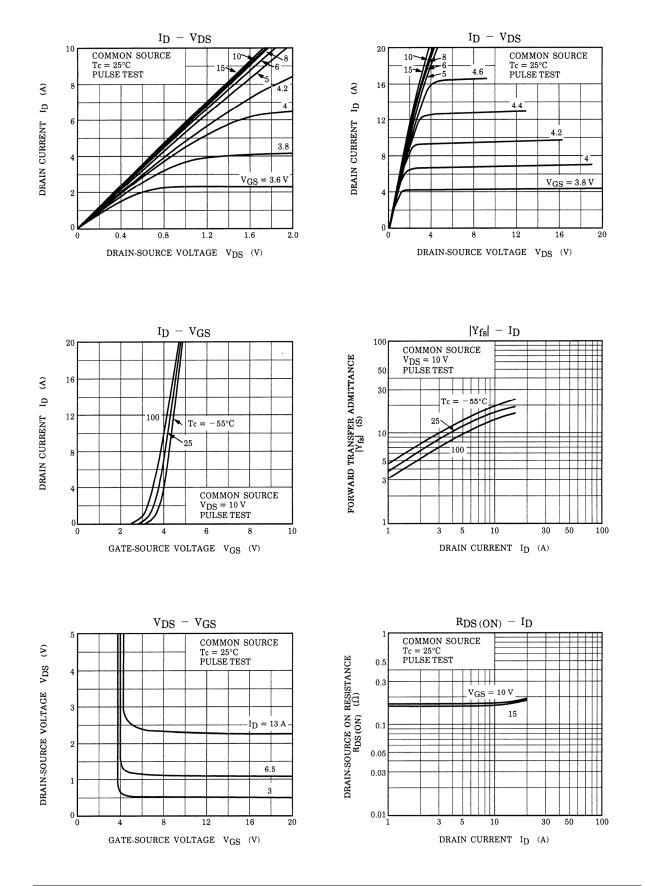
#### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	13	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	—	52	A
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V	_	_	-2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V		260		ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	0.3	_	μC

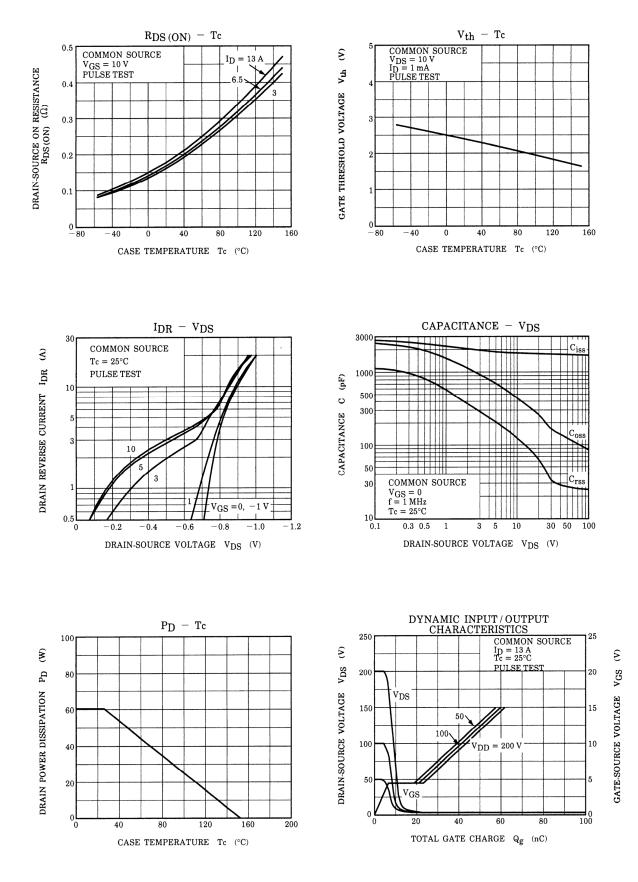
#### Marking

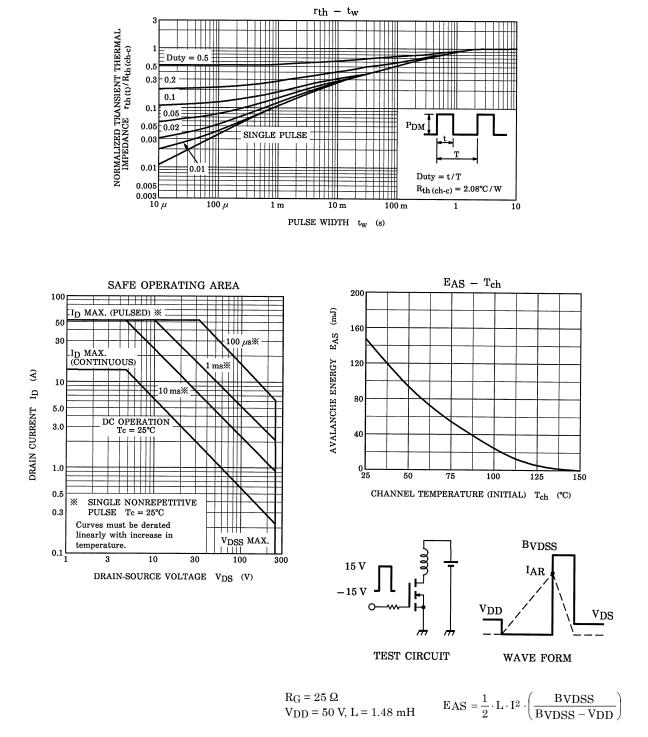


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