## <u>TOSHIBA</u>

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

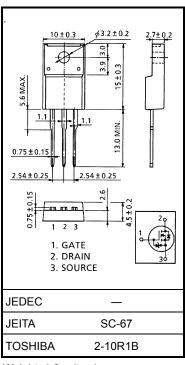
# 2SK2508

Switching Regulator and DC–DC Converter and Motor Applications

- Low drain-source ON resistance  $: R_{DS}(ON) = 0.18 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 13 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 250 \ V)$
- Enhancement mode :  $V_{th} = 1.5$  to 3.5 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	250	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	250	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	13	А	
	Pulse (Note 1)	I <sub>DP</sub>	52	А	
Drain power dissipation	n (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	148	mJ	
Avalanche current		I <sub>AR</sub>	13	А	
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	2.78	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V<sub>DD</sub> = 50 V, T<sub>ch</sub> = 25°C (initial), L = 1.48 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

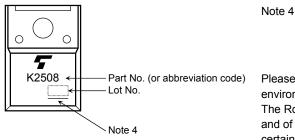
#### **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	te leakage current $I_{GSS}$ $V_{GS}$ = ±16 V, $V_{DS}$ = 0 V		_	_	±10	μA	
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V		_	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	250	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		0.18	0.25	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	6	13	_	S
Input capacitand	ce	C <sub>iss</sub>			1800	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		130	—	pF
Output capacitance		Coss			500	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \int I_{D} = 6.5A$ $V_{GS} \stackrel{10V}{_{0V}} \int I_{D} = 6.5A$ $V_{OUT} \stackrel{V}{_{0V}} I_{D} \stackrel{V}{_{0V}} V_{OUT}$ $R_{L} = 20\Omega$ $V_{DD} = 130V$		15	_	
	Turn-on time	t <sub>on</sub>		_	25		20
	Fall time	t <sub>f</sub>		_	10	_	ns
	Turn-off time	toff	Duty $\leq 1\%$ , t <sub>w</sub> =10µs	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg		—	40	_	nC
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 200 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 13 A	_	25	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>			15	_	

#### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	13	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	52	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V			-2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V		260	—	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / µs	_	0.3	_	μC

#### Marking

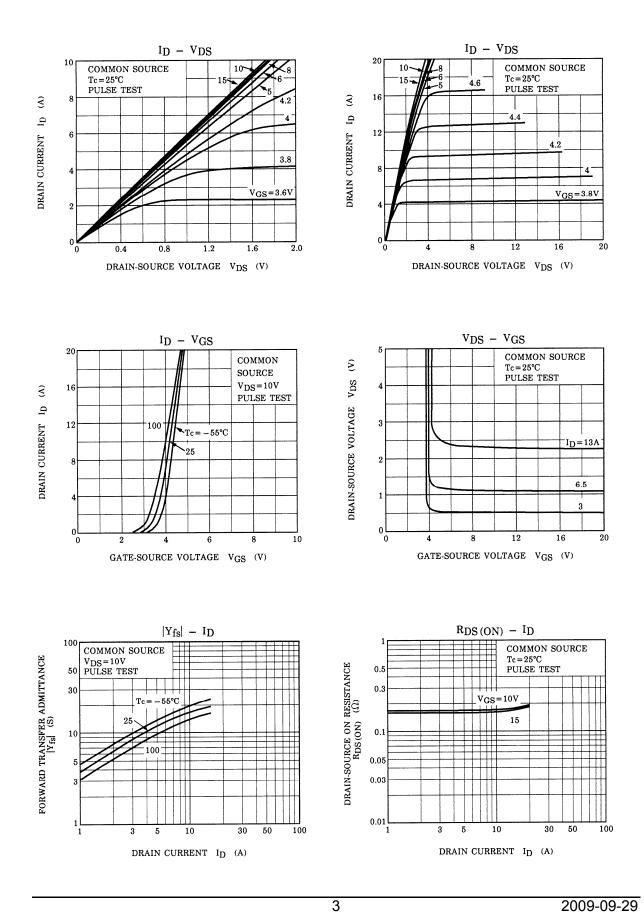


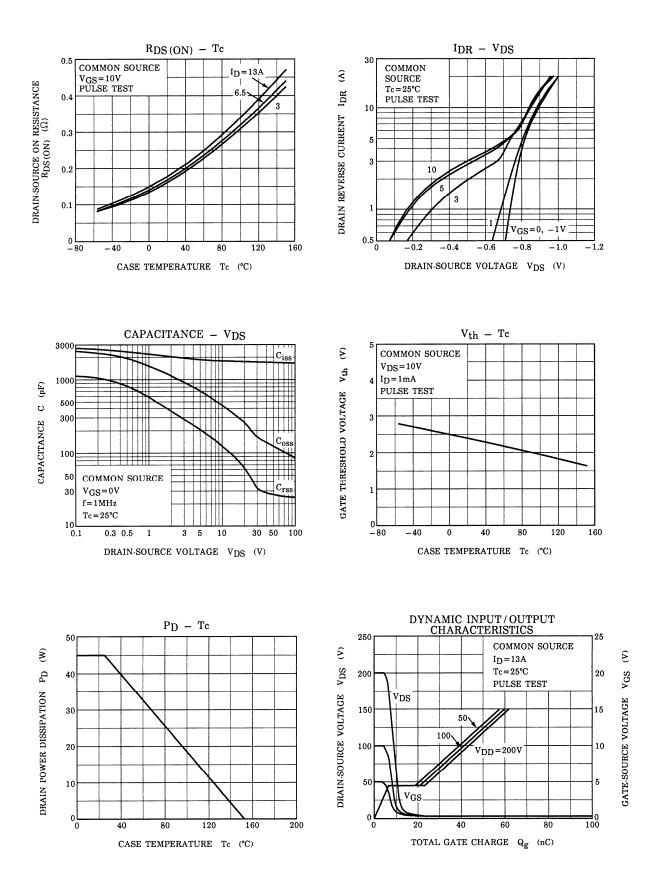
Note 4: A line under a Lot No. identifies the indication of product Labels.

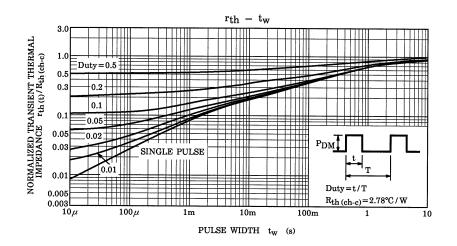
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

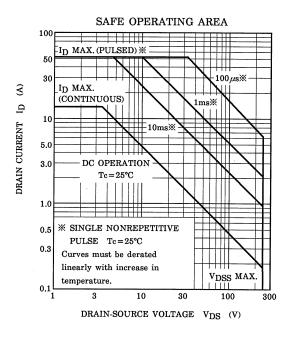
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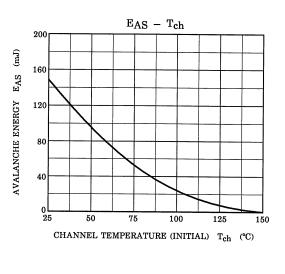
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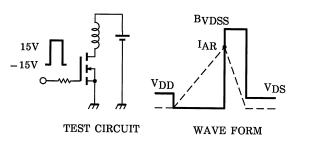












$$\begin{array}{ll} \mathrm{R}_{\mathrm{G}} = 25 \ \Omega \\ \mathrm{V}_{\mathrm{DD}} = 50 \ \mathrm{V}, \ \mathrm{L} = 1.48 \ \mathrm{mH} \end{array} \qquad \qquad \\ \mathrm{EAS} = \frac{1}{2} \cdot \mathrm{L} \cdot \mathrm{I}^2 \cdot \left( \frac{\mathrm{B} \mathrm{VDSS}}{\mathrm{B} \mathrm{VDSS} - \mathrm{V}\mathrm{DD}} \right) \\ \end{array}$$

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