Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

## 2SK2507

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance  $: RDS(ON) = 0.034 \Omega (typ.)$ 

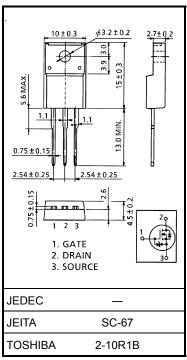
• High forward transfer admittance :  $|Y_{fs}| = 16 \text{ S (typ.)}$ 

• Low leakage current :  $IDSS = 100 \mu A (max) (VDS = 50 V)$ 

• Enhancement mode :  $V_{th} = 0.8 \text{ to } 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	50	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	50	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	ΙD	25	А	
Diam current	Pulse (Note 1)	I <sub>DP</sub>	75		
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	30	W	
Single pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	138	mJ	
Avalanche current		I <sub>AR</sub>	25	Α	
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	3	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature r	ange	T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	4.17	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 272  $\mu$ H,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 25 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



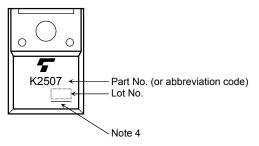
#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	50	_	_	V
Gate threshold v	/oltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 6 A	_	0.058	0.08	Ω
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A	1	0.034	0.046	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 12 A	8.0	16		S
Input capacitano	ce	C <sub>iss</sub>			900		
Reverse transfer capacitance		C <sub>rss</sub>		_	130	_	pF
Output capacitance		Coss		_	370	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 12 \text{ A}$ $V_{GS} = 12 \text{ A}$ $V_{OU} = 12 \text{ A}$ $V_{OU} = 2.5 \Omega$ $V_{DD} = 30 \text{ V}$	_	15		
	Turn-on time	t <sub>on</sub>		l	25		ns
	Fall time	t <sub>f</sub>		l	30		115
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_{\mathbf{W}} = 10 \ \mu s$	_	110		
Total gate charge (Gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 40 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A		25	_	nC
Gate-source charge		Q <sub>gs</sub>		_	19	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	6	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	25	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	75	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V	_	_	-1.6	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 50 A / μs	_	60	_	ns
Reverse recovery charge	Qrr	TIDR - 25 A, VGS - 0 V, αΙDR / αι - 50 A / μs		45	_	μC

### Marking

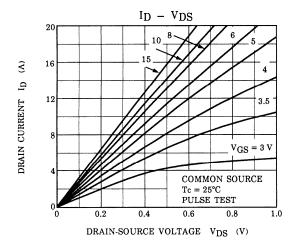


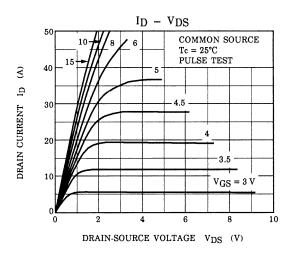
Note 4: A line under a Lot No. identifies the indication of product Labels.

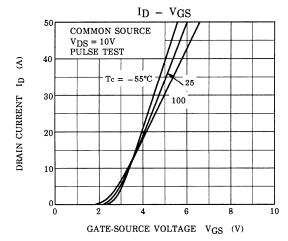
Not underlined: [[Pb]]/INCLUDES > MCV

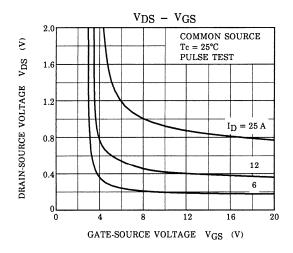
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

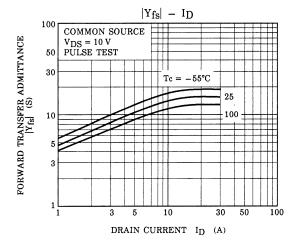
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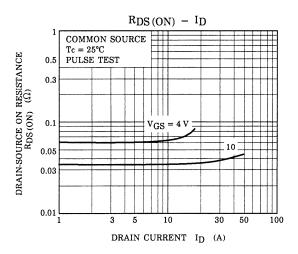


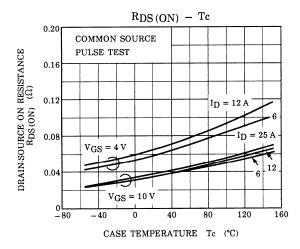


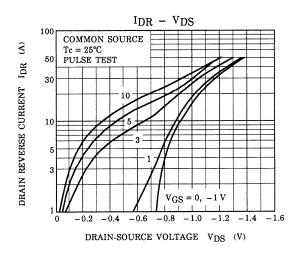


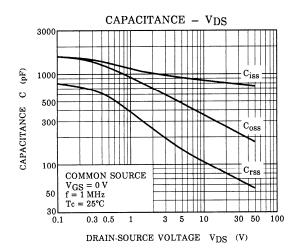


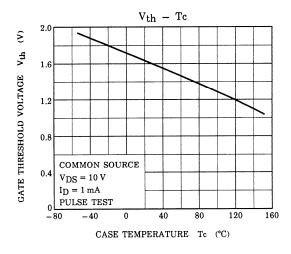


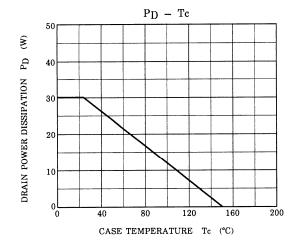


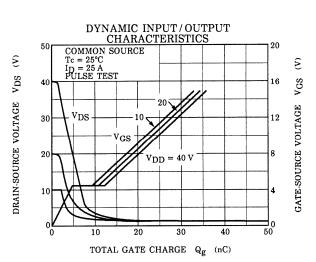


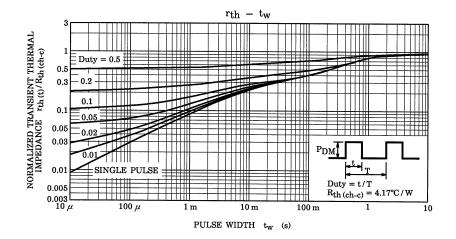


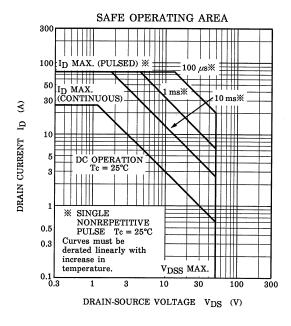


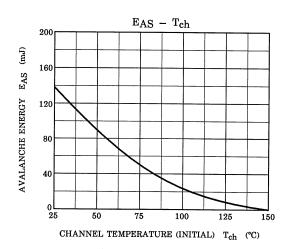


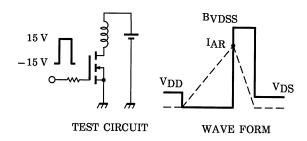












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 272~\mu H \end{aligned} \qquad EAS &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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