TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK2401

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance :  $R_{DS\ (ON)} = 0.13\ \Omega$  (typ.) • High forward transfer admittance :  $|Y_{fs}| = 17\ S$  (typ.) • Low leakage current :  $I_{DSS} = 100\ \mu A$  (max) ( $V_{DS} = 200\ V$ ) • Enhancement mode :  $V_{th} = 1.5$  to  $3.5\ V$  ( $V_{DS} = 10\ V$ ,  $I_{D} = 1\ mA$ )

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	200	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	200	V
Gate-source voltage		V <sub>GSS</sub>	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	15	Α
Diam current	Pulse (Note 1)	I <sub>DP</sub>	45	Α
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	75	W
Single pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	166	mJ
Avalanche current		I <sub>AR</sub>	15	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	7.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.67	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

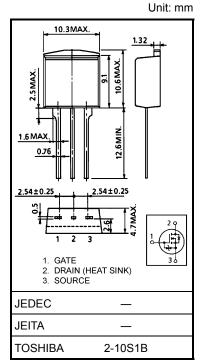
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 50 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 1.2 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 15 \text{ A}$ 

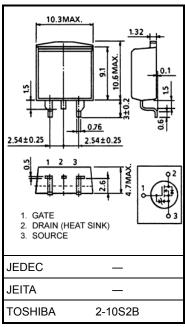
Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Weight: 1.5 g (typ.)



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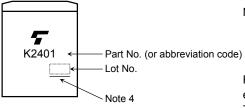
# Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	200	_		V
Gate threshold v	/oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	-	0.13	0.18	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	10	17	_	S
Input capacitano	e	C <sub>iss</sub>			2000		pF
Reverse transfe	se transfer capacitance $C_{rss}$ $V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1 MHz		V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		200	_	
Output capacitance		C <sub>oss</sub>	]		600	-	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} \stackrel{10 \text{ V}}{\text{0 V}} \stackrel{\text{I}_{D} = 10 \text{ A}}{\text{V}_{out}} \\ \stackrel{\text{R}_{L}}{\text{V}_{DD} = 100 \text{ V}}$	_	35	_	ns
	Turn-on time	t <sub>on</sub>		_	50	_	
	Fall time	t <sub>f</sub>		_	10	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_{\rm W} = 10~\mu \rm s$	_	66	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	40	_	
Gate-source charge		$Q_{gs}$	$V_{DD} \approx 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 15 \text{ A}$		25		nC -
Gate-drain ("miller") charge		$Q_{gd}$			15	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	45	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	180	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	_	1.13	_	μC

## Marking

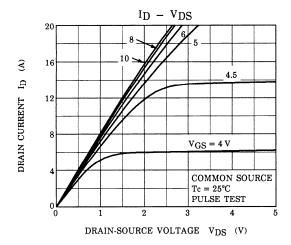


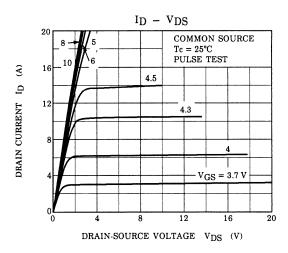
Note 4: A line under a Lot No. identifies the indication of product Labels.

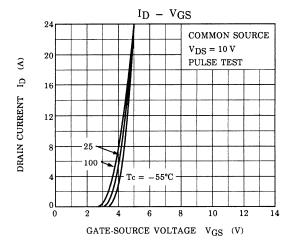
Not underlined: [[Pb]]/INCLUDES > MCV

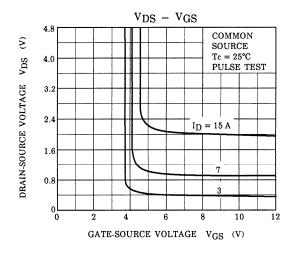
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

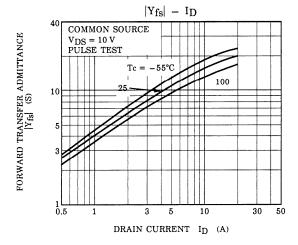
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

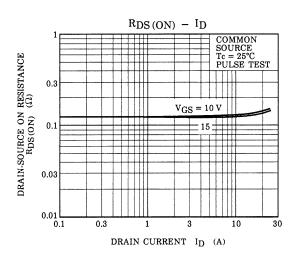


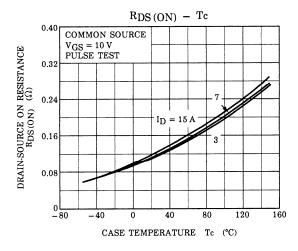


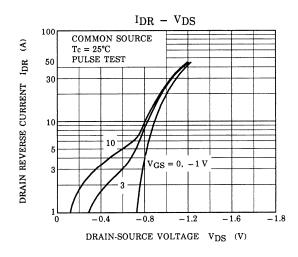


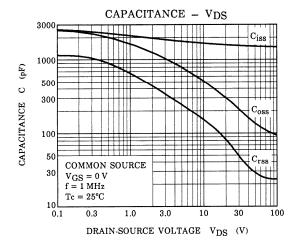


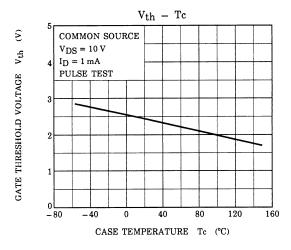


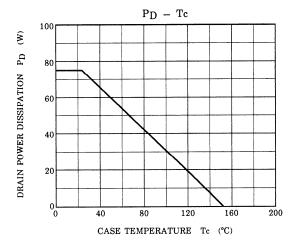


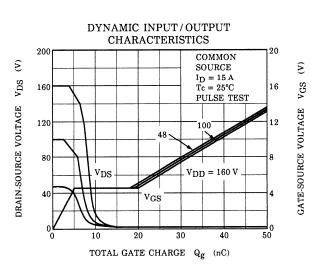


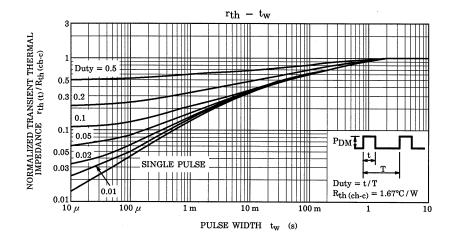


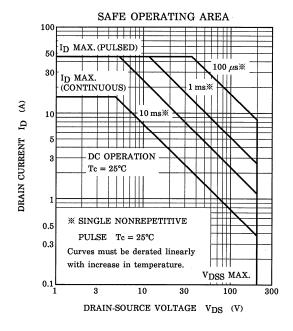


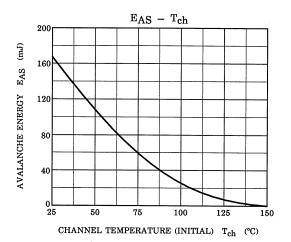


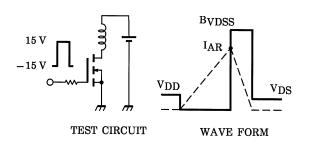












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 50~V,~L = 1.2~mH \end{aligned} \qquad EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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