Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2400

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & : R_{DS}\ (ON) = 17\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & : |Y_{fs}| = 4.5\ S\ (typ.) \\ \bullet & Low\ leakage\ current & : I_{DSS} = 100\ \mu A\ (max)\ (V_{DS} = 100\ V) \\ \bullet & Enhancement\ mode & : V_{th} = 0.8 \sim 2.0\ V\ (V_{DS} = 10\ V,\ I_D = 1\ mA) \\ \end{array}$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	100	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	100	V
Gate-source voltage		V _{GSS}	±20	V
	DC (Note 1)	ΙD	5	Α
Drain current	Pulse (Note 1)	I _{DP}	20	Α
Drain power dissipation	n	PD	1.3	W
Single pulse avalanche energy (Note 2)		E _{AS}	180	mJ
Avalanche current		I _{AR}	5	Α
Repetitive avalanche energy (Note 3)		E _{AR}	0.13	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C

1.4±0.1 1.4±0.1 1.05±0.1 1.05±0.1 1.5±0.5 2.5±0.5 2.5±0.5 1.SOURCE 2.DRAIN 3.GATE

Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	96.1	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 11.6 mH, $R_G = 25 \Omega$, $I_{AR} = 5 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

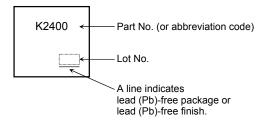
Electrical Characteristics (Ta = 25°C)

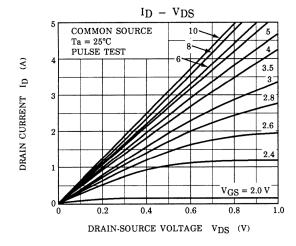
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ	
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	100	μΑ	
Drain-source br voltage	eakdown	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	٧	
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V	
Drain-source O	N registance	,	V _{GS} = 4 V, I _D = 2.5 A	_	0.22	0.30		
Drain-source ON resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A	_	0.17	0.23	Ω		
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5	_	S	
Input capacitano	e	C _{iss}		_	500	_		
Reverse transfer capacitance Output capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	80	_	pF	
		Coss		_	190	_		
Switching time	Rise time	t _r	$V_{GS} \stackrel{10V}{_{0V}} \stackrel{I_{D}=2.5A}{_{0V}} \stackrel{V_{OUT}}{_{10}} V_$	_	17	_	ns	
	Turn-on time	t _{on}		_	25	_		
	Fall time	t _f		_	50	_		
	Turn-off time	t _{off}			195			
Total gate charge (Gate-source plus gate-drain)		Qg			22			
Gate-source charge		Qgs	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		15	_	nC	
Gate-drain ("miller") charge		Q _{gd}		1	7	1		

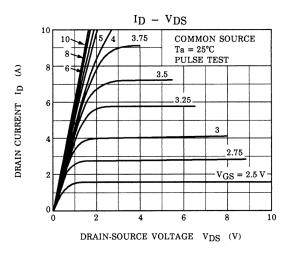
Source-Drain Ratings and Characteristics (Ta = 25°C)

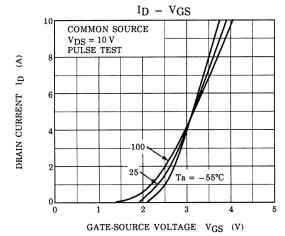
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	20	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	IDR = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / µs		160	_	ns
Reverse recovery charge	Q _{rr}	1 IDR - 3 Λ, VGS - 0 V, αIDR / αι - 30 Α / μs	_	0.28	_	μC

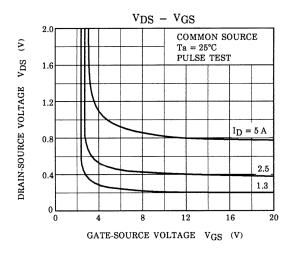
Marking

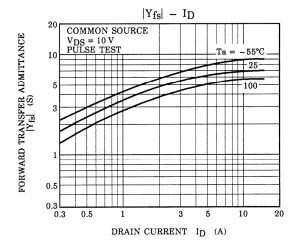


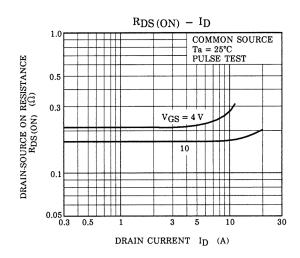




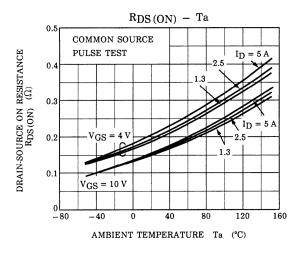


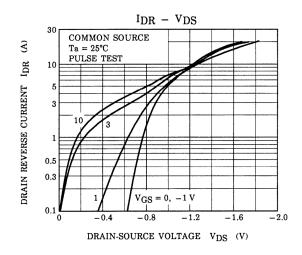


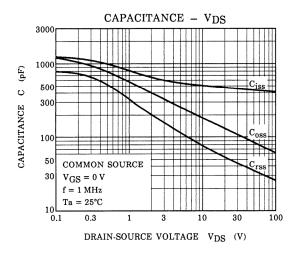


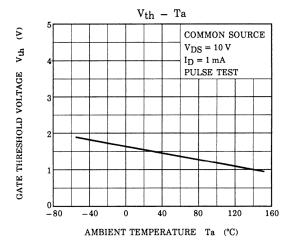


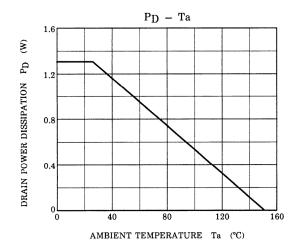
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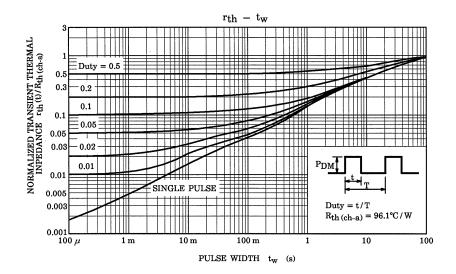


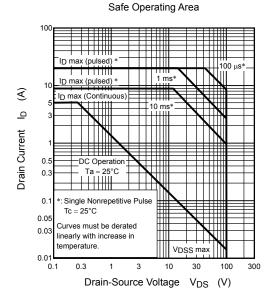


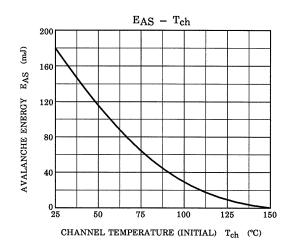


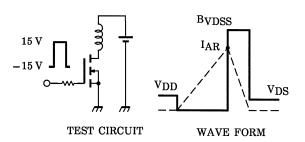












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 25~V,~L = 11.6~mH \end{aligned} \qquad EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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20070701-EN

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