TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (L^2 - π -MOSV)

2SK2399

Chopper Regulator, DC/DC Converter and Motor Drive Applications

4 V gate drive

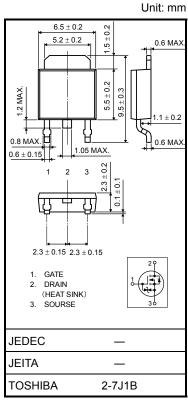
• Low drain-source ON-resistance : $R_{DS(ON)} = 0.17 \Omega \text{ (typ.)}$

High forward transfer admittance : |Y_{fS}| = 4.5 S (typ.)
 Low leakage current : I_{DSS} = 100 µA (max) (V_{DS} = 100 V)

• Enhancement mode : $V_{th} = 0.8$ to 2.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	100	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	100	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	ID	5	Α
Diain current	Pulse (Note 1)	I _{DP}	20	Α
Drain power dissipatio	n (Tc = 25°C)	PD	20	W
Single-pulse avalanch	e energy (Note 2)	E _{AS}	180	mJ
Avalanche current		I _{AR}	5	Α
Repetitive avalanche	energy (Note 3)	E _{AR}	2	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature r	ange	T _{stg}	-55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 11.6 mH, R_G = 25 Ω , I_{AR} = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



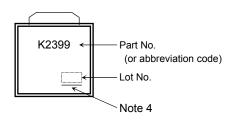
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bre	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Gate threshold v	/oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA		_	2.0	V
Drain-source ON-resistance		1	V _{GS} = 4 V, I _D = 2.5 A	_	0.22	0.30	Ω
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A	_	0.17	0.23	
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5	_	S
Input capacitano	e	C _{iss}		_	500	_	
Reverse transfer capacitance		C _{rss}	C _{rss} V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	80	_	pF
Output capacitance		Coss		_	190	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10V}{\underset{0V}{\prod}} \stackrel{I_{D}=2.5A}{\underset{0V}{\prod}} \stackrel{\circ}{\underset{N}{\prod}} R_{L} = 20\Omega$ $V_{DD} = 50V$ $Duty \leq 1\%, \ t_{W} = 10\mu s$	_	17	_	
	Turn-on time	t _{on}		_	25	_	no
	Fall time	t _f		_	50	_	ns
	Turn-off time	t _{off}		_	195	_	
Total gate charge (gate-source plus gate-drain)		Qg			22		
Gate-source charge		Q _{gs}	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	15	_	nC
Gate-drain ("Miller") charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

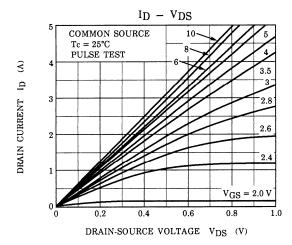
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs		160		ns
Reverse recovery charge	Q _{rr}		_	0.28	_	μC

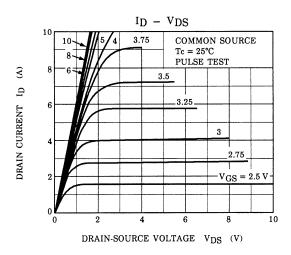
Marking

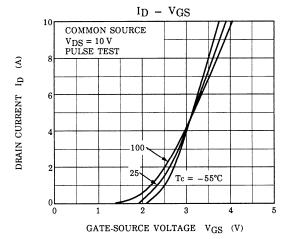


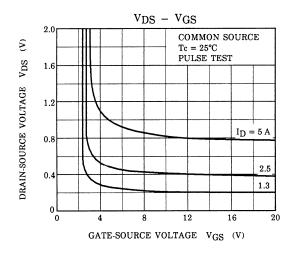
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

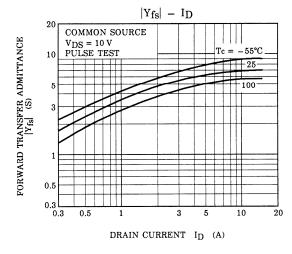
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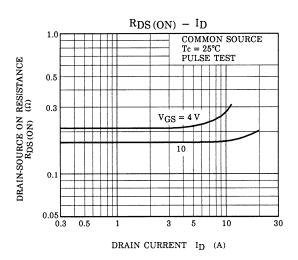




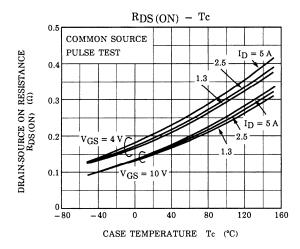


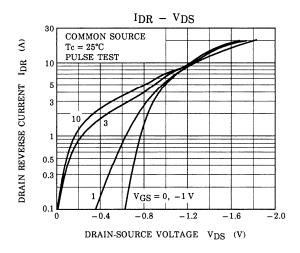


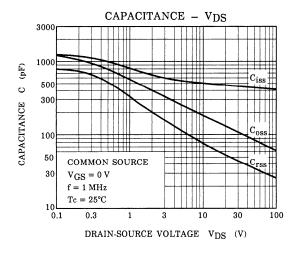


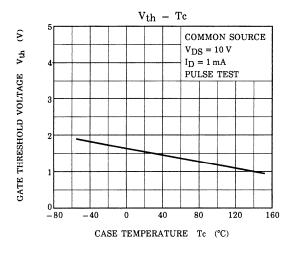


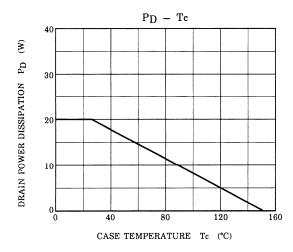
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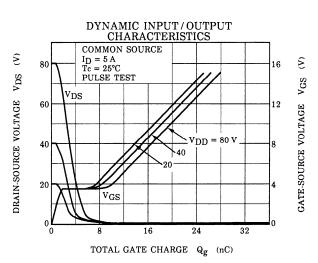




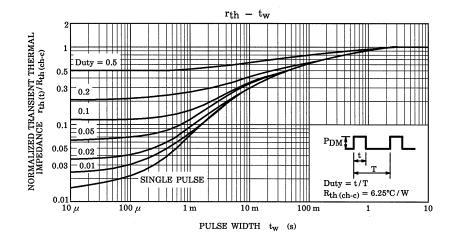


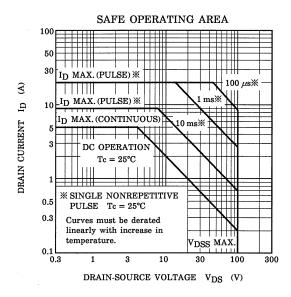


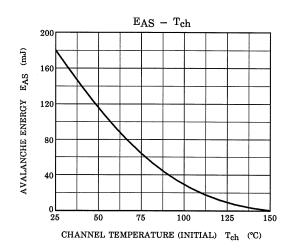


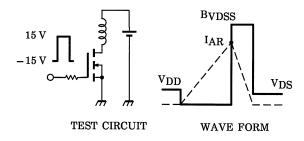


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 11.6~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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