TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

## 2SK2201

# Chopper Regulator, DC/DC Converter and Motor Drive Applications

• 4 V gate drive

• Low drain-source ON-resistance :  $R_{DS (ON)} = 0.28 \Omega (typ.)$ 

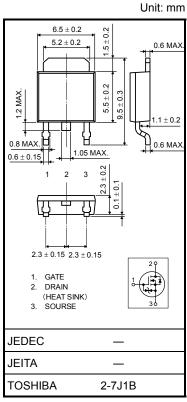
• High forward transfer admittance :  $|Y_{fs}| = 3.5 \text{ S (typ.)}$ 

• Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 100 V)

• Enhancement mode :  $V_{th} = 0.8$  to 2.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	100	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	100	٧
Gate-source voltage		V <sub>GSS</sub>	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	3	Α
	Pulse (Note 1)	I <sub>DP</sub>	12	Α
Drain power dissipatio	n (Tc = 25°C)	$P_{D}$	20	W
Single-pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	140	mJ
Avalanche current		I <sub>AR</sub>	3	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	2	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature r	ange	T <sub>stg</sub>	-55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	6.25	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 50 V,  $T_{ch}$  = 25°C (initial), L = 25 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 3 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

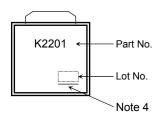
## **Electrical Characteristics (Ta = 25°C)**

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		_	±10	μΑ	
Drain cutoff curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V	_	_	100	μA	
Drain-source bre	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	100	_	_	V	
Gate threshold v	/oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V	
Drain-source ON-resistance		-	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 2 A	_	0.36	0.45	Ω	
Diain-source Or	N-Tesistatice	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2 A	_	0.28	0.35	7.7	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2 A	1.5	3.5	_	S	
Input capacitano	ce	C <sub>iss</sub>		_	280	_		
Reverse transfer capacitance		C <sub>rss</sub>	C <sub>rss</sub> V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	50	_	pF	
Output capacitance		C <sub>oss</sub>		_	105	_		
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = V_{OUT}$ $V_{GS} = V_{OUT}$ $V_{DD} = 50V$ $V_{DU} = 10 \mu s$	_	20	_	ns ns	
	Turn-on time	ton		_	50	_		
	Fall time	t <sub>f</sub>		_	40	_		
	Turn-off time	t <sub>off</sub>		_	170	_		
Total gate charge (gate-source plus gate-drain)			_	13.5	_	nC		
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 80 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A		8.5		_	
Gate-drain ("Miller") charge		Q <sub>gd</sub>			5		_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

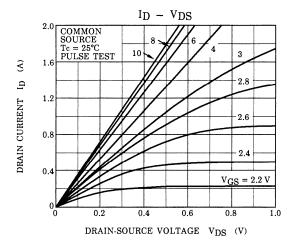
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	3	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	12	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 3 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	IDR = 3 A, VGS = 0 V, dIDR / dt = 50 A / µs	_	100	_	ns
Reverse recovery charge	Q <sub>rr</sub>	- 1DR - 3 A, VGS - 0 V, αιDR / αι - 30 A / μs	1	0.2	_	μC

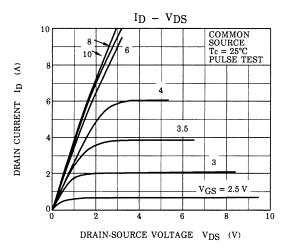
#### Marking

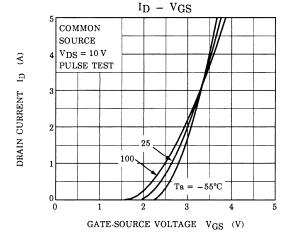


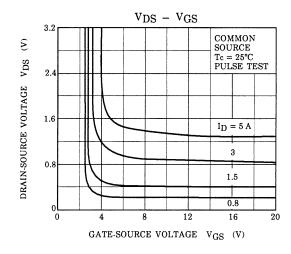
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

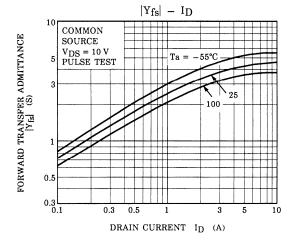
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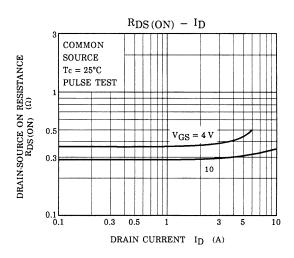




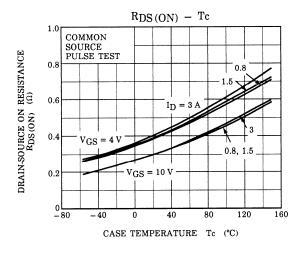


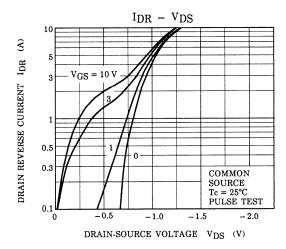


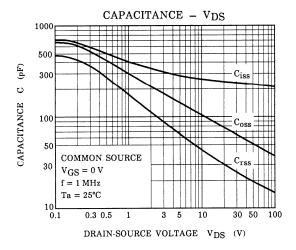


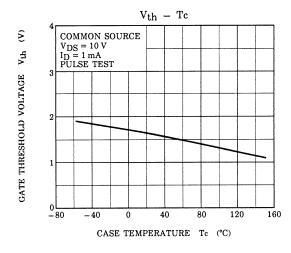


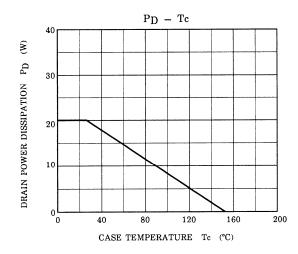
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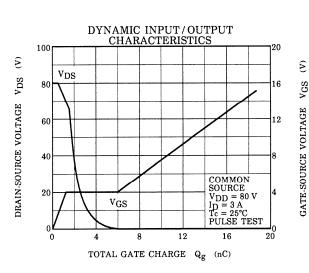




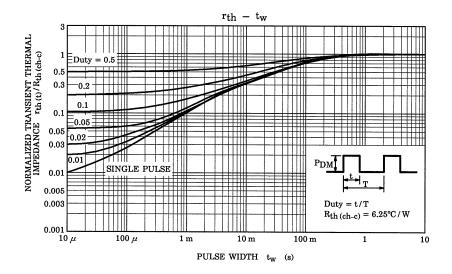


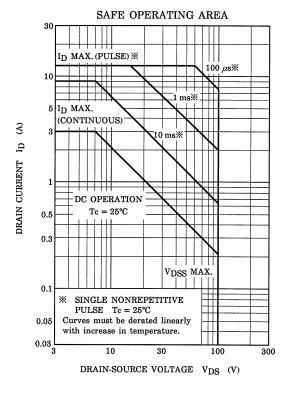


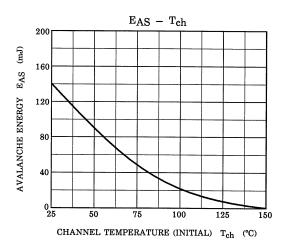


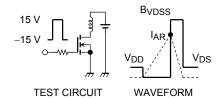


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 50~V,~L = 25~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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