100V P-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

 $V_{(BR)DSS} = -100V : R_{DS}(_{on}) = 0.150\Omega$; $I_{D} = -3.7A$

DESCRIPTION

This new generation of Trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

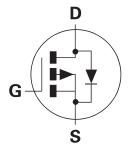


FEATURES SOT223

- · Low on-resistance
- · Fast switching speed
- Low threshold
- · Low gate drive
- SOT223 package

APPLICATIONS

- DC-DC Converters
- Power Management functions
- Relay and Solenoid driving
- Motor control



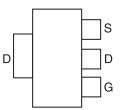
ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMP10A18GTA	7″	12mm	1,000 units
ZXMP10A18GTC	13"	12mm	4,000 units

DEVICE MARKING

 ZXMP 10A18

PINOUT





ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DSS}	-100	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current @ V _{GS} =10V; T _A =25°C (b)	I _D	-3.7	А
@V _{GS} =10V; T _A =70°C ^(b)		-3.0	А
@ V _{GS} =10V; T _A =25°C ^(a)		-2.6	А
Pulsed Drain Current ^(c)	I _{DM}	-16.5	Α
Continuous Source Current (Body Diode) (b)	I _S	-5.3	А
Pulsed Source Current (Body Diode) (c)	I _{SM}	-16.5	А
Power Dissipation at T _A =25°C ^(a)	P _D	2	W
Linear Derating Factor		16	mW/°C
Power Dissipation at T _A =25°C ^(b)	P _D	3.9	W
Linear Derating Factor		31	mW/°C
Operating and Storage Temperature Range	T _j , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient ^(a)	$R_{\Theta JA}$	62.5	°C/W
Junction to Ambient ^(b)	$R_{\Theta JA}$	32.2	°C/W

NOTES

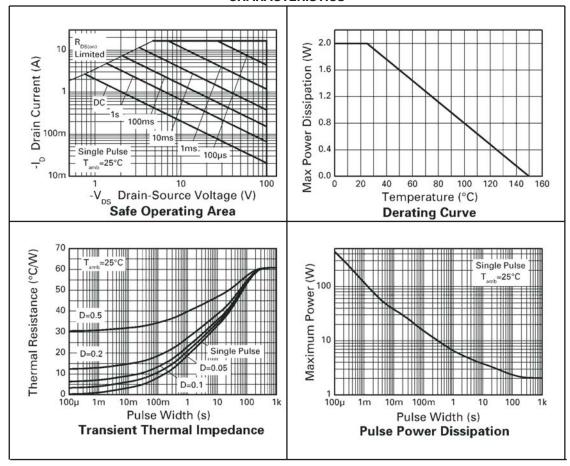
(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.



⁽b) For a device surface mounted on FR4 PCB measured at t \leq 10 sec.

⁽c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02 pulse width = $300 \mu s$ - pulse width limited by maximum junction temperature.

CHARACTERISTICS



ZETEX

ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}C$ unless otherwise stated)

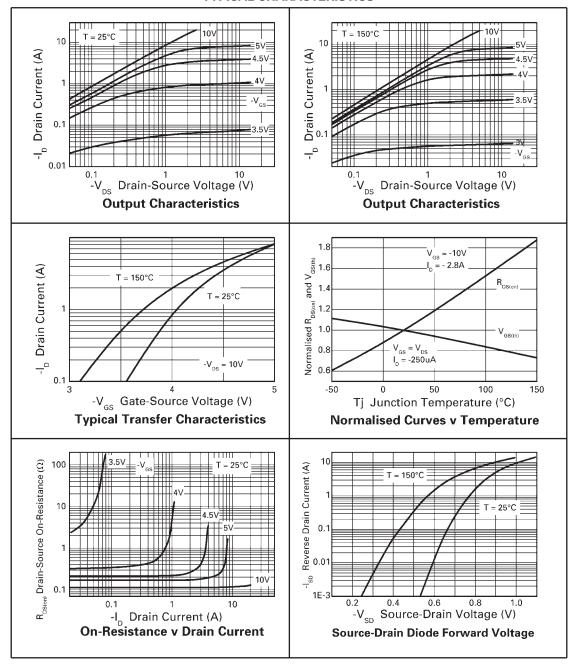
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC		•					
Drain-Source Breakdown Voltage	V _{(BR)DSS}	-100			V	I _D = -250μA, V _{GS} =0V	
Zero Gate Voltage Drain Current	I _{DSS}			-1	μΑ	V _{DS} = -100V, V _{GS} =0V	
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V	
Gate-Source Threshold Voltage	V _{GS(th)}	-2.0		-4.0	V	I _D = -250μA, V _{DS} =V _{GS}	
Static Drain-Source On-State	R _{DS(on)}			0.150	Ω	V _{GS} = -10V, I _D = -2.8A	
Resistance (1)				0.190	Ω	$V_{GS} = -6V, I_{D} = -2.4A$	
Forward Transconductance (1)(3)	g _{fs}		6.0		S	V _{DS} = -15V, I _D = -2.8A	
DYNAMIC (3)	·	•					
Input Capacitance	C _{iss}		1055		pF		
Output Capacitance	C _{oss}		90		pF	V _{DS} = -50V, V _{GS} =0V	
Reverse Transfer Capacitance	C _{rss}		76		pF	f=1MHz	
SWITCHING (2) (3)		•			•		
Turn-On Delay Time	t _{d(on)}		4.6		ns		
Rise Time	t _r		6.8		ns	V _{DD} = -50V, I _D = -1A	
Turn-Off Delay Time	t _{d(off)}		33.9		ns	$R_{G} = 6.0\Omega, V_{GS} = -10V$	
Fall Time	t _f		17.9		ns	1.0 2.2.2, 1.03	
Total Gate Charge	Qg		26.9		nC	V _{DS} = -50V, V _{GS} = -10V	
Gate-Source Charge	Q _{gs}		3.9		nC	$I_{DS} = -30V, V_{GS} = -10V$	
Gate-Drain Charge	Q _{gd}		10.2		nC	102.07	
SOURCE-DRAIN DIODE	, -	•			•	,	
Diode Forward Voltage (1)	V _{SD}		-0.85	-0.95	V	T _j =25°C, I _S = -3.5A,	
						V _{GS} =0V	
Reverse Recovery Time (3)	t _{rr}		49		ns	T _j =25°C, I _S = -2.8A,	
Reverse Recovery Charge (3)	Q _{rr}		107		nC	di/dt=100A/μs	

NOTES

- (1) Measured under pulsed conditions. Pulse width \leq 300ms; duty cycle \leq 2%.
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

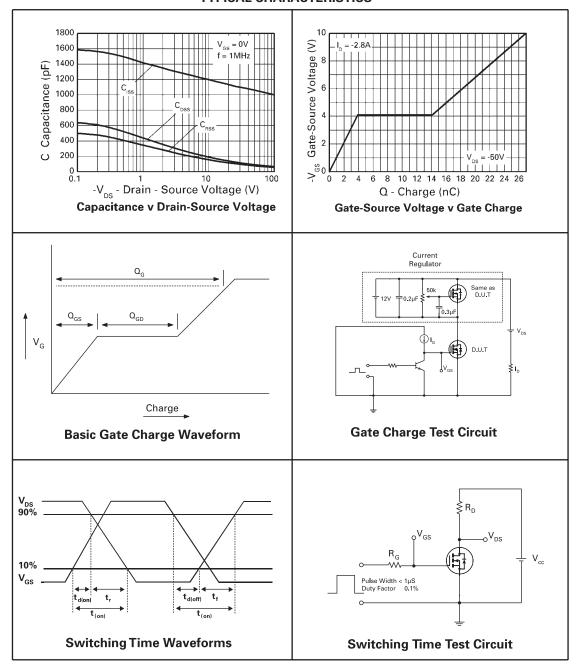


TYPICAL CHARACTERISTICS





TYPICAL CHARACTERISTICS

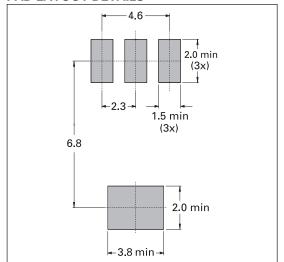




PACKAGE OUTLINE

A C C E K K G G G

PAD LAYOUT DETAILS



Controlling dimensions are in millimetres. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	Millin	netres	Inc	hes	DIM	Millimetres		Inches	
DIIVI	Min	Max	Min	Max	ואווט	Min	Max	Min	Max
А	6.3	6.7	0.248	0.264	G	NON	1 4.6	NOM	0.181
В	3.3	3.7	0.130	0.146	Н	0.85	1.05	0.033	0.041
С	-	1.7	-	0.067	K	0.02	0.10	0.0008	0.004
D	0.6	0.8	0.024	0.031	L	6.7	7.3	0.264	0.287
Е	2.9	3.1	0.114	0.122	М	NON	1 2.3	NOM	0.0905
F	0.24	0.32	0.009	0.13					

© Zetex Semiconductors plc 2005

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH	Zetex Inc	Zetex (Asia) Ltd	Zetex Semiconductors plc
Streitfeldstraße 19	700 Veterans Memorial Hwy	3701-04 Metroplaza Tower 1	Zetex Technology Park, Chadderton
D-81673 München	Hauppauge, NY 11788	Hing Fong Road, Kwai Fong	Oldham, OL9 9LL
Germany	USA	Hong Kong	United Kingdom
Telefon: (49) 89 45 49 49 0	Telephone: (1) 631 360 2222	Telephone: (852) 26100 611	Telephone (44) 161 622 4444
Fax: (49) 89 45 49 49 49	Fax: (1) 631 360 8222	Fax: (852) 24250 494	Fax: (44) 161 622 4446
europe.sales@zetex.com	<u>usa.sales@zetex.com</u>	<u>asia.sales@zetex.com</u>	hq@zetex.com

These offices are supported by agents and distributors in major countries world-wide.

This publication is issued to provide outline information only which (unless agreed by the Company in writing) may not be used, applied or reproduced for any purpose or form part of any order or contract or be regarded as a representation relating to the products or services concerned. The Company reserves the right to alter without notice the specification, design, price or conditions of supply of any product or service.

For the latest product information, log on to $\underline{www.zetex.com}$

