

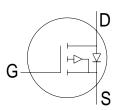
IRF9240 IRFN9240 IRF9240SMD

MECHANICAL DATA

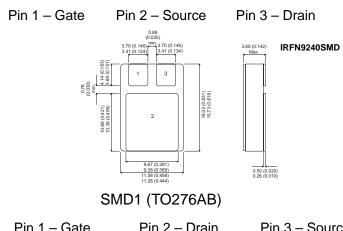
Dimensions in mm (inches)

IRF9240

P-CHANNEL POWER MOSFET



TO-3 Package (TO-204AA)



Pin 1 – Gate Pin 2 - Drain Pin 3 - Source

FEATURES

- P-CHANNEL POWER MOSFET
- HIGH VOLTAGE
- INTEGRAL PROTECTION DIODE
- AVAILABLE IN TO-3 (TO-204AA) AND **CERAMIC SURFACE MOUNT SMD1** (TO276AB)PACKAGE

Note: IRF9240SMD also available with pins 1 and 3 reversed on SMD 1 package.

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

$\overline{V_{DSS}}$	Drain – Source Voltage		-200V
V_{DGR}	Drain – Gate Voltage ($R_{GS} = 20K\Omega$)		–200V
V_{GS}	Gate – Source Voltage		±20V
I _D	Continuous Drain Current	$@ T_{case} = 25^{\circ}C$	–11A
		@ $T_{case} = 100^{\circ}C$	–7.0A
I_{DM}	Pulsed Drain Current		–44A
P_{D}	Max. Power Dissipation	$@ T_{case} = 25^{\circ}C$	125W
	Linear Derating Factor		1W / °C
T _i	Operating Junction and		FF to 150°C
T _{stg}	Storage Temperature Range		_55 to 150°C

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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Issue 2

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IRF9240 IRFN9240 IRF9240SMD

ELECTRICAL RATINGS (T_{case} = 25°C unless otherwise stated)

	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain – Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = -1mA$	-200			V
V _{GS(TH)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	-2		-4	V
	Gate – Source Leakage Current (forward)	V _{GS} = −20V			-100	nA
I_{GSS}	Gate – Source Leakage Current (reverse)	V _{GS} = 20V			100	nA
	Zero Gate Voltage Drain Current	V _{DS} = -160V , V _{GS} = 0V			-25	μА
I_{DSS}		V _{DS} = -160V			-1	mA
		V _{GS} = 0V , T _{case} = 125°C				
ı	On State Drain Current ¹	$V_{DS} > I_{D(ON)} \times R_{DS(ON)} Max$	-11			Α
I _{D(ON)}		V _{GS} = −10V	-''			_ ^
R _{DS(ON)}	Static Drain – Source On-State Resistance	$V_{GS} = -10V , I_{D} = -7A$		0.35	0.5	Ω
α.	Forward Transconductance ¹	$V_{DS} > I_{D(ON)} \times R_{DS(ON)} Max$	4	6		s
g_{fs}		$I_D = -7A$		0		3
C _{iss}	Input capacitance	V _{GS} = 0V		1200		
C _{oss}	Output capacitance	V _{DS} = −25V		570		pF
C _{rss}	Reverse transfer capacitance	f = 1MHz		81		
Qg	Total Gate Charge	V _{GS} = −10V	28		60	
Q _{gs}	Gate – Source Charge	I _D = -11A	3.0		15	nC
Q _{gd}	Gate - Drain ("Miller") Charge	V _{DS} = -100V	4.5		38	
t _{d(on)}	Turn-on Delay Time	V 100V			35	
t _r	Rise Time	V _{DD} = -100V			85	
t _{d(off)}	Turn-off Delay Time	$I_D = -11A$			85	ns
t _f	Fall Time	$Z_{O} = 9.1\Omega$			65	
L _D	Internal Drain Inductance			5.0		nH
L _S	Internal Source Inductance			12.5		nH

THERMAL CHARACTERISTICS

Γ		Characteristic		Min.	Тур.	Max.	Unit
Γ	$R_{\theta JC}$	Junction to Case				1.0	°C/W
Γ	$R_{\theta JA}$	Junction to Ambient	(TO-3 package only)			30	°C/W
Γ	TL	Max. Lead Temperature 0.063" from case for 10 sec.	(TO-3 package only)		300		°C

SOURCE - DRAIN DIODE RATINGS AND CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current (Body Diode)				-11	Α
I _{SM}	Pulsed Source Current ¹ (Body Diode)				-44	
V _{SD}	Diode Forward Voltage ²	$V_{GS} = 0V$, $I_S = -11A$ $T_{case} = 25^{\circ}C$			-4.6	V
t _{rr}	Reverse Recovery Time	$I_F = -11A$, $dI_F / dt = 100A/\mu s$ $T_j = 25^{\circ}C$		270		ns
Q _{rr}	Reverse Recovery Charge	$I_F = -11A$, $dI_F / dt = 100A/\mu s$ $T_j = 25^{\circ}C$		2.0		μС

¹⁾ Pulse Test: Pulse Width < $300\mu S$, Duty Cycle $\leq 2\%$ 2) Repetitive Rating: Pulse Width limited by maximum junction temperature.

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