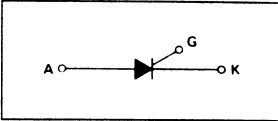


**2N6167  
 thru  
 2N6170**

**SCRs  
 20 AMPERES RMS  
 100 thru 600 VOLTS**



## Silicon Controlled Rectifier Reverse Blocking Triode Thyristor

... designed for industrial and consumer applications such as power supplies; battery chargers; temperature, motor, light and welder controls.

- Economical for a Wide Range of Uses
- High Surge Current —  $I_{TSM} = 240$  Amps
- Rugged Construction in Isolated Stud Package

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
*Peak Repetitive Forward and Reverse Blocking Voltage (1) ( $T_J = -40^\circ\text{C}$ to $+100^\circ\text{C}$ )	$V_{DRM}$ or $V_{RRM}$	100 200 400 600	Volts
*Non-Repetitive Peak Reverse Blocking Voltage ( $t \leq 5$ ms)	$V_{RSM}$	150 250 450 650	Volts
*Average Forward Current ( $T_C = -40$ to $+65^\circ\text{C}$ ) ( $+85^\circ\text{C}$ )	$I_{T(AV)}$	13 6.5	Amps
*Peak Surge Current (One cycle, 60 Hz) ( $T_C = +65^\circ\text{C}$ ) (1.5 ms pulse @ $T_J = 100^\circ\text{C}$ ) Preceded and followed by no current or Voltage	$I_{TSM}$	240 560	Amps
Circuit Fusing ( $T_J = -40$ to $+100^\circ\text{C}$ ) ( $t = 1$ to 8.3 ms)	$I^2t$	235	$\text{A}^2\text{s}$
*Peak Gate Power	$P_{GM}$	5	Watts
*Average Gate Power	$P_{G(AV)}$	0.5	Watt

\*Indicates JEDEC Registered Data.

(cont.)

(1) Ratings apply for zero or negative gate voltage. Devices shall not have a positive bias applied to the gate concurrently with a negative potential on the anode. Devices should not be tested with a constant current source for forward or reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.

